

A

A

B

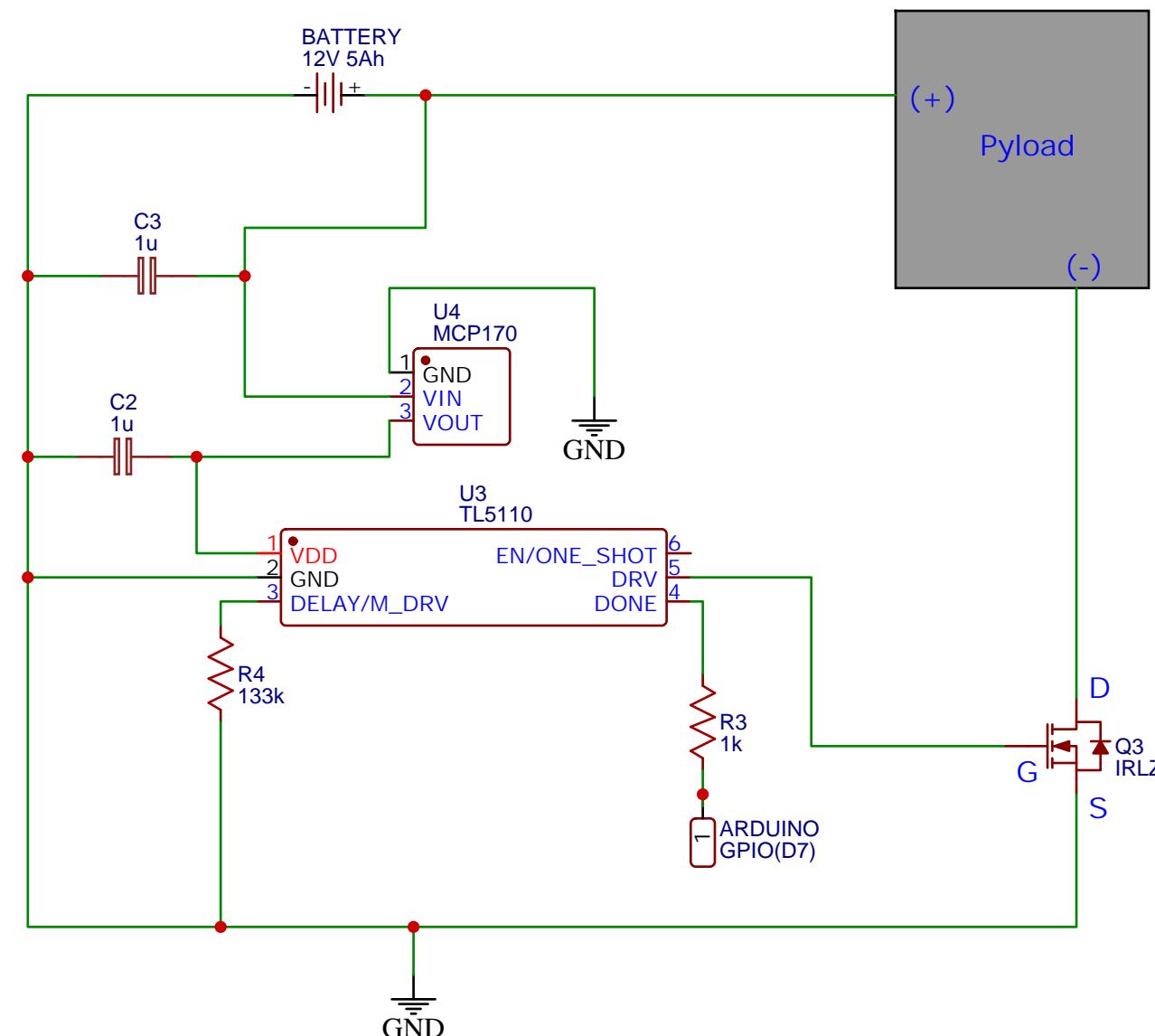
B

C

C

D

D



TITLE:

Timer TPL5110

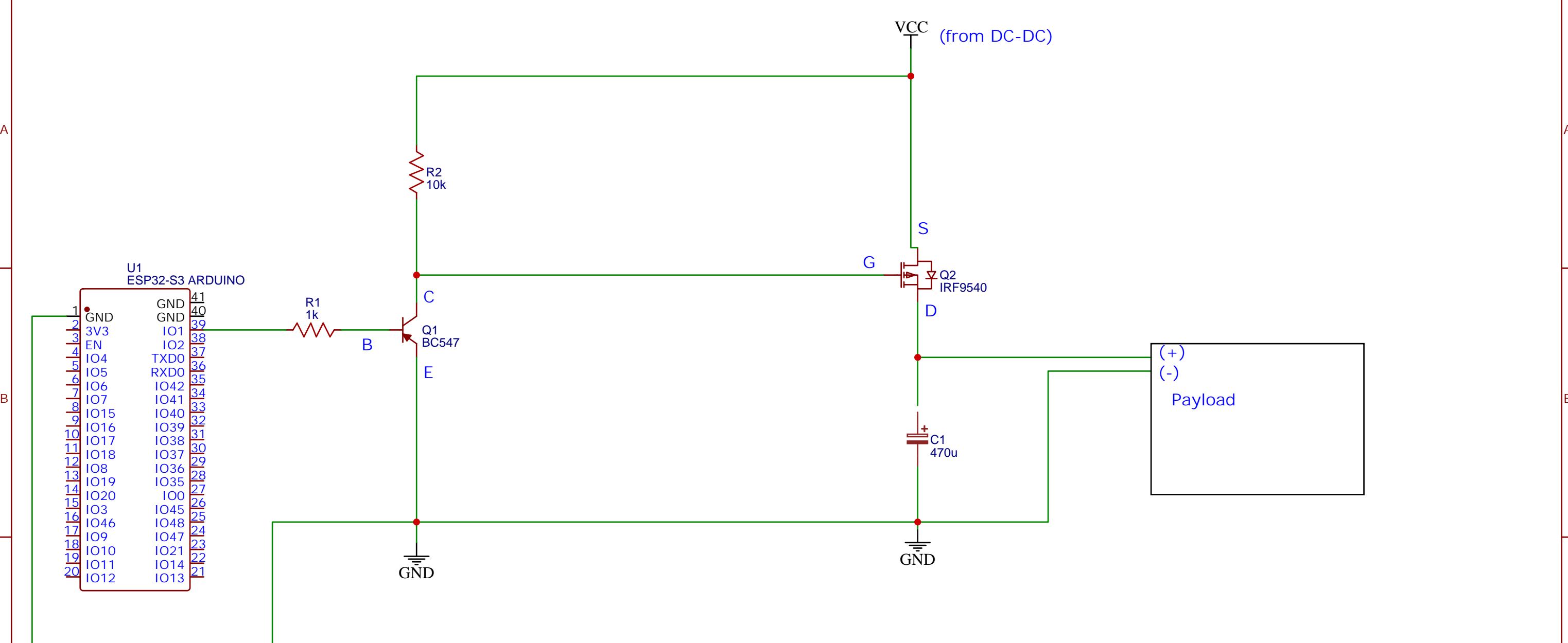
REV: 1.0

EasyEDA

Company: Orlov

Sheet: 1/1

Date: 2025-10-27 Drawn By: corlov



TITLE: Power control schema (sim/sensor)		REV: 1.0
	Company: Orlov	Sheet: 1/1
	Date: 2025-10-26	Drawn By: corlov

A

A

B

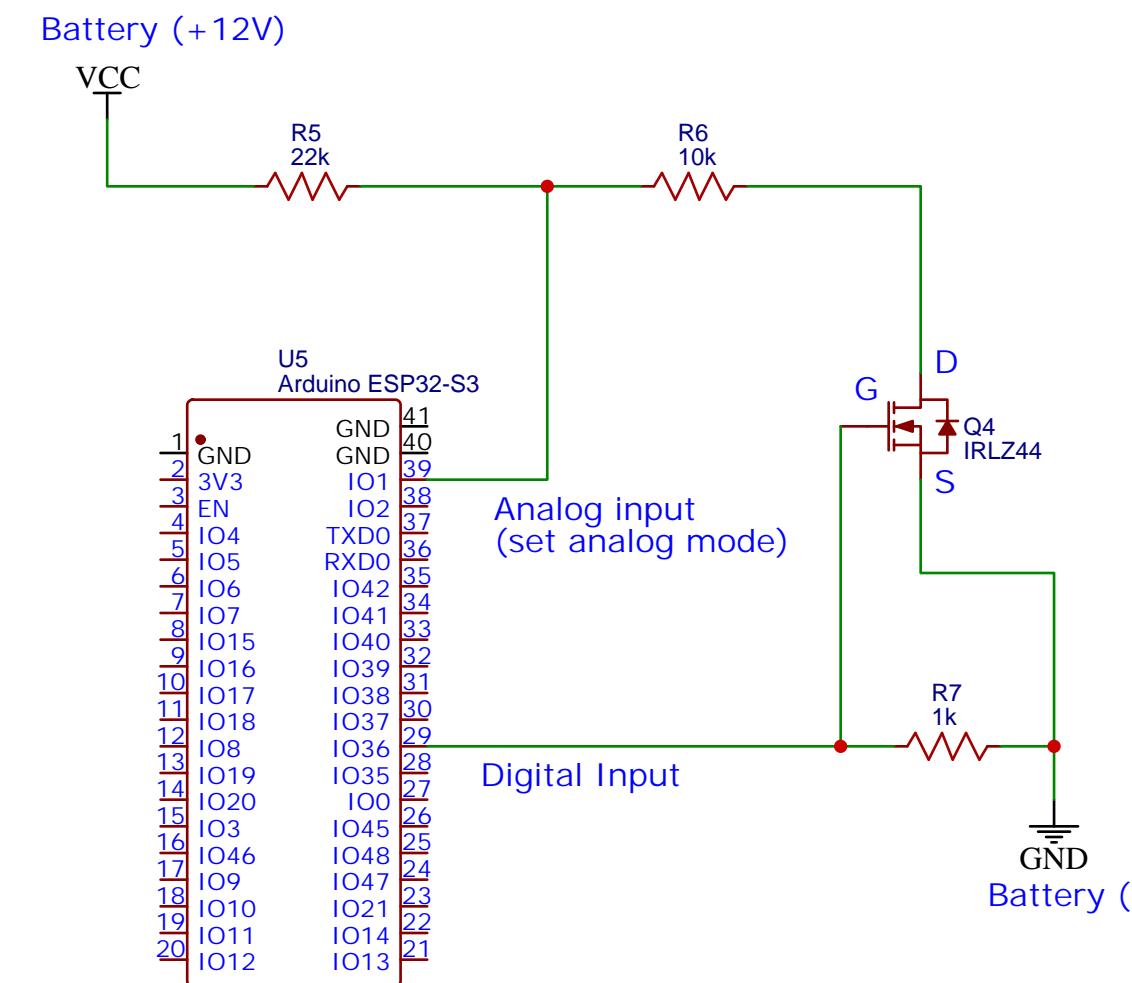
B

C

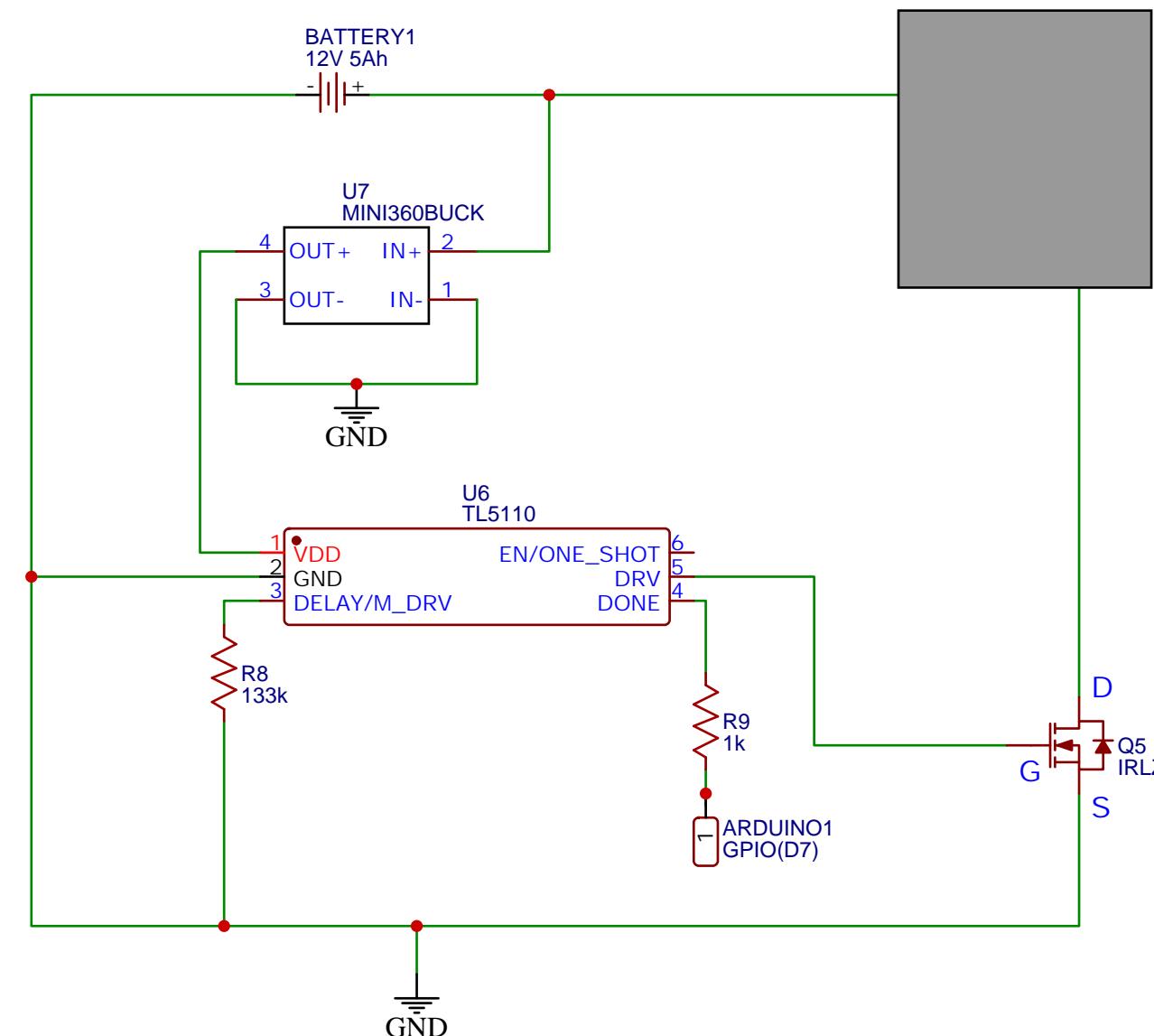
C

D

D



TITLE: Voltage delimiter (voltage measurement schema)		REV: 1.0
	Company: Orlov	Sheet: 1/1
	Date: 2025-10-27	Drawn By: corlov



TITLE:

Sheet_1

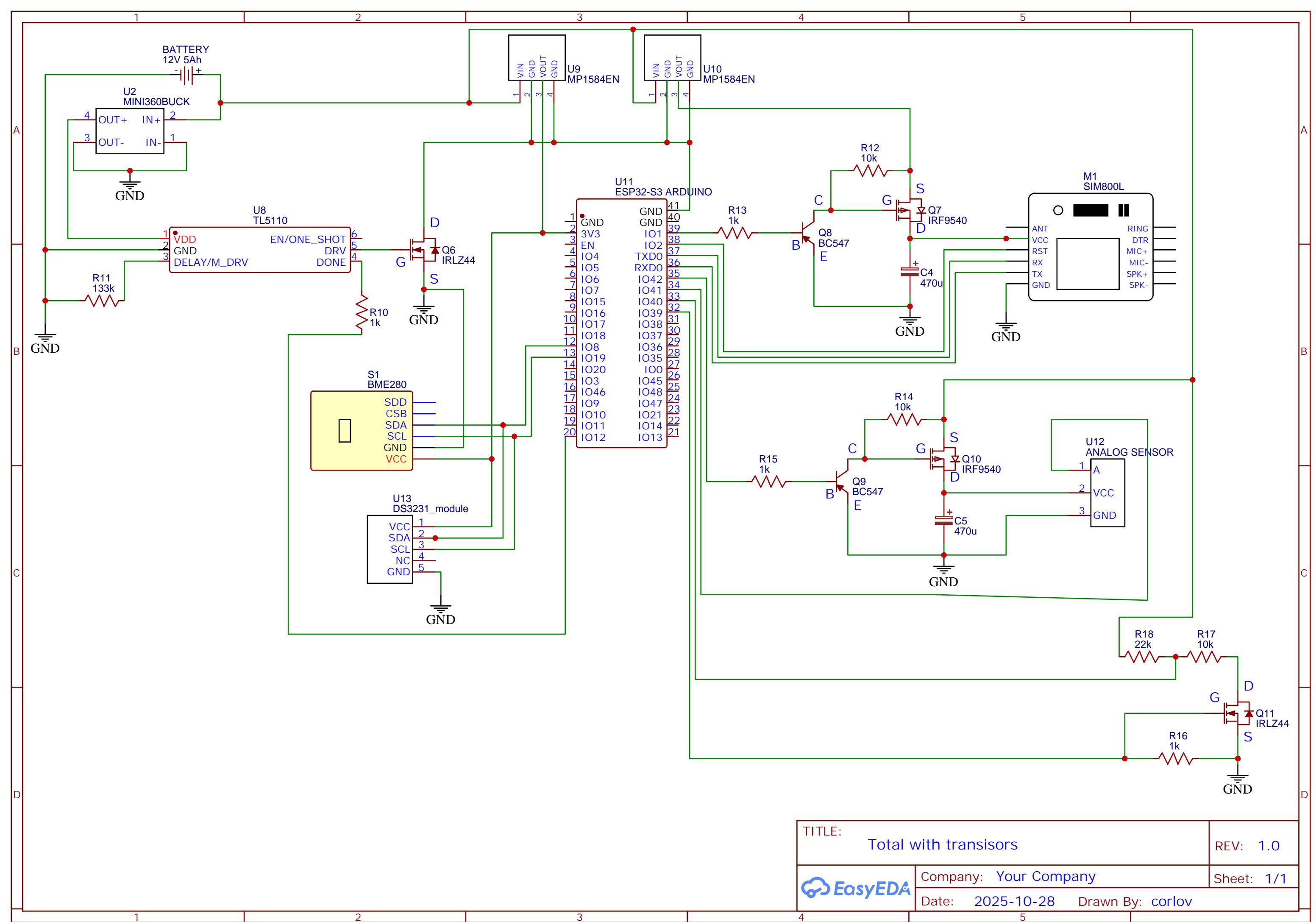
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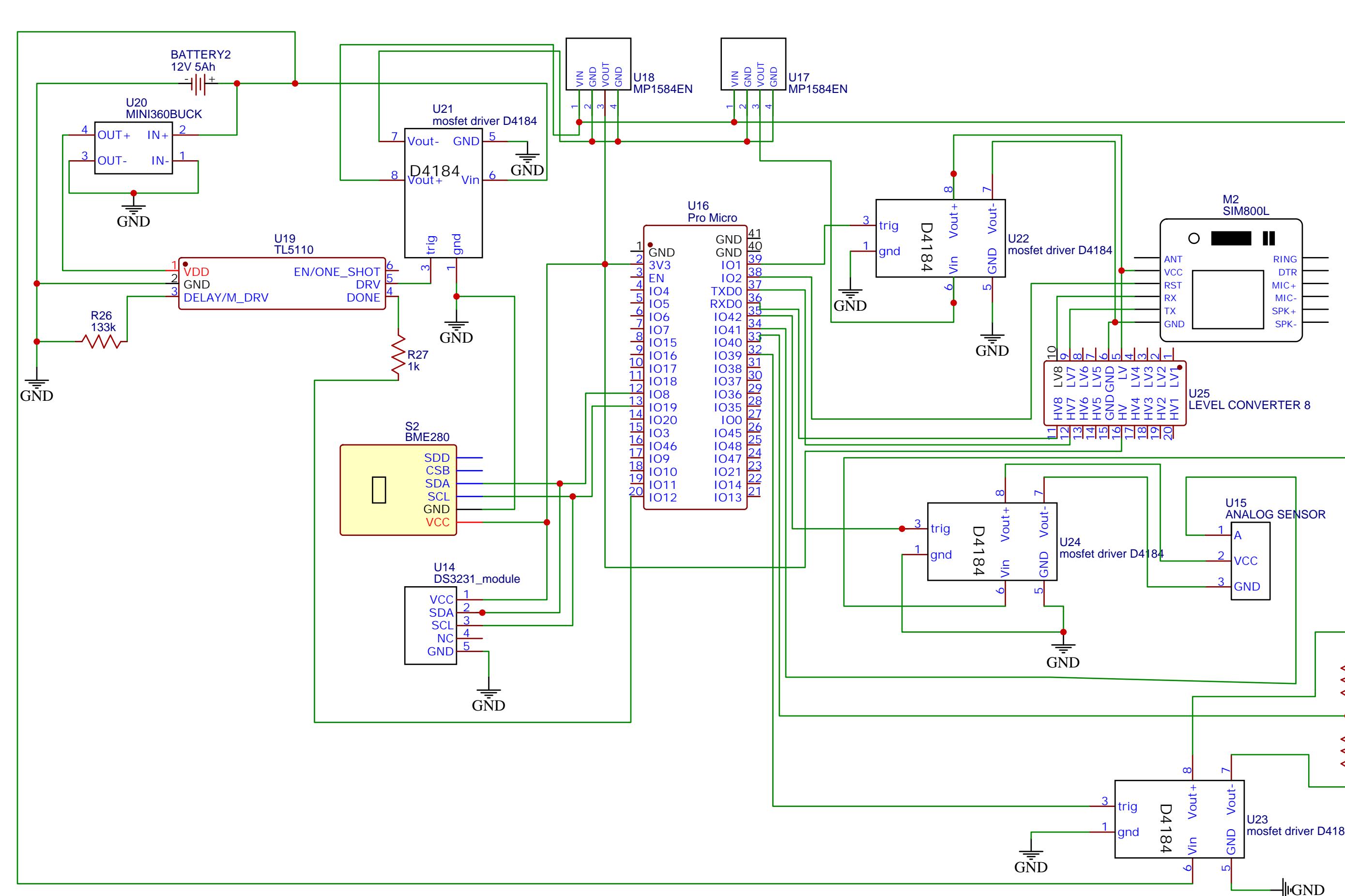


Company: Your Company

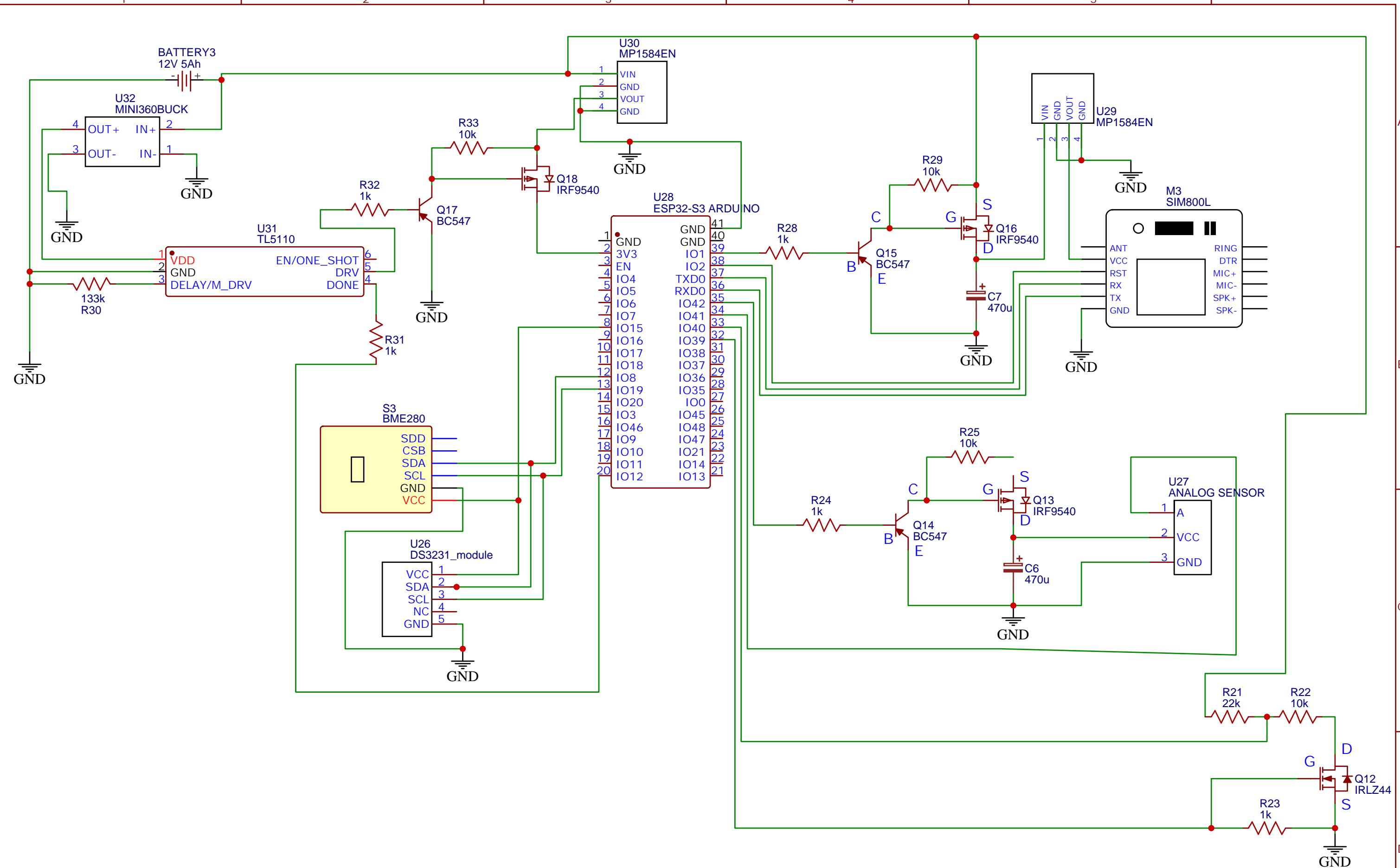
Sheet: 1/1

Date: 2025-10-28 Drawn By: corlov





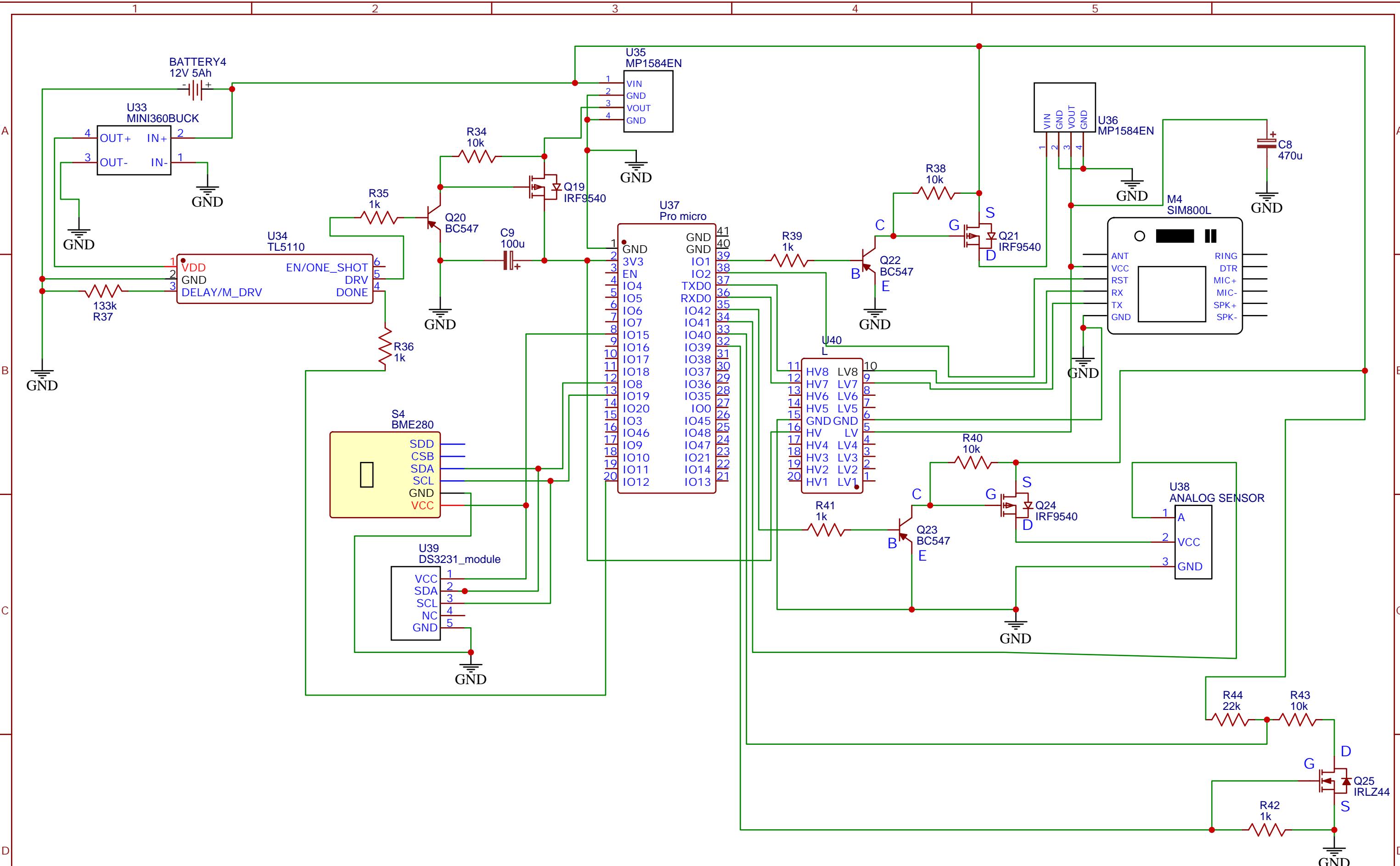
1 2 3 4 5



TITLE:

Total (high side switch) ESP32

REV: 1.0



TITLE: Total (high side switch) ProMicro		REV: 1.0
	Company: Your Company	Sheet: 1/1
	Date: 2025-10-28	Drawn By: corlov

