

A

A

B

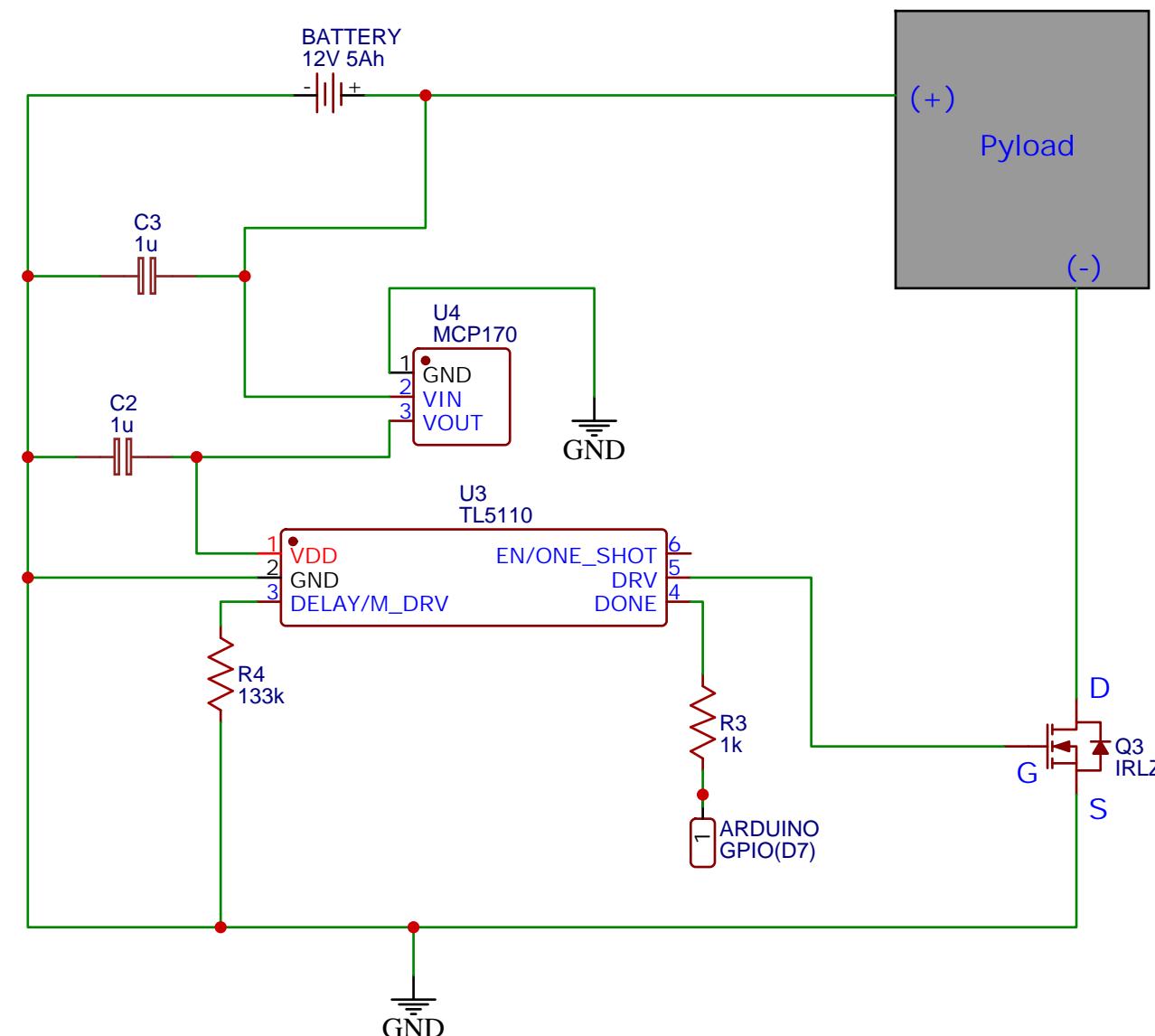
B

C

C

D

D



TITLE:

Timer TPL5110

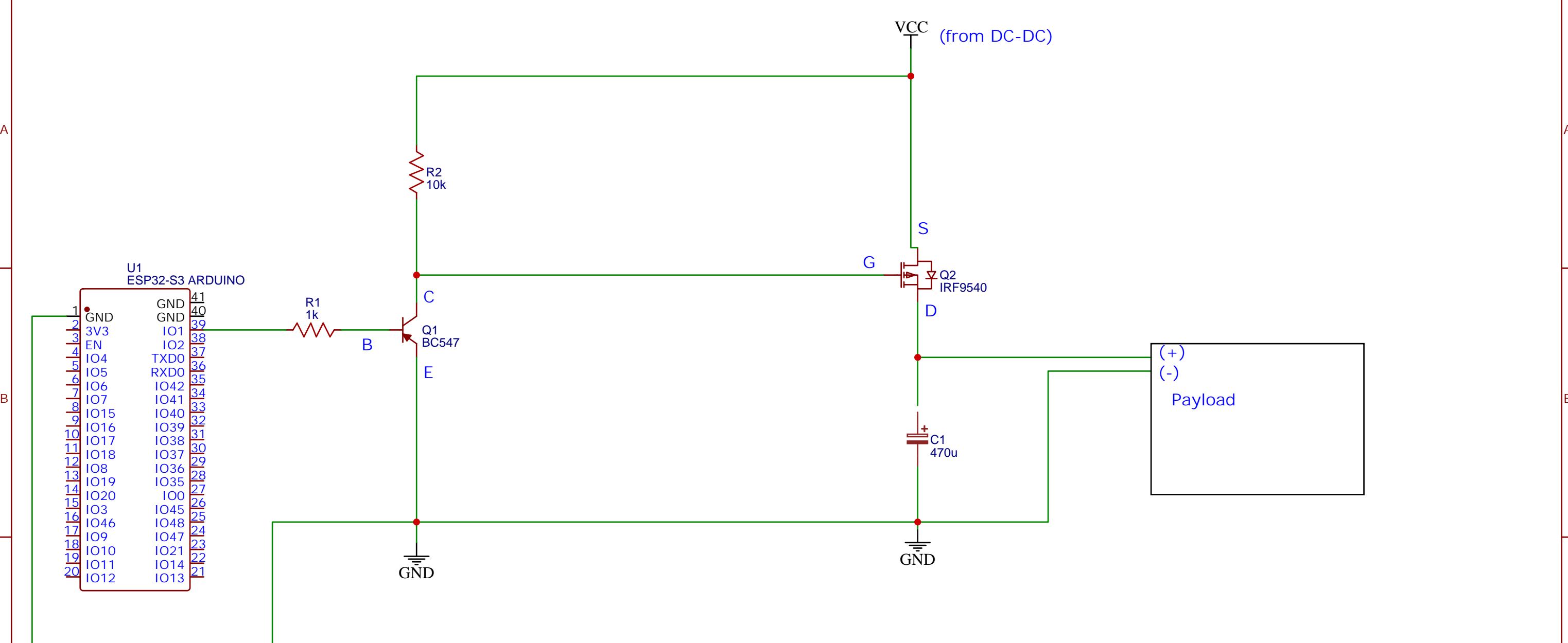
REV: 1.0

EasyEDA

Company: Orlov

Sheet: 1/1

Date: 2025-10-27 Drawn By: corlov



TITLE: Power control schema (sim/sensor)		REV: 1.0
	Company: Orlov	Sheet: 1/1
	Date: 2025-10-26	Drawn By: corlov

A

A

B

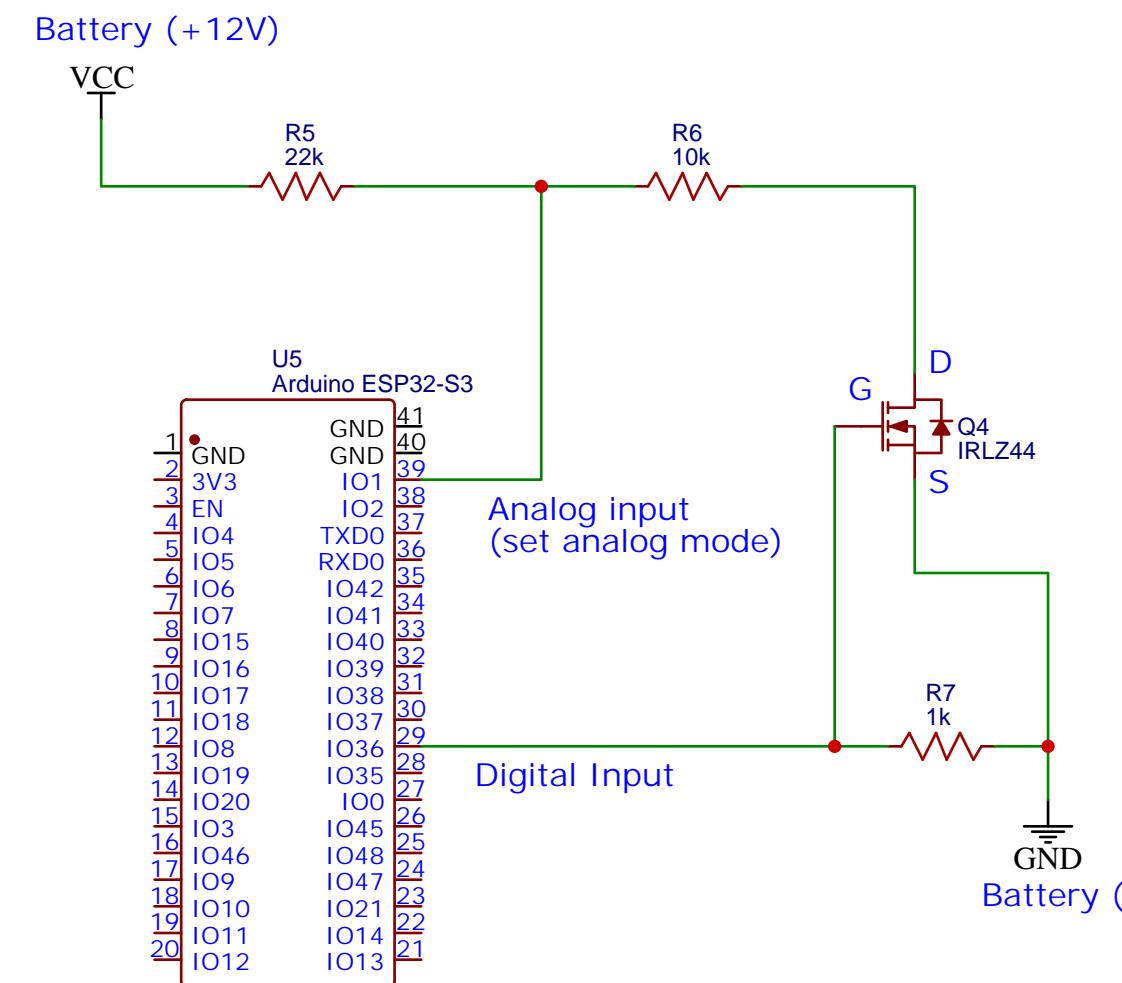
B

C

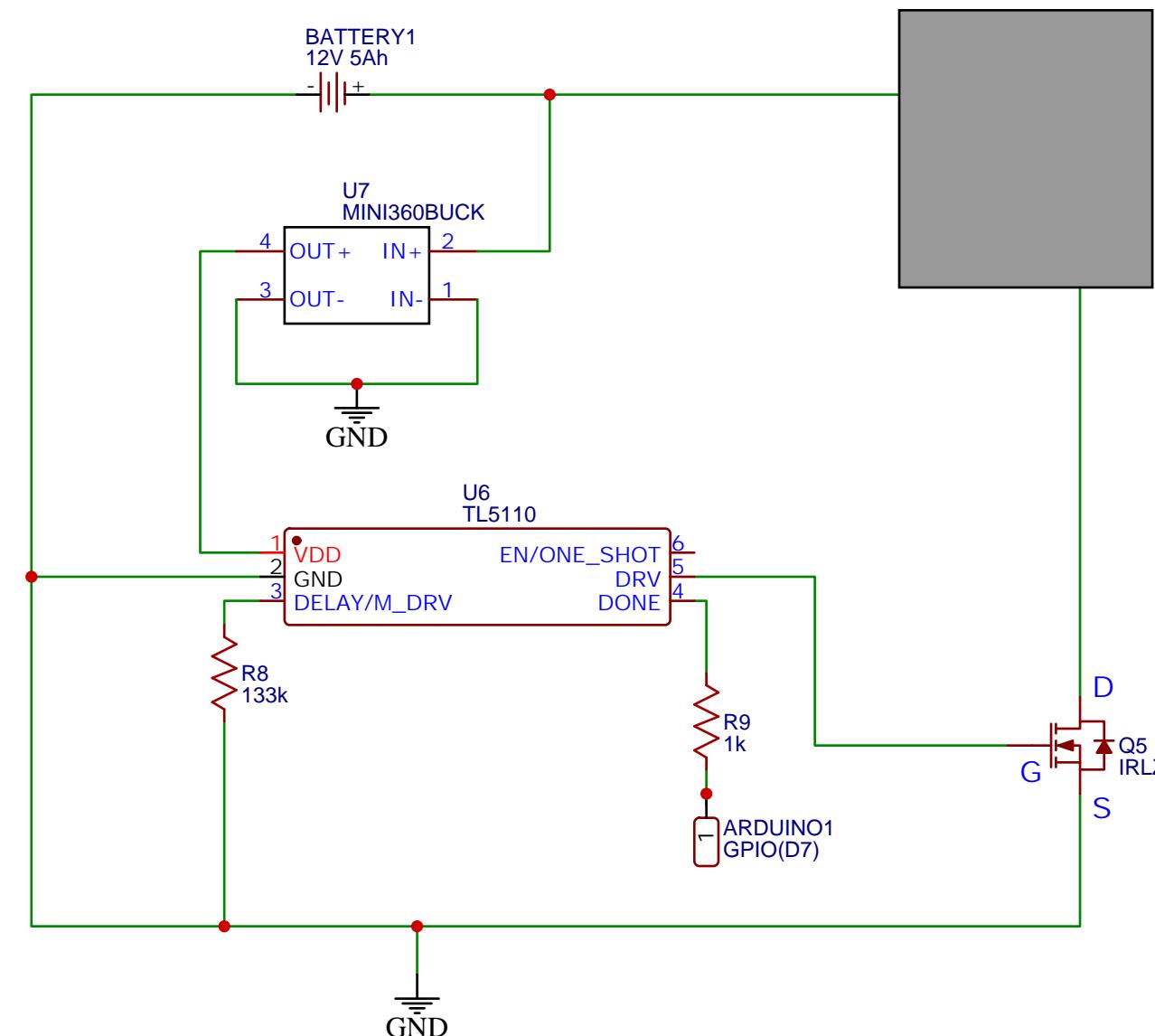
C

D

D



TITLE: Voltage delimiter (voltage measurement schema)		REV: 1.0
	Company: Orlov	Sheet: 1/1
	Date: 2025-10-27	Drawn By: corlov



TITLE:

Sheet\_1

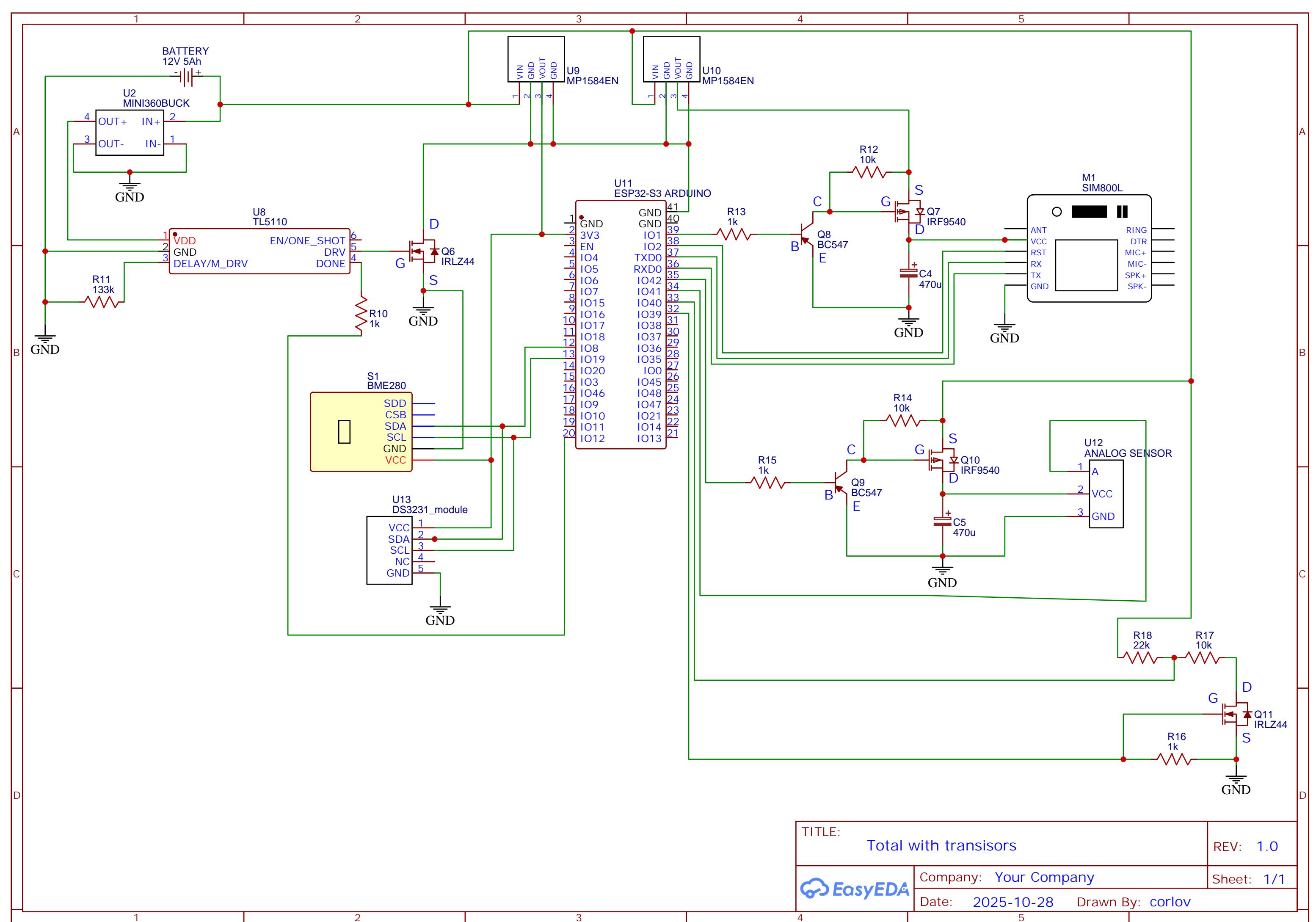
REV: 1.0



Company: Your Company

Sheet: 1/1

Date: 2025-10-28 Drawn By: corlov



TITLE:

Total with transistors

REV: 1.0

