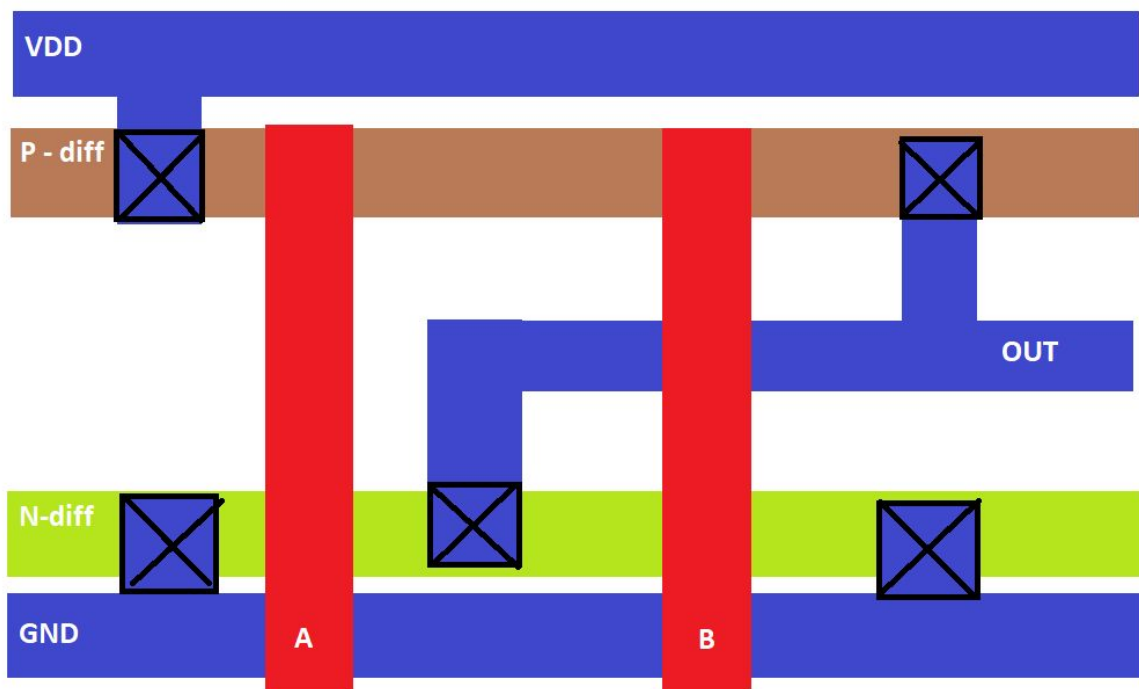
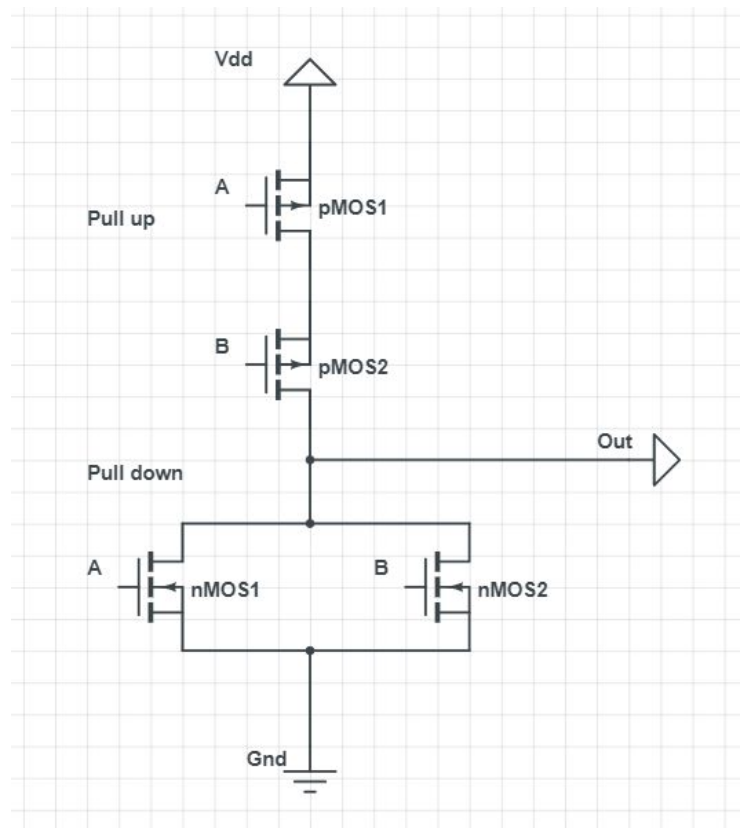
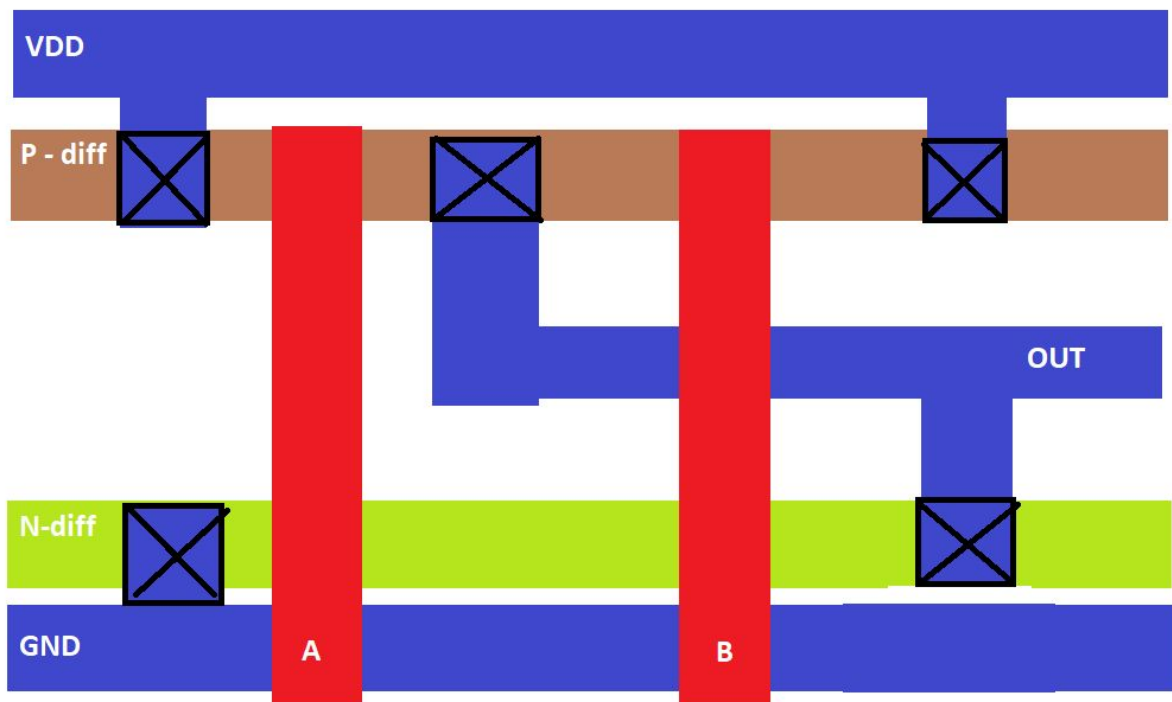
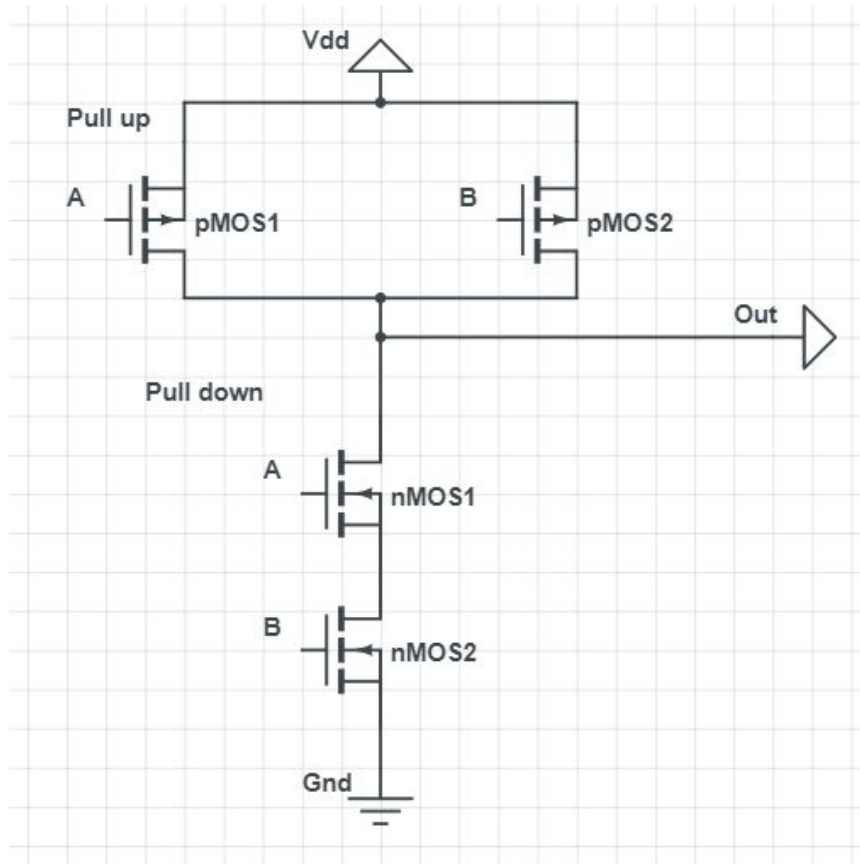


Dibuixa l'esquemàtic a nivell de transistor i el layout seguint l'esquema de cel·les estàndard.

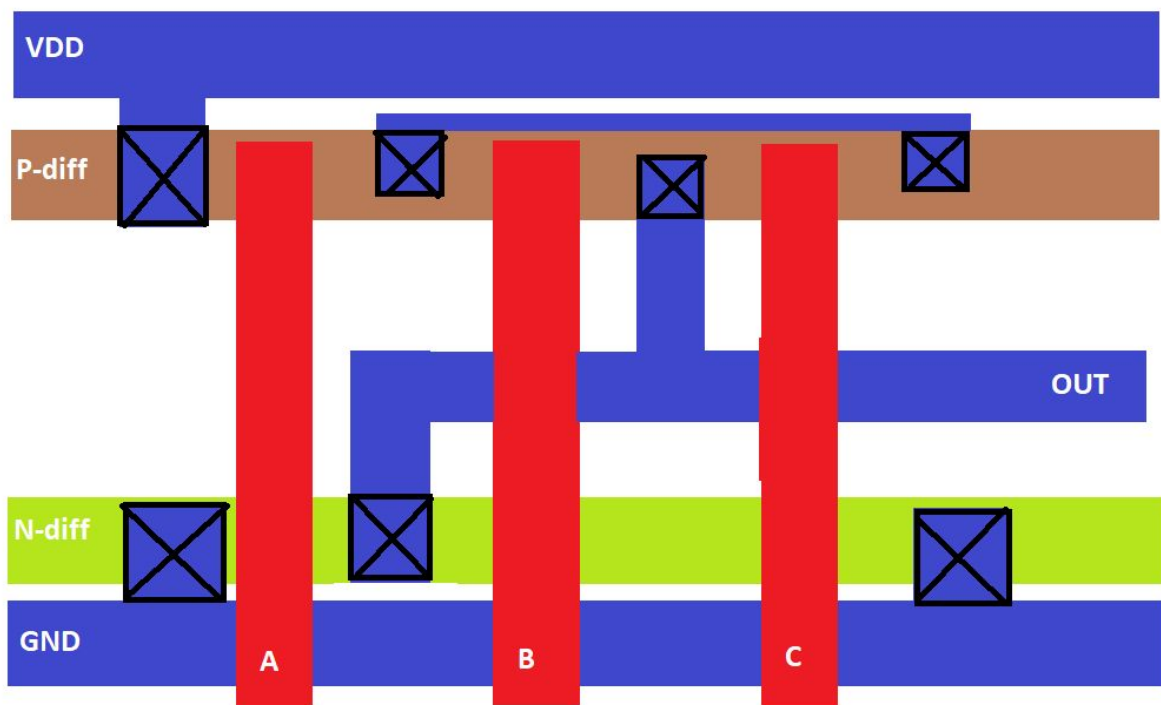
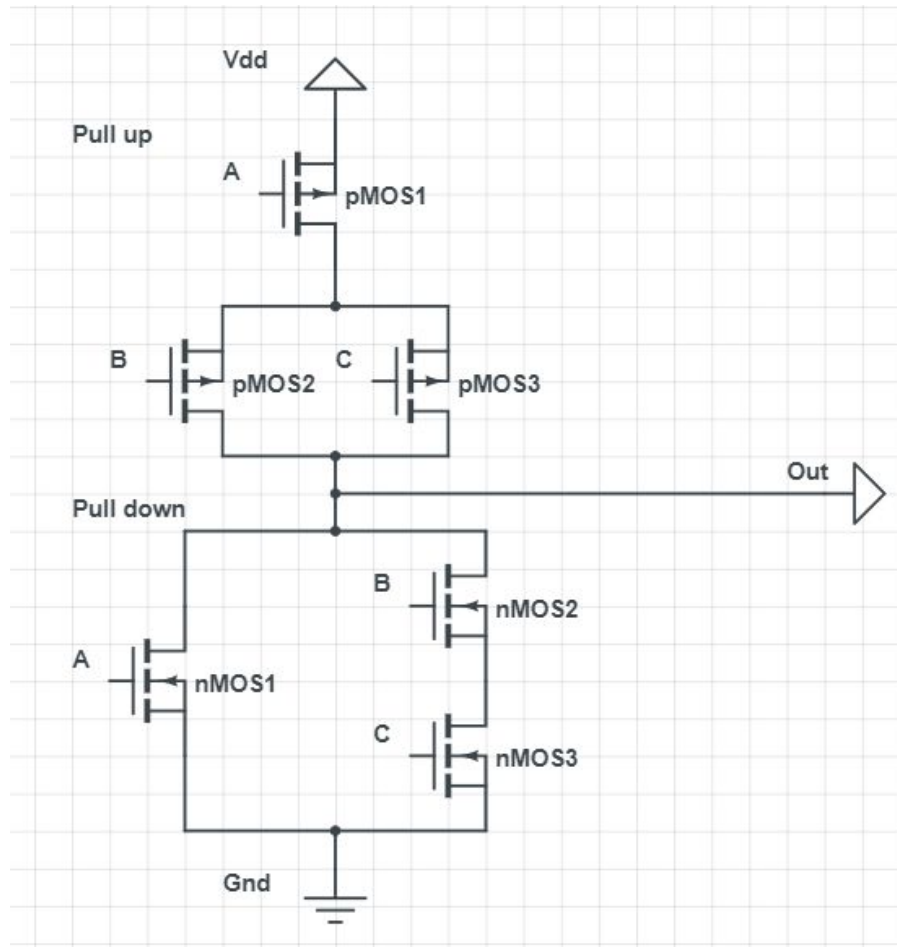
$$-F = A + B$$



$$\neg F = A \cdot B$$



$$-F = A + B \cdot C$$



$$-F = A \cdot (B + C)$$

