

File: vгатiming.kicad_sch

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File: vg_memsignal.kicad_sch

File: vga_vidmem.kicad_sch

File: ram.kicad_sch

File: cpu_rom.kicad_sch

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File: Communication_sch.kicad

File: address_decoder.kicad_sch

[illegible]

File: softswitches.kicad_sch

File: banking.kicad_sch

File: text.kicad_sch

FIGURE 1 | Diagram illustrating the study design.

File: PLL.kicad_sch

Future Work:

File: pixels.kicad_sch

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File: graphicsv2.kicad_sch

File: Power.kicad_sch

File: romdisk_sch.kicad_sch

File: Mockingboard.kicad_sch

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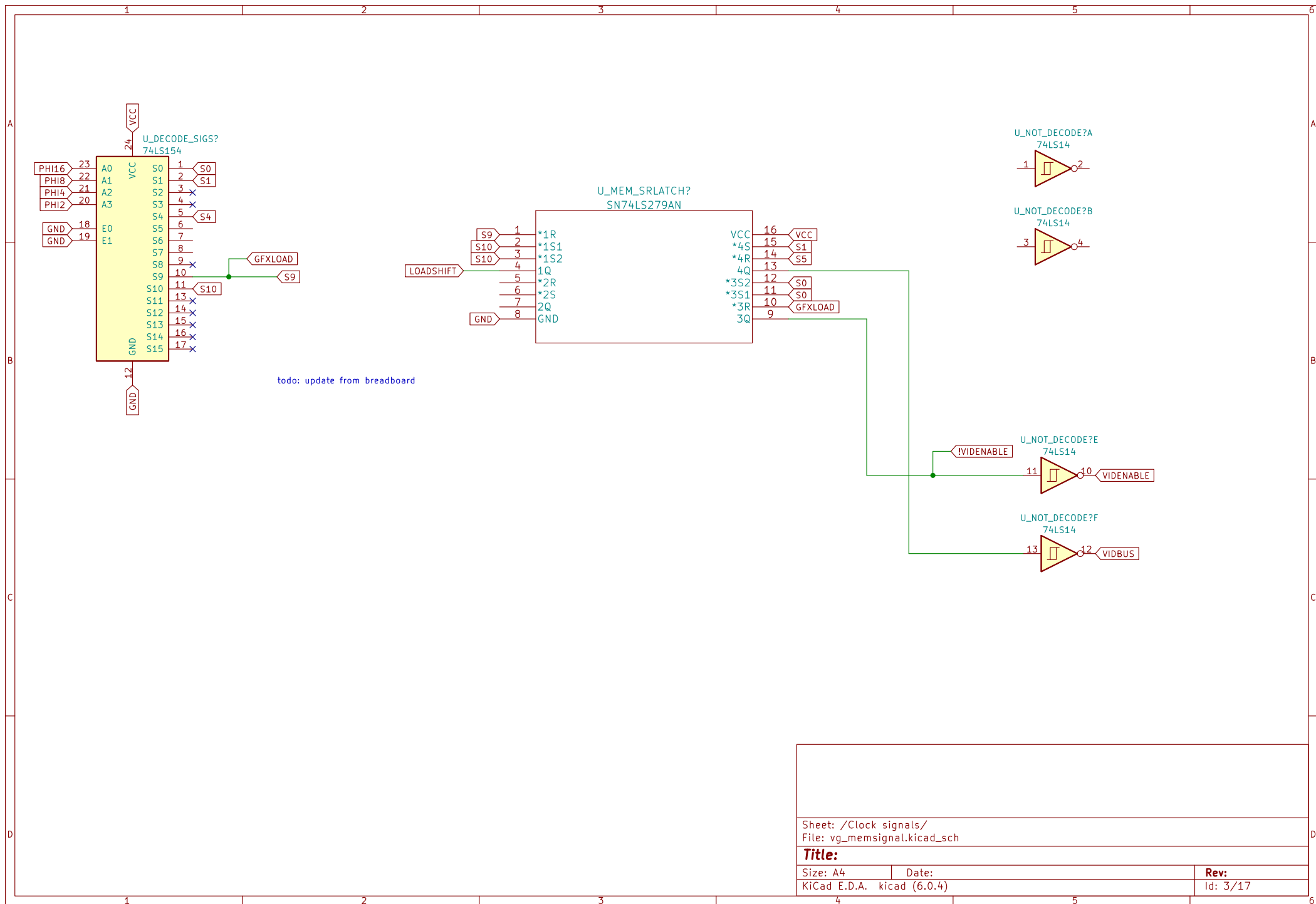
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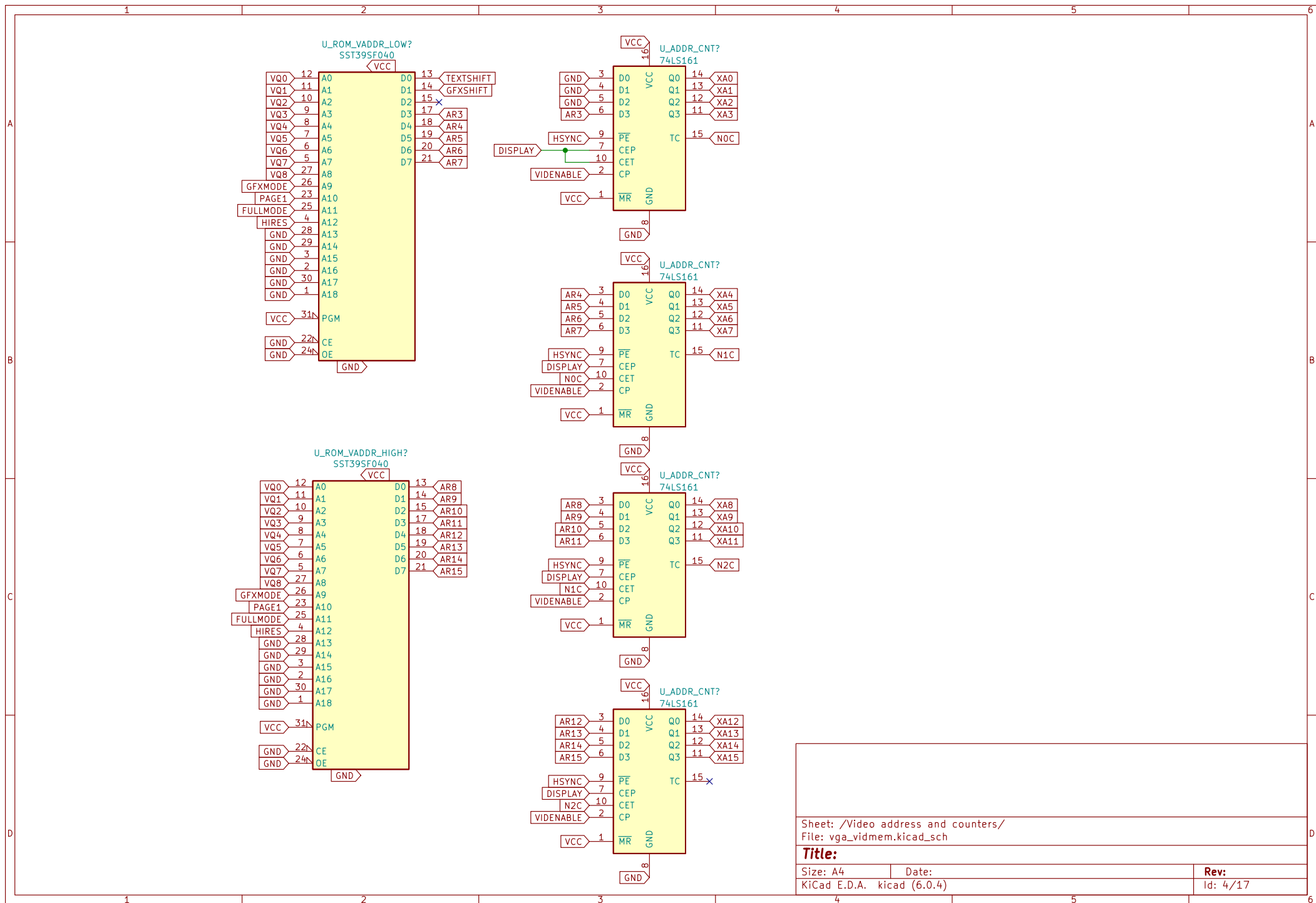


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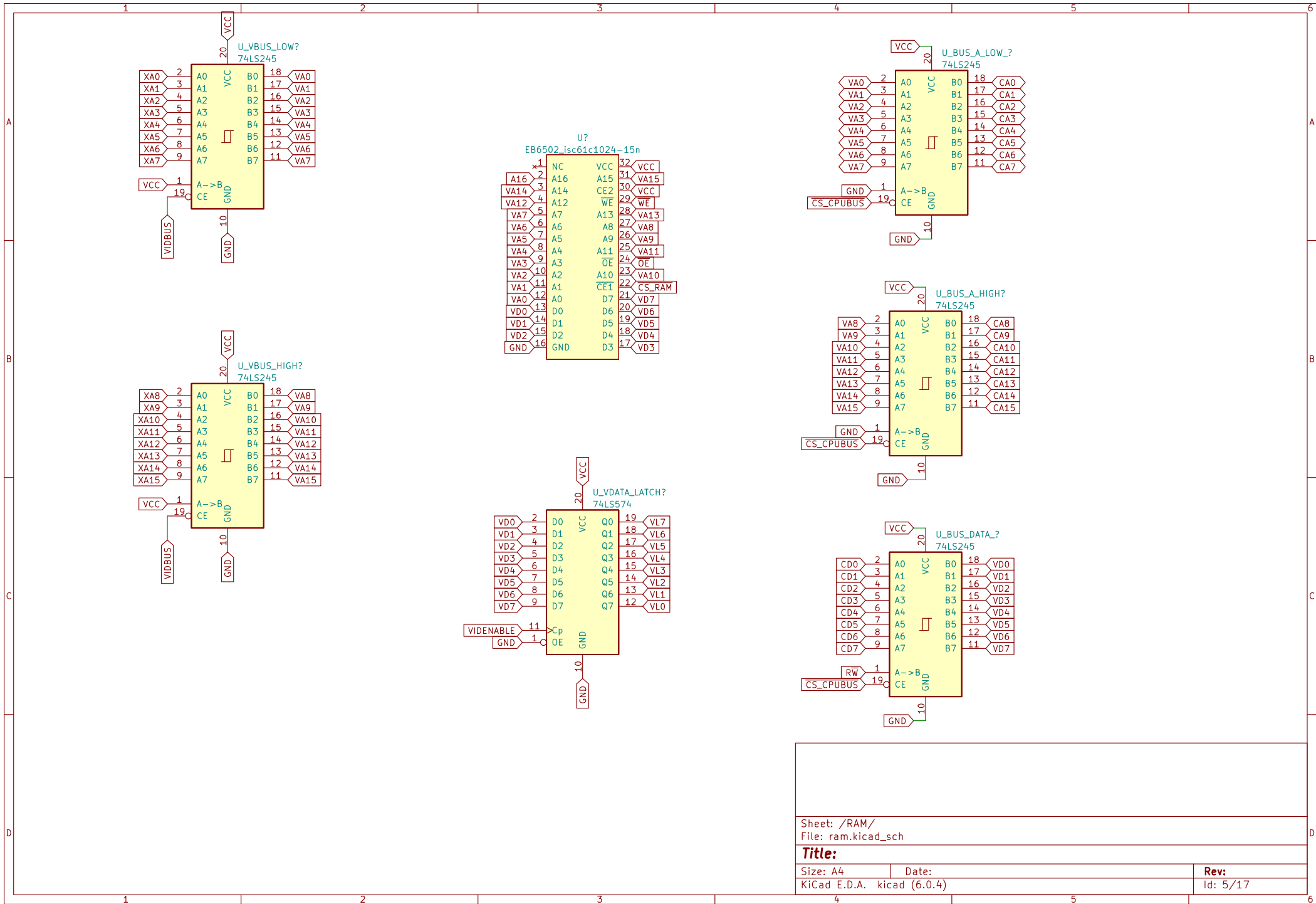


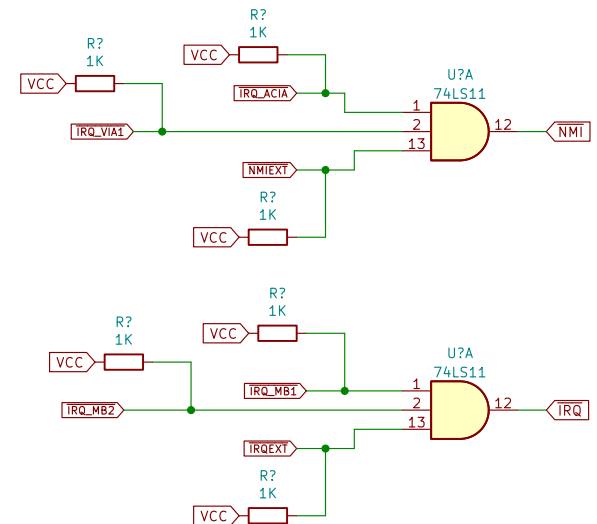


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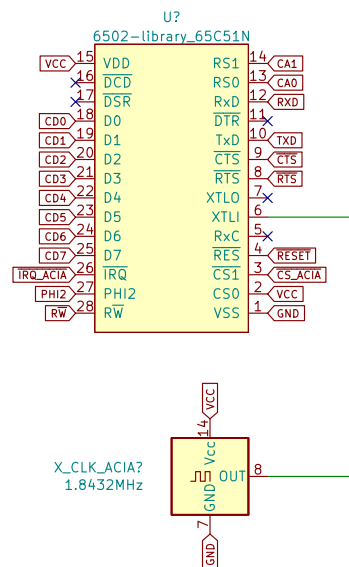
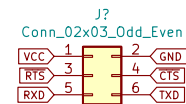
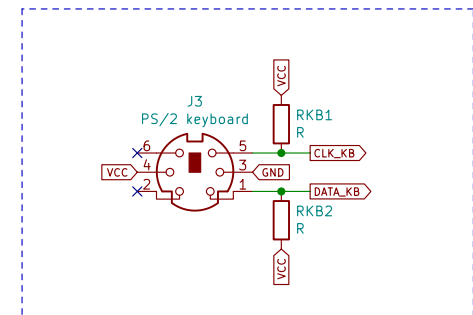
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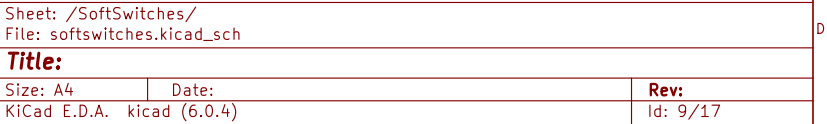


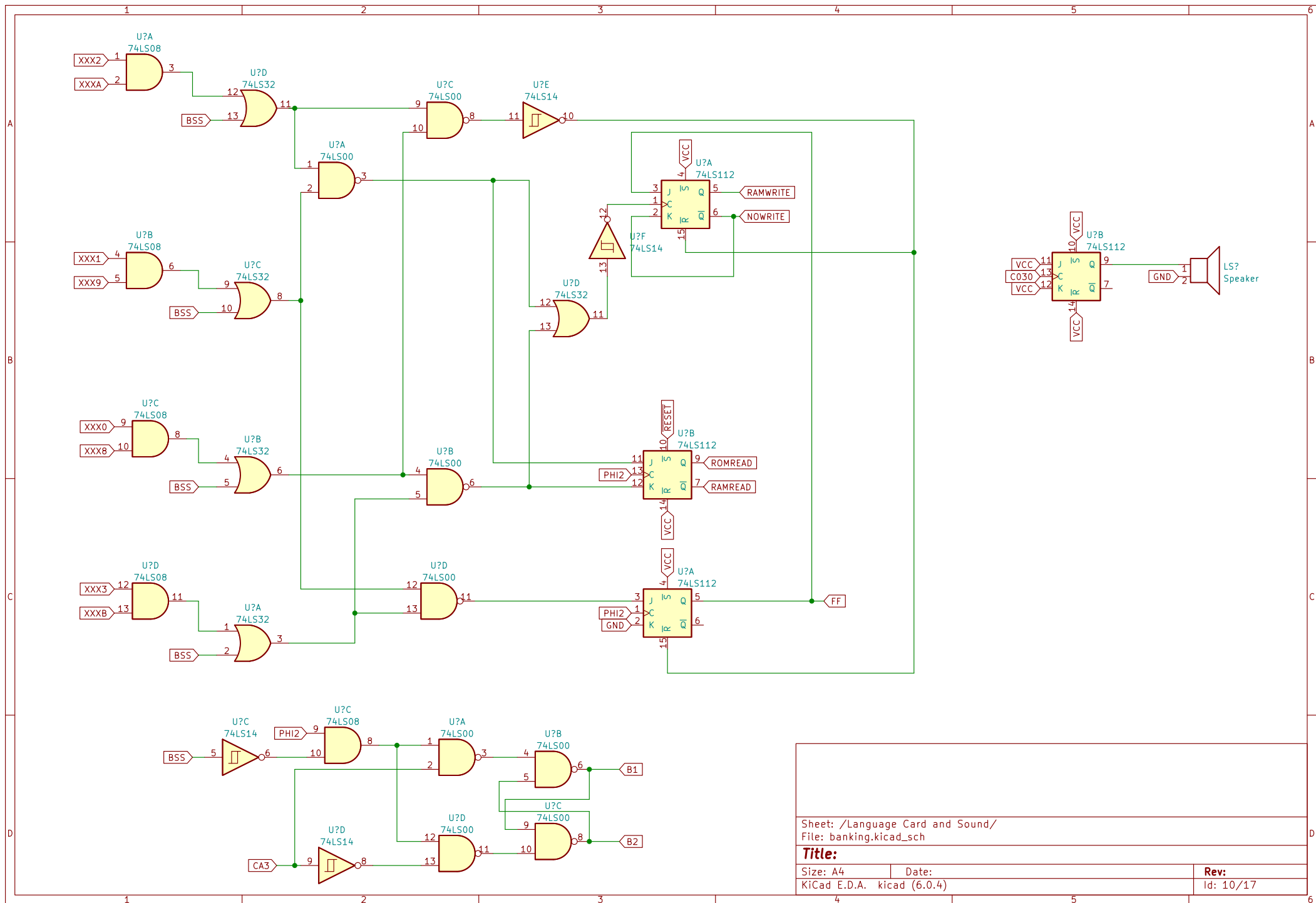


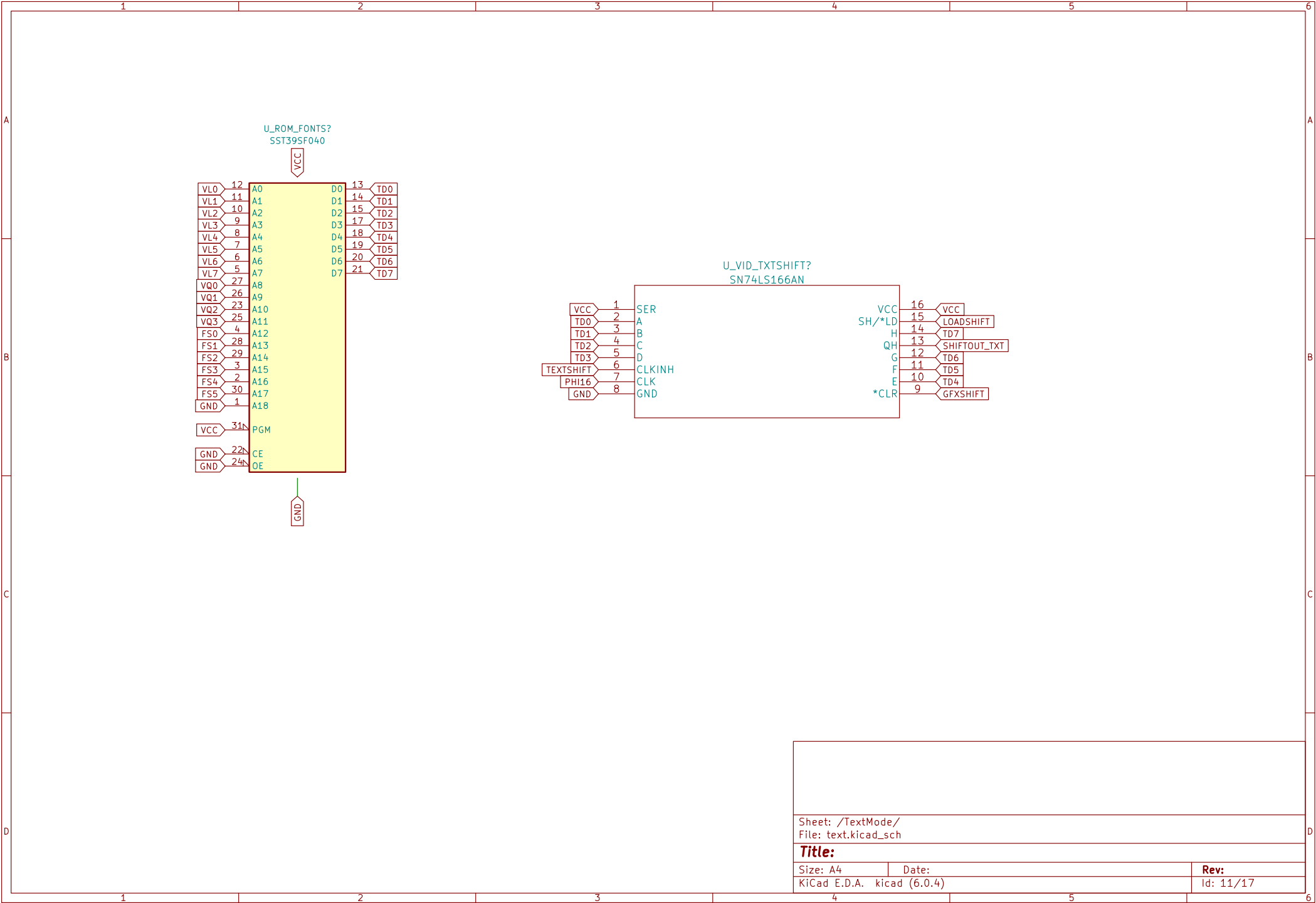
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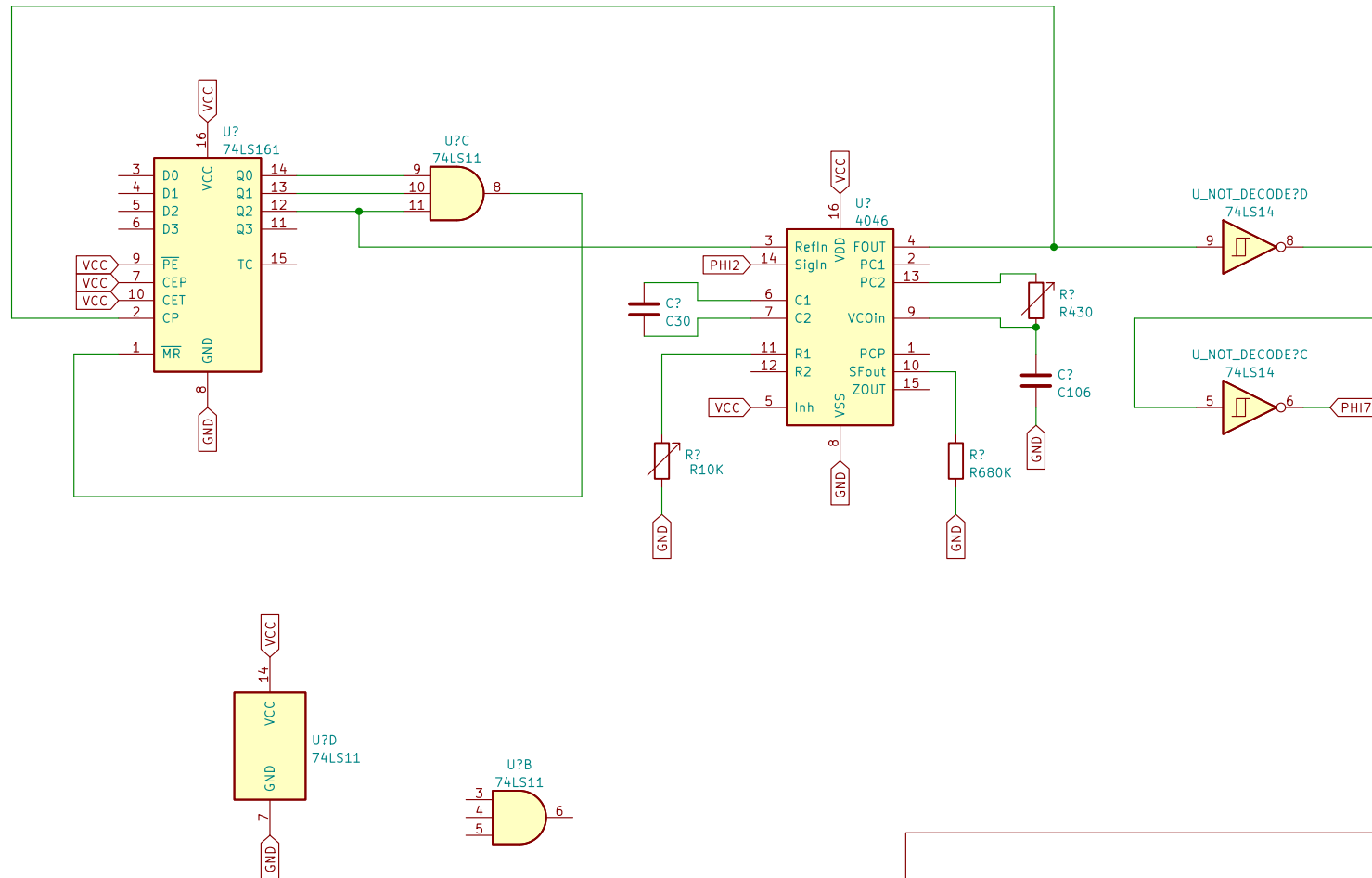


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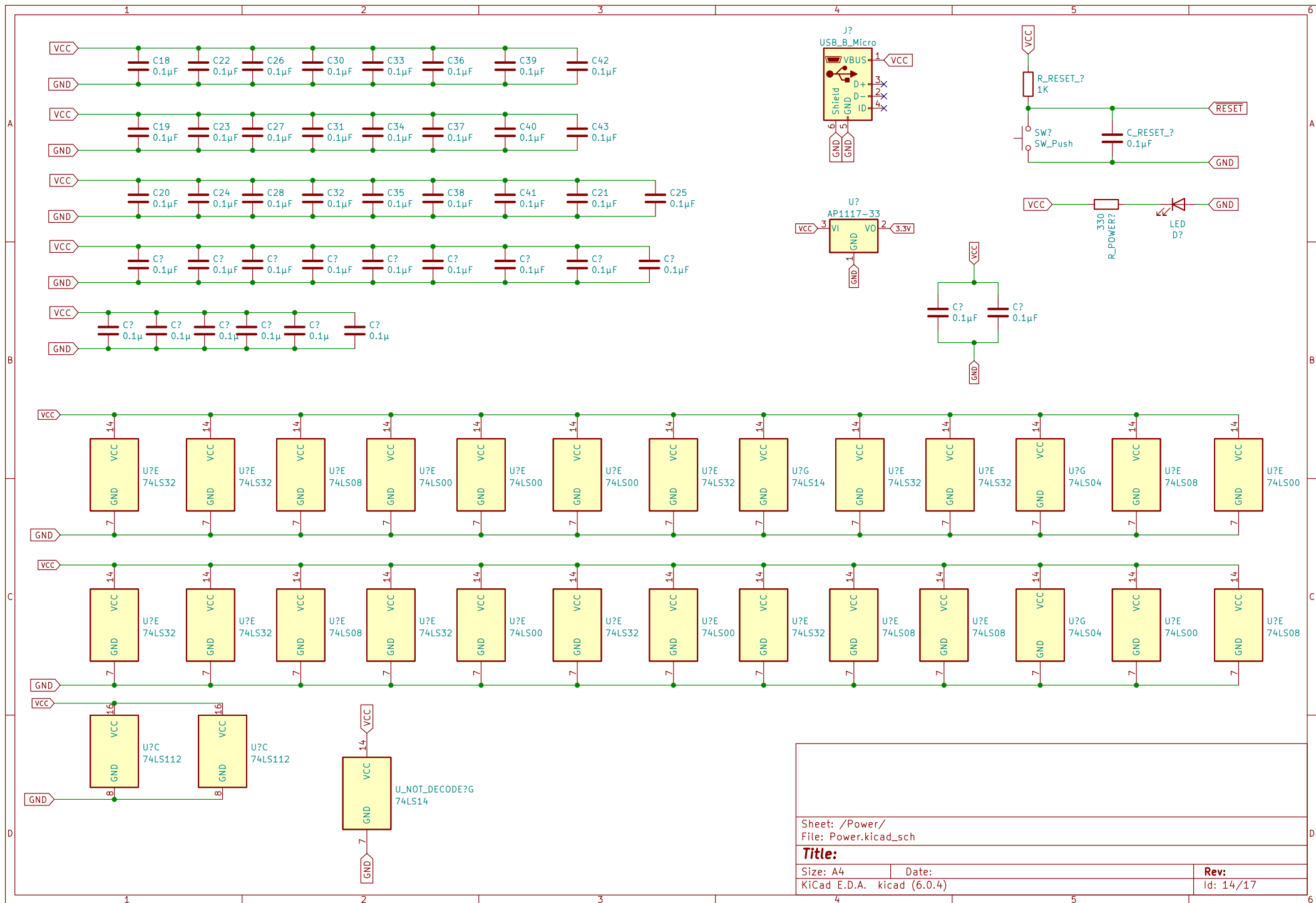








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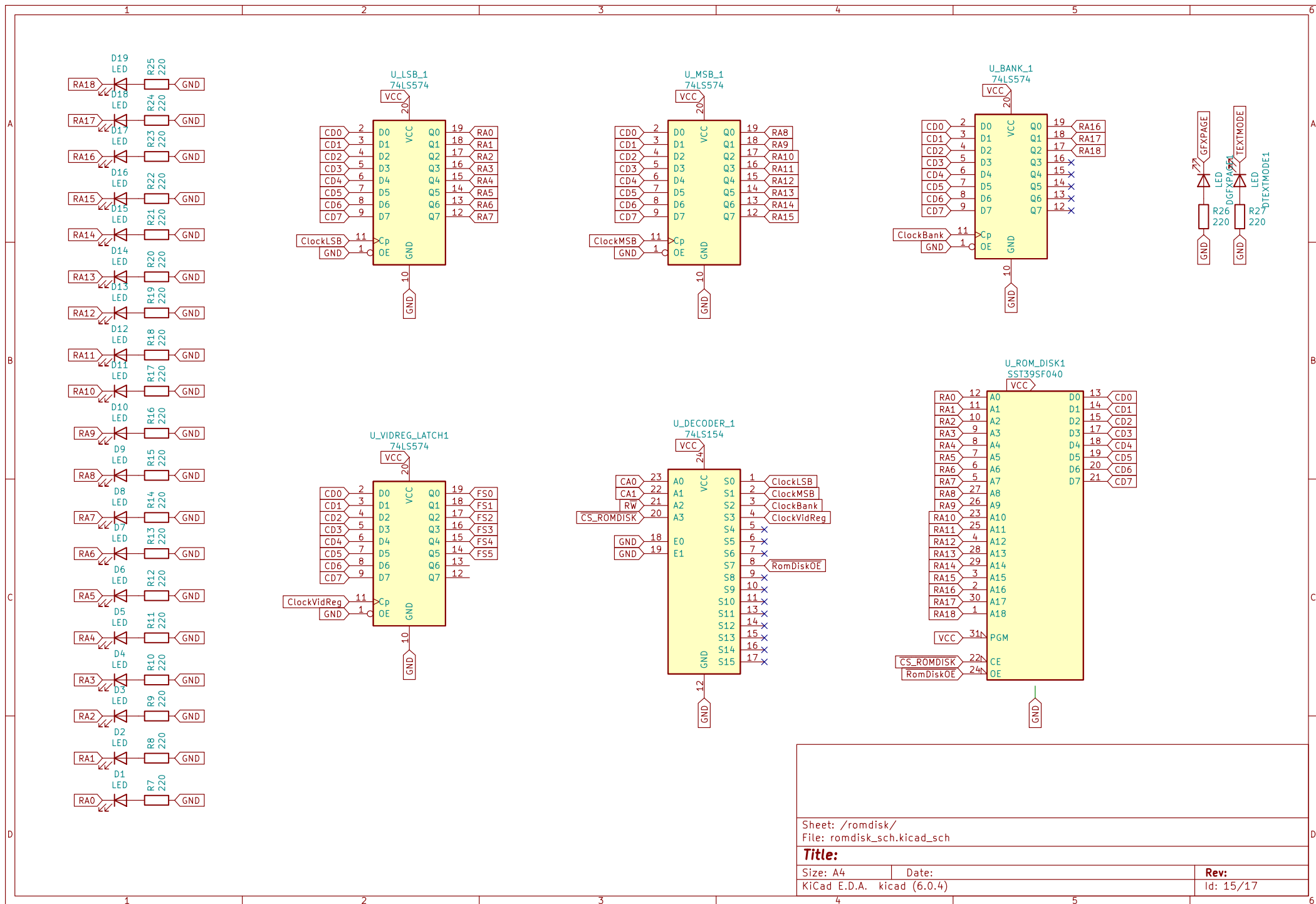
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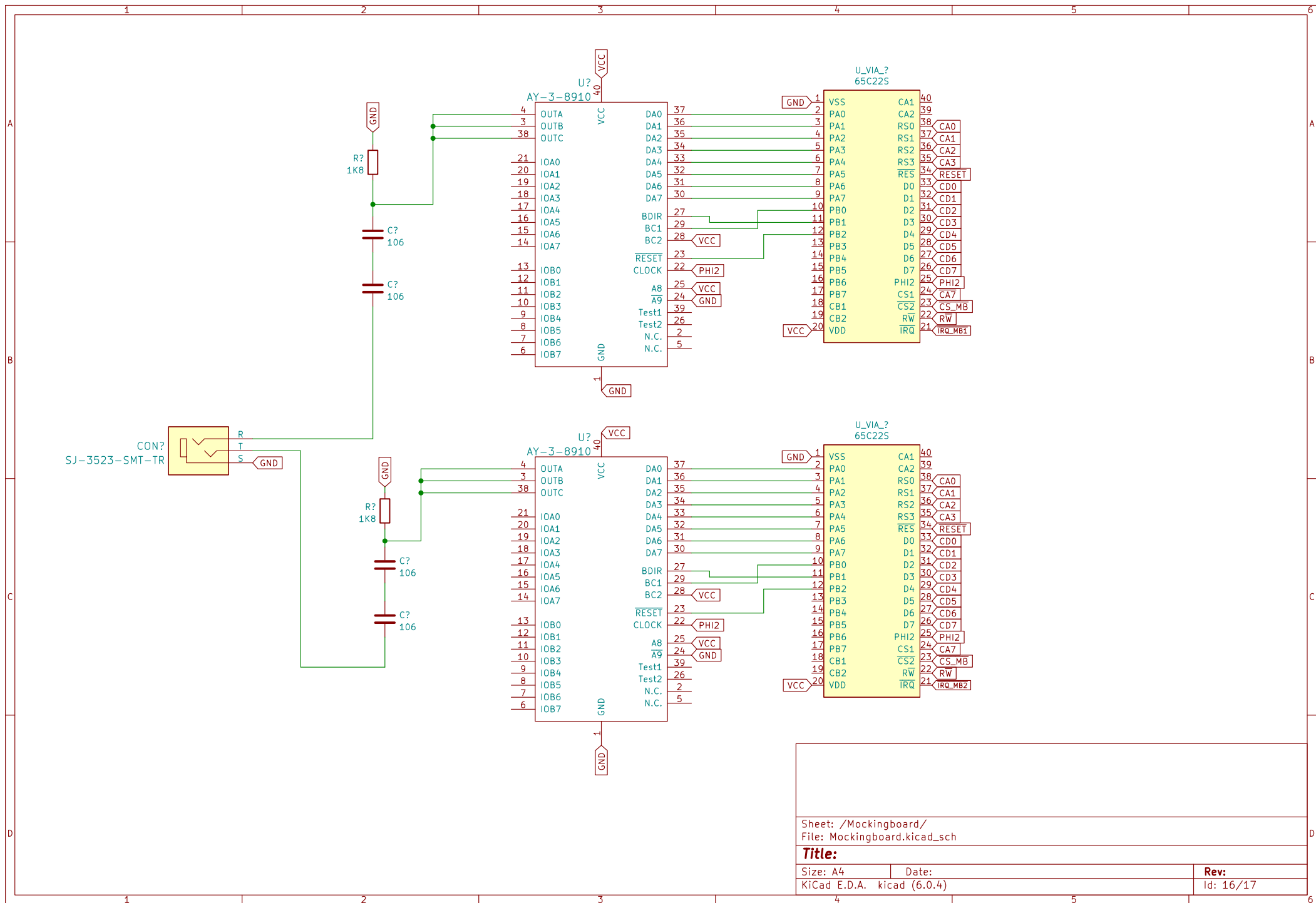
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