

General Aptitude (GA)**Q.1 – Q.5 Carry ONE mark Each**

Q.1	We reached the station late, and _____ missed the train.
(A)	near
(B)	nearly
(C)	utterly
(D)	mostly
Q.2	Kind : _____ : : Often : Frequently (By word meaning)
(A)	Mean
(B)	Type
(C)	Cruel
(D)	Kindly

Q.3	<p>A series of natural numbers $F_1, F_2, F_3, F_4, F_5, F_6, F_7, \dots$ obeys $F_{n+1} = F_n + F_{n-1}$ for all integers $n \geq 2$.</p> <p>If $F_6 = 37$, and $F_7 = 60$, then what is F_1 ?</p>
(A)	4
(B)	5
(C)	8
(D)	9

Q.4	<p>A survey for a certain year found that 90% of pregnant women received medical care at least once before giving birth. Of these women, 60% received medical care from doctors, while 40% received medical care from other healthcare providers.</p> <p>Given this information, which one of the following statements can be inferred with <i>certainty</i>?</p>
(A)	More than half of the pregnant women received medical care at least once from a doctor.
(B)	Less than half of the pregnant women received medical care at least once from a doctor.
(C)	More than half of the pregnant women received medical care at most once from a doctor.
(D)	Less than half of the pregnant women received medical care at most once from a doctor.
Q.5	<p>Looking at the surface of a smooth 3-dimensional object from the outside, which one of the following options is TRUE?</p>
(A)	The surface of the object must be concave everywhere.
(B)	The surface of the object must be convex everywhere.
(C)	The surface of the object may be concave in some places and convex in other places.
(D)	The object can have edges, but no corners.

Q.6 – Q.10 Carry TWO marks Each

Q.6	<p>The country of Zombieland is in distress since more than 75% of its working population is suffering from serious health issues. Studies conducted by competent health experts concluded that a complete lack of physical exercise among its working population was one of the leading causes of their health issues. As one of the measures to address the problem, the Government of Zombieland has decided to provide monetary incentives to those who ride bicycles to work.</p> <p>Based only on the information provided above, which one of the following statements can be logically inferred with <i>certainty</i>?</p>
(A)	All the working population of Zombieland will henceforth ride bicycles to work.
(B)	Riding bicycles will ensure that all of the working population of Zombieland is free of health issues.
(C)	The health experts suggested to the Government of Zombieland to declare riding bicycles as mandatory.
(D)	The Government of Zombieland believes that riding bicycles is a form of physical exercise.

Q.7	<p>Consider two functions of time (t),</p> $f(t) = 0.01 t^2$ $g(t) = 4 t$ <p>where $0 < t < \infty$.</p> <p>Now consider the following two statements:</p> <p>(i) For some $t > 0$, $g(t) > f(t)$.</p> <p>(ii) There exists a T, such that $f(t) > g(t)$ for all $t > T$.</p> <p>Which one of the following options is TRUE?</p>
(A)	only (i) is correct
(B)	only (ii) is correct
(C)	both (i) and (ii) are correct
(D)	neither (i) nor (ii) is correct

Q.8	Which one of the following sentence sequences creates a coherent narrative? (i) Once on the terrace, on her way to her small room in the corner, she notices the man right away. (ii) She begins to pant by the time she has climbed all the stairs. (iii) Mina has bought vegetables and rice at the market, so her bags are heavy. (iv) He was leaning against the parapet, watching the traffic below.
(A)	(i), (ii), (iv), (iii)
(B)	(ii), (iii), (i), (iv)
(C)	(iv), (ii), (i), (iii)
(D)	(iii), (ii), (i), (iv)
Q.9	$f(x)$ and $g(y)$ are functions of x and y , respectively, and $f(x) = g(y)$ for all real values of x and y . Which one of the following options is <i>necessarily</i> TRUE for all x and y ?
(A)	$f(x) = 0$ and $g(y) = 0$
(B)	$f(x) = g(y) = \text{constant}$
(C)	$f(x) \neq \text{constant}$ and $g(y) \neq \text{constant}$
(D)	$f(x) + g(y) = f(x) - g(y)$

Q.10	Which one of the options best describes the transformation of the 2-dimensional figure P to Q , and then to R , as shown?
	<p>The diagram shows three 5-pointed stars labeled P, Q, and R. P is the original star. An arrow labeled 'Operation 1' points from P to Q. Q is a 90-degree clockwise rotation of P. Another arrow labeled 'Operation 2' points from Q to R. R is a reflection of Q across a vertical line.</p>
(A)	<p><i>Operation 1:</i> A clockwise rotation by 90° about an axis perpendicular to the plane of the figure</p> <p><i>Operation 2:</i> A reflection along a horizontal line</p>
(B)	<p><i>Operation 1:</i> A counter clockwise rotation by 90° about an axis perpendicular to the plane of the figure</p> <p><i>Operation 2:</i> A reflection along a horizontal line</p>
(C)	<p><i>Operation 1:</i> A clockwise rotation by 90° about an axis perpendicular to the plane of the figure</p> <p><i>Operation 2:</i> A reflection along a vertical line</p>
(D)	<p><i>Operation 1:</i> A counter clockwise rotation by 180° about an axis perpendicular to the plane of the figure</p> <p><i>Operation 2:</i> A reflection along a vertical line</p>

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Q.11 – Q.35 Carry ONE mark each.

Q.11	<p>Consider the following statements regarding the front-end and back-end of a compiler.</p> <p>S1: The front-end includes phases that are independent of the target hardware.</p> <p>S2: The back-end includes phases that are specific to the target hardware.</p> <p>S3: The back-end includes phases that are specific to the programming language used in the source code.</p> <p>Identify the CORRECT option.</p>
(A)	Only S1 is TRUE.
(B)	Only S1 and S2 are TRUE.
(C)	S1 , S2 , and S3 are all TRUE.
(D)	Only S1 and S3 are TRUE.

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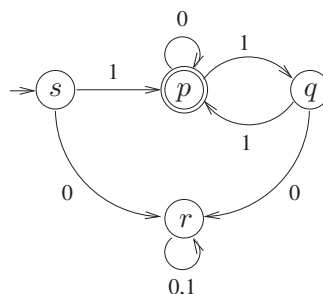
Q.12	Which one of the following sequences when stored in an array at locations $A[1], \dots, A[10]$ forms a max-heap?
(A)	23, 17, 10, 6, 13, 14, 1, 5, 7, 12
(B)	23, 17, 14, 7, 13, 10, 1, 5, 6, 12
(C)	23, 17, 14, 6, 13, 10, 1, 5, 7, 15
(D)	23, 14, 17, 1, 10, 13, 16, 12, 7, 5

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Q.13	<p>Let SLLdel be a function that deletes a node in a singly-linked list given a pointer to the node and a pointer to the head of the list. Similarly, let DLLdel be another function that deletes a node in a doubly-linked list given a pointer to the node and a pointer to the head of the list.</p> <p>Let n denote the number of nodes in each of the linked lists. Which one of the following choices is TRUE about the worst-case time complexity of SLLdel and DLLdel?</p>
(A)	SLLdel is $O(1)$ and DLLdel is $O(n)$
(B)	Both SLLdel and DLLdel are $O(\log(n))$
(C)	Both SLLdel and DLLdel are $O(1)$
(D)	SLLdel is $O(n)$ and DLLdel is $O(1)$

Q.14

Consider the Deterministic Finite-state Automaton (DFA) \mathcal{A} shown below. The DFA runs on the alphabet $\{0, 1\}$, and has the set of states $\{s, p, q, r\}$, with s being the start state and p being the only final state.



Which one of the following regular expressions correctly describes the language accepted by \mathcal{A} ?

(A) $1(0^*11)^*$ (B) $0(0 + 1)^*$ (C) $1(0 + 11)^*$ (D) $1(110^*)^*$

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Q.15	<p>The Lucas sequence L_n is defined by the recurrence relation:</p> $L_n = L_{n-1} + L_{n-2}, \quad \text{for } n \geq 3,$ <p>with $L_1 = 1$ and $L_2 = 3$.</p> <p>Which one of the options given is TRUE?</p>
(A)	$L_n = \left(\frac{1 + \sqrt{5}}{2}\right)^n + \left(\frac{1 - \sqrt{5}}{2}\right)^n$
(B)	$L_n = \left(\frac{1 + \sqrt{5}}{2}\right)^n - \left(\frac{1 - \sqrt{5}}{3}\right)^n$
(C)	$L_n = \left(\frac{1 + \sqrt{5}}{2}\right)^n + \left(\frac{1 - \sqrt{5}}{3}\right)^n$
(D)	$L_n = \left(\frac{1 + \sqrt{5}}{2}\right)^n - \left(\frac{1 - \sqrt{5}}{2}\right)^n$

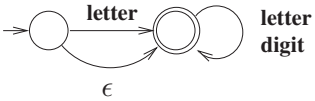
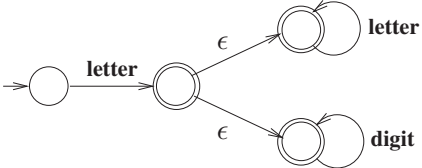
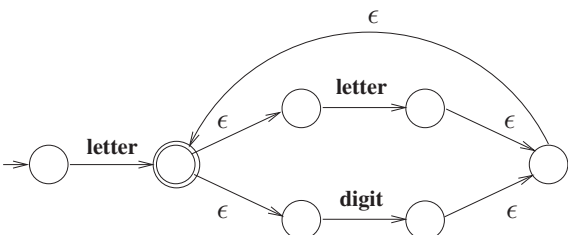
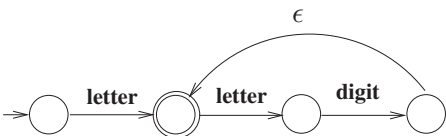
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Q.16	Which one of the options given below refers to the degree (or arity) of a relation in relational database systems?
(A)	Number of attributes of its relation schema.
(B)	Number of tuples stored in the relation.
(C)	Number of entries in the relation.
(D)	Number of distinct domains of its relation schema.

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Q.17	Suppose two hosts are connected by a point-to-point link and they are configured to use Stop-and-Wait protocol for reliable data transfer. Identify in which one of the following scenarios, the utilization of the link is the lowest.
(A)	Longer link length and lower transmission rate
(B)	Longer link length and higher transmission rate
(C)	Shorter link length and lower transmission rate
(D)	Shorter link length and higher transmission rate

Q.18	<p>Let</p> $A = \begin{bmatrix} 1 & 2 & 3 & 4 \\ 4 & 1 & 2 & 3 \\ 3 & 4 & 1 & 2 \\ 2 & 3 & 4 & 1 \end{bmatrix}$ <p>and</p> $B = \begin{bmatrix} 3 & 4 & 1 & 2 \\ 4 & 1 & 2 & 3 \\ 1 & 2 & 3 & 4 \\ 2 & 3 & 4 & 1 \end{bmatrix}.$ <p>Let $\det(A)$ and $\det(B)$ denote the determinants of the matrices A and B, respectively.</p> <p>Which one of the options given below is TRUE?</p>
(A)	$\det(A) = \det(B)$
(B)	$\det(B) = -\det(A)$
(C)	$\det(A) = 0$
(D)	$\det(AB) = \det(A) + \det(B)$

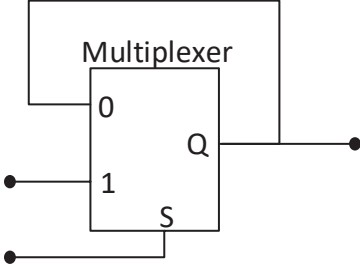
Q.19	<p>Consider the following definition of a lexical token id for an identifier in a programming language, using extended regular expressions:</p> $\begin{aligned}\text{letter} &\rightarrow [A-Za-z] \\ \text{digit} &\rightarrow [0-9] \\ \text{id} &\rightarrow \text{letter} (\text{letter} \mid \text{digit})^*\end{aligned}$ <p>Which one of the following Non-deterministic Finite-state Automata with ϵ-transitions accepts the set of valid identifiers? (A double-circle denotes a final state)</p>
(A)	
(B)	
(C)	
(D)	

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Q.20	<p>An algorithm has to store several keys generated by an adversary in a hash table. The adversary is malicious who tries to maximize the number of collisions. Let k be the number of keys, m be the number of slots in the hash table, and $k > m$.</p> <p>Which one of the following is the best hashing strategy to counteract the adversary?</p>
(A)	Division method, i.e., use the hash function $h(k) = k \bmod m$.
(B)	Multiplication method, i.e., use the hash function $h(k) = \lfloor m(kA - \lfloor kA \rfloor) \rfloor$, where A is a carefully chosen constant.
(C)	Universal hashing method.
(D)	If k is a prime number, use Division method. Otherwise, use Multiplication method.

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Q.21	<p>The output of a 2-input multiplexer is connected back to one of its inputs as shown in the figure.</p>  <p>Match the functional equivalence of this circuit to one of the following options.</p>
(A)	D Flip-flop
(B)	D Latch
(C)	Half-adder
(D)	Demultiplexer

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Q.22	Which one or more of the following need to be saved on a context switch from one thread (T1) of a process to another thread (T2) of the same process?
(A)	Page table base register
(B)	Stack pointer
(C)	Program counter
(D)	General purpose registers

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Q.23	Which one or more of the following options guarantee that a computer system will transition from user mode to kernel mode?
(A)	Function Call
(B)	<code>malloc</code> Call
(C)	Page Fault
(D)	System Call

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Q.24	Which of the following statements is/are CORRECT?
(A)	The intersection of two regular languages is regular.
(B)	The intersection of two context-free languages is context-free.
(C)	The intersection of two recursive languages is recursive.
(D)	The intersection of two recursively enumerable languages is recursively enumerable.

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Q.25	Which of the following statements is/are INCORRECT about the OSPF (Open Shortest Path First) routing protocol used in the Internet?
(A)	OSPF implements Bellman-Ford algorithm to find shortest paths.
(B)	OSPF uses Dijkstra's shortest path algorithm to implement least-cost path routing.
(C)	OSPF is used as an inter-domain routing protocol.
(D)	OSPF implements hierarchical routing.

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Q.26	<p>Geetha has a conjecture about integers, which is of the form</p> $\forall x \left(P(x) \implies \exists y Q(x, y) \right),$ <p>where P is a statement about integers, and Q is a statement about pairs of integers. Which of the following (one or more) option(s) would <i>imply</i> Geetha's conjecture?</p>
(A)	$\exists x \left(P(x) \wedge \forall y Q(x, y) \right)$
(B)	$\forall x \forall y Q(x, y)$
(C)	$\exists y \forall x \left(P(x) \implies Q(x, y) \right)$
(D)	$\exists x \left(P(x) \wedge \exists y Q(x, y) \right)$

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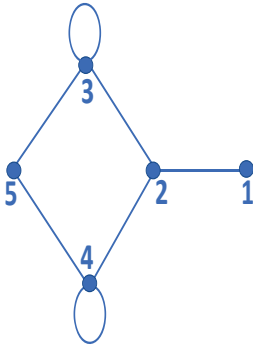
Q.27	Which one or more of the following CPU scheduling algorithms can potentially cause starvation?
(A)	First-in First-Out
(B)	Round Robin
(C)	Priority Scheduling
(D)	Shortest Job First

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Q.28	<p>Let</p> $f(x) = x^3 + 15x^2 - 33x - 36$ <p>be a real-valued function.</p> <p>Which of the following statements is/are TRUE?</p>
(A)	$f(x)$ does not have a local maximum.
(B)	$f(x)$ has a local maximum.
(C)	$f(x)$ does not have a local minimum.
(D)	$f(x)$ has a local minimum.

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Q.29	Let f and g be functions of natural numbers given by $f(n) = n$ and $g(n) = n^2$. Which of the following statements is/are TRUE?
(A)	$f \in O(g)$
(B)	$f \in \Omega(g)$
(C)	$f \in o(g)$
(D)	$f \in \Theta(g)$

Q.30	<p>Let A be the adjacency matrix of the graph with vertices $\{1, 2, 3, 4, 5\}$.</p>  <p>Let $\lambda_1, \lambda_2, \lambda_3, \lambda_4$, and λ_5 be the five eigenvalues of A. Note that these eigenvalues need not be distinct. The value of $\lambda_1 + \lambda_2 + \lambda_3 + \lambda_4 + \lambda_5 =$ _____.</p>

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Q.31	<p>The value of the definite integral</p> $\int_{-3}^3 \int_{-2}^2 \int_{-1}^1 (4x^2y - z^3) \, dz \, dy \, dx$ <p>is _____. (Rounded off to the nearest integer)</p>

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Q.32	A particular number is written as 132 in radix-4 representation. The same number in radix-5 representation is _____.

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Q.33	<p>Consider a 3-stage pipelined processor having a delay of 10 ns (nanoseconds), 20 ns, and 14 ns, for the first, second, and the third stages, respectively. Assume that there is no other delay and the processor does not suffer from any pipeline hazards. Also assume that one instruction is fetched every cycle.</p> <p>The total execution time for executing 100 instructions on this processor is _____ ns.</p>

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Q.34	<p>A keyboard connected to a computer is used at a rate of 1 keystroke per second. The computer system polls the keyboard every 10 ms (milli seconds) to check for a keystroke and consumes $100\ \mu\text{s}$ (micro seconds) for each poll. If it is determined after polling that a key has been pressed, the system consumes an additional $200\ \mu\text{s}$ to process the keystroke. Let T_1 denote the fraction of a second spent in polling and processing a keystroke.</p> <p>In an alternative implementation, the system uses interrupts instead of polling. An interrupt is raised for every keystroke. It takes a total of 1 ms for servicing an interrupt and processing a keystroke. Let T_2 denote the fraction of a second spent in servicing the interrupt and processing a keystroke.</p> <p>The ratio $\frac{T_1}{T_2}$ is _____. (Rounded off to one decimal place)</p>

Q.35	<p>The integer value printed by the ANSI-C program given below is _____.</p> <pre>#include<stdio.h> int funcp(){ static int x = 1; x++; return x; } int main(){ int x,y; x = funcp(); y = funcp()+x; printf("%d\n", (x+y)); return 0; }</pre>

Q.36 – Q.65 Carry TWO mark each.

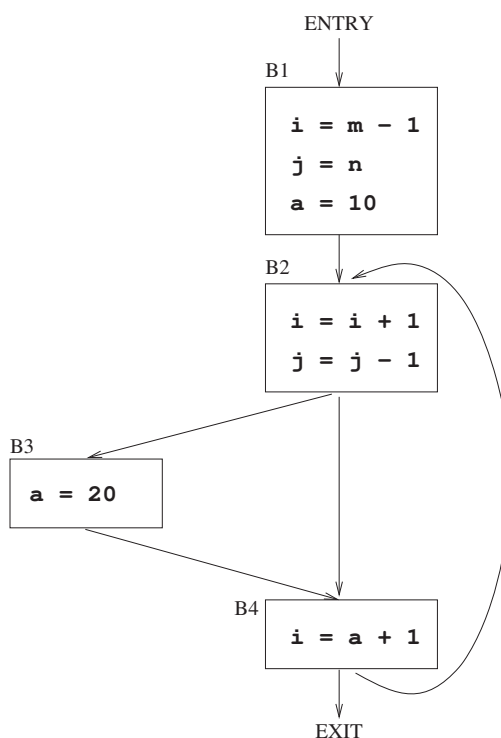
Q.36	<p>Consider the following program:</p> <pre> int main() int f1() int f2(int X) int f3() { f1(); { return(1); { f3(); if (X==1) return f1(); else return (X*f2(X-1)); } } } f2(2); f3(); return(0); } </pre> <p>Which one of the following options represents the activation tree corresponding to the main function?</p>
(A)	<pre> graph TD main --> f1 main --> f2 main --> f3 f2 --> f3 f2 --> f1 f3 --> f1 </pre>
(B)	<pre> graph TD main --> f1 main --> f2 main --> f3 f2 --> f3 f2 --> f1 </pre>
(C)	<pre> graph TD main --> f1 f1 --> f2 f2 --> f3 f2 --> f1 </pre>
(D)	<pre> graph TD main --> f1 main --> f2 main --> f3 f2 --> f3 f2 --> f2 f2 --> f1 </pre>

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Q.37

Consider the control flow graph shown.



Which one of the following choices correctly lists the set of *live* variables at the exit point of each basic block?

- (A) B1: {}, B2: {a}, B3: {a}, B4: {a}
- (B) B1: {i, j}, B2: {a}, B3: {a}, B4: {i}
- (C) B1: {a, i, j}, B2: {a, i, j}, B3: {a, i}, B4: {a}
- (D) B1: {a, i, j}, B2: {a, j}, B3: {a, j}, B4: {a, i, j}

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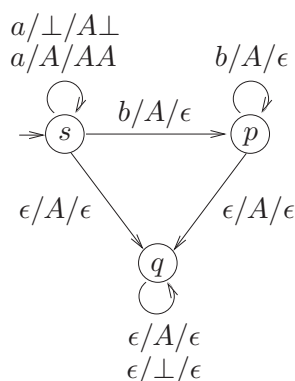
Q.38	<p>Consider the two functions <code>incr</code> and <code>decr</code> shown below.</p> <table border="0" style="width: 100%;"> <tr> <td style="width: 50%; vertical-align: top;"> <pre>incr(){ wait(s); X = X+1; signal(s); }</pre> </td><td style="width: 50%; vertical-align: top;"> <pre>decr(){ wait(s); X = X-1; signal(s); }</pre> </td></tr> </table> <p>There are 5 threads each invoking <code>incr</code> once, and 3 threads each invoking <code>decr</code> once, on the same shared variable <code>X</code>. The initial value of <code>X</code> is 10.</p> <p>Suppose there are two implementations of the semaphore <code>s</code>, as follows:</p> <p>I-1: <code>s</code> is a binary semaphore initialized to 1.</p> <p>I-2: <code>s</code> is a counting semaphore initialized to 2.</p> <p>Let <code>V1</code>, <code>V2</code> be the values of <code>X</code> at the end of execution of all the threads with implementations I-1, I-2, respectively.</p> <p>Which one of the following choices corresponds to the minimum possible values of <code>V1</code>, <code>V2</code>, respectively?</p>	<pre>incr(){ wait(s); X = X+1; signal(s); }</pre>	<pre>decr(){ wait(s); X = X-1; signal(s); }</pre>
<pre>incr(){ wait(s); X = X+1; signal(s); }</pre>	<pre>decr(){ wait(s); X = X-1; signal(s); }</pre>		
(A)	15, 7		
(B)	7, 7		
(C)	12, 7		
(D)	12, 8		

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Q.39	<p>Consider the context-free grammar G below</p> $\begin{aligned} S &\rightarrow aSb \mid X \\ X &\rightarrow aX \mid Xb \mid a \mid b, \end{aligned}$ <p>where S and X are non-terminals, and a and b are terminal symbols. The starting non-terminal is S.</p> <p>Which one of the following statements is CORRECT?</p>
(A)	The language generated by G is $(a + b)^*$
(B)	The language generated by G is $a^*(a + b)b^*$
(C)	The language generated by G is $a^*b^*(a + b)$
(D)	The language generated by G is not a regular language

Q.40

Consider the pushdown automaton (PDA) P below, which runs on the input alphabet $\{a, b\}$, has stack alphabet $\{\perp, A\}$, and has three states $\{s, p, q\}$, with s being the start state. A transition from state u to state v , labelled $c/X/\gamma$, where c is an input symbol or ϵ , X is a stack symbol, and γ is a string of stack symbols, represents the fact that in state u , the PDA can read c from the input, with X on the top of its stack, pop X from the stack, push in the string γ on the stack, and go to state v . In the initial configuration, the stack has only the symbol \perp in it. The PDA accepts by empty stack.



Which one of the following options correctly describes the language accepted by P ?

(A) $\{a^m b^n \mid 1 \leq m \text{ and } n < m\}$

(B) $\{a^m b^n \mid 0 \leq n \leq m\}$

(C) $\{a^m b^n \mid 0 \leq m \text{ and } 0 \leq n\}$

(D) $\{a^m \mid 0 \leq m\} \cup \{b^n \mid 0 \leq n\}$

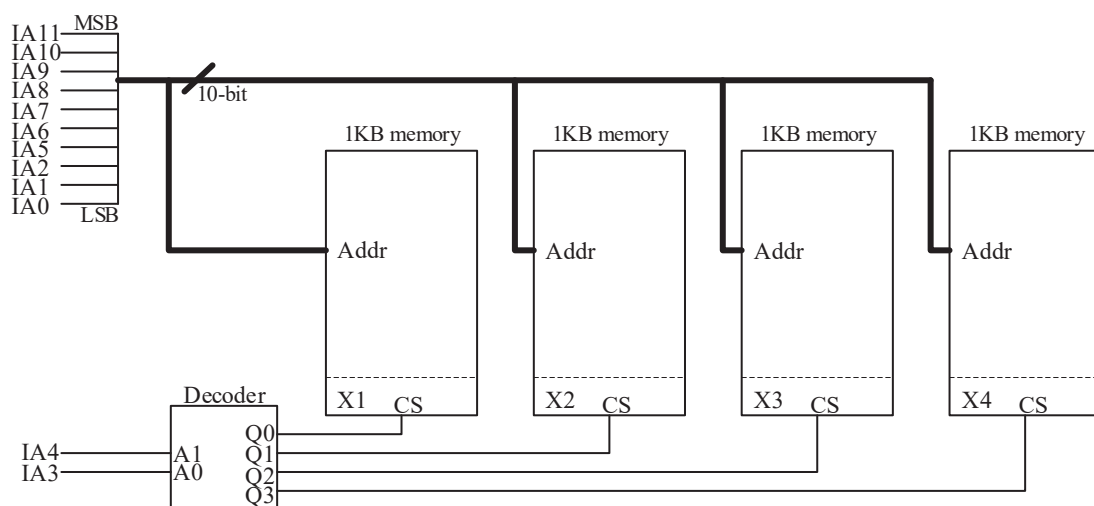
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Q.41	<p>Consider the given C-code and its corresponding assembly code, with a few operands U1–U4 being unknown. Some useful information as well as the semantics of each unique assembly instruction is annotated as inline comments in the code. The memory is byte-addressable.</p> <table border="1" data-bbox="332 451 1461 976"> <tr> <td data-bbox="332 451 669 493">//C-code</td><td data-bbox="669 451 1461 493">;assembly-code (; indicates comments)</td></tr> <tr> <td data-bbox="332 493 669 535"></td><td data-bbox="669 493 1461 535">;r1-r5 are 32-bit integer registers</td></tr> <tr> <td data-bbox="332 535 669 577"></td><td data-bbox="669 535 1461 577">;initialize r1=0, r2=10</td></tr> <tr> <td data-bbox="332 577 669 619"></td><td data-bbox="669 577 1461 619">;initialize r3, r4 with base address of a, b</td></tr> <tr> <td data-bbox="332 640 669 682">int a[10], b[10], i;</td><td data-bbox="669 640 1461 682">L01: jeq r1, r2, end ;if(r1==r2) goto end</td></tr> <tr> <td data-bbox="332 682 669 724">// int is 32-bit</td><td data-bbox="669 682 1461 724">L02: lw r5, 0(r4) ;r5 <- Memory[r4+0]</td></tr> <tr> <td data-bbox="332 724 669 766">for (i=0; i<10;i++)</td><td data-bbox="669 724 1461 766">L03: shl r5, r5, U1 ;r5 <- r5 << U1</td></tr> <tr> <td data-bbox="332 766 669 808">a[i] = b[i] * 8;</td><td data-bbox="669 766 1461 808">L04: sw r5, 0(r3) ;Memory[r3+0] <- r5</td></tr> <tr> <td data-bbox="332 808 669 850"></td><td data-bbox="669 808 1461 850">L05: add r3, r3, U2 ;r3 <- r3+U2</td></tr> <tr> <td data-bbox="332 850 669 892"></td><td data-bbox="669 850 1461 892">L06: add r4, r4, U3</td></tr> <tr> <td data-bbox="332 892 669 934"></td><td data-bbox="669 892 1461 934">L07: add r1, r1, 1</td></tr> <tr> <td data-bbox="332 934 669 976"></td><td data-bbox="669 934 1461 976">L08: jmp U4 ;goto U4</td></tr> <tr> <td data-bbox="332 976 669 1018"></td><td data-bbox="669 976 1461 1018">L09: end</td></tr> </table> <p>Which one of the following options is a CORRECT replacement for operands in the position (U1, U2, U3, U4) in the above assembly code?</p>	//C-code	;assembly-code (; indicates comments)		;r1-r5 are 32-bit integer registers		;initialize r1=0, r2=10		;initialize r3, r4 with base address of a, b	int a[10], b[10], i;	L01: jeq r1, r2, end ;if(r1==r2) goto end	// int is 32-bit	L02: lw r5, 0(r4) ;r5 <- Memory[r4+0]	for (i=0; i<10;i++)	L03: shl r5, r5, U1 ;r5 <- r5 << U1	a[i] = b[i] * 8;	L04: sw r5, 0(r3) ;Memory[r3+0] <- r5		L05: add r3, r3, U2 ;r3 <- r3+U2		L06: add r4, r4, U3		L07: add r1, r1, 1		L08: jmp U4 ;goto U4		L09: end
//C-code	;assembly-code (; indicates comments)																										
	;r1-r5 are 32-bit integer registers																										
	;initialize r1=0, r2=10																										
	;initialize r3, r4 with base address of a, b																										
int a[10], b[10], i;	L01: jeq r1, r2, end ;if(r1==r2) goto end																										
// int is 32-bit	L02: lw r5, 0(r4) ;r5 <- Memory[r4+0]																										
for (i=0; i<10;i++)	L03: shl r5, r5, U1 ;r5 <- r5 << U1																										
a[i] = b[i] * 8;	L04: sw r5, 0(r3) ;Memory[r3+0] <- r5																										
	L05: add r3, r3, U2 ;r3 <- r3+U2																										
	L06: add r4, r4, U3																										
	L07: add r1, r1, 1																										
	L08: jmp U4 ;goto U4																										
	L09: end																										
(A)	(8, 4, 1, L02)																										
(B)	(3, 4, 4, L01)																										
(C)	(8, 1, 1, L02)																										
(D)	(3, 1, 1, L01)																										

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Q.42

A 4 kilobyte (KB) byte-addressable memory is realized using four 1 KB memory blocks. Two input address lines (IA4 and IA3) are connected to the chip select (CS) port of these memory blocks through a decoder as shown in the figure. The remaining ten input address lines from IA11–IA0 are connected to the address port of these blocks. The chip select (CS) is active high.



The input memory addresses (IA11–IA0), in decimal, for the starting locations (Addr=0) of each block (indicated as X1, X2, X3, X4 in the figure) are among the options given below. Which one of the following options is CORRECT?

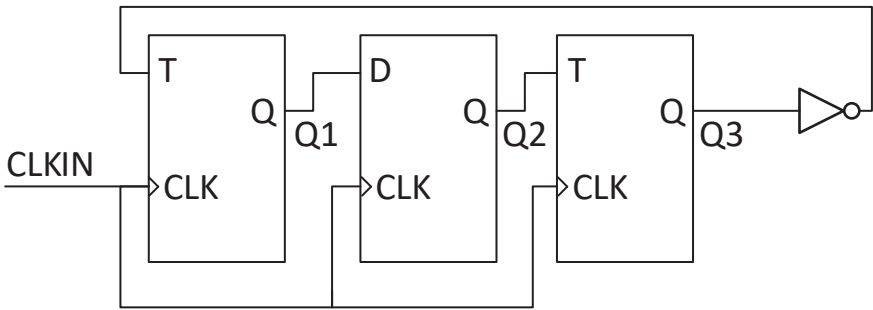
(A) (0, 1, 2, 3)

(B) (0, 1024, 2048, 3072)

(C) (0, 8, 16, 24)

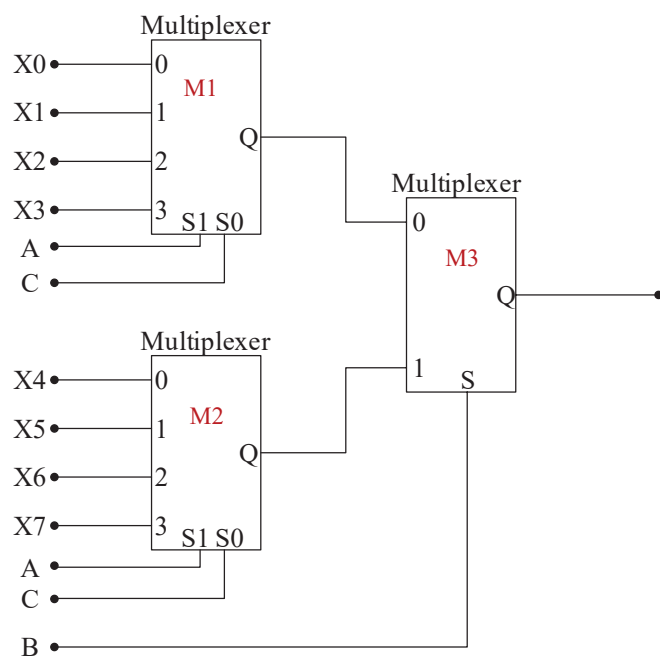
(D) (0, 0, 0, 0)

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<p>Q.43</p>	<p>Consider a sequential digital circuit consisting of T flip-flops and D flip-flops as shown in the figure. CLKIN is the clock input to the circuit. At the beginning, Q1, Q2 and Q3 have values 0, 1 and 1, respectively.</p>  <p>Which one of the given values of (Q1, Q2, Q3) can NEVER be obtained with this digital circuit?</p>
<p>(A)</p>	<p>(0, 0, 1)</p>
<p>(B)</p>	<p>(1, 0, 0)</p>
<p>(C)</p>	<p>(1, 0, 1)</p>
<p>(D)</p>	<p>(1, 1, 1)</p>

Q.44

A Boolean digital circuit is composed using two 4-input multiplexers (M1 and M2) and one 2-input multiplexer (M3) as shown in the figure. X0–X7 are the inputs of the multiplexers M1 and M2 and could be connected to either 0 or 1. The select lines of the multiplexers are connected to Boolean variables A, B and C as shown.



Which one of the following set of values of (X0, X1, X2, X3, X4, X5, X6, X7) will realise the Boolean function $\overline{A} + \overline{A}.\overline{C} + A.\overline{B}.C$?

(A) (1, 1, 0, 0, 1, 1, 1, 0)

(B) (1, 1, 0, 0, 1, 1, 0, 1)

(C) (1, 1, 0, 1, 1, 1, 0, 0)

(D) (0, 0, 1, 1, 0, 1, 1, 1)

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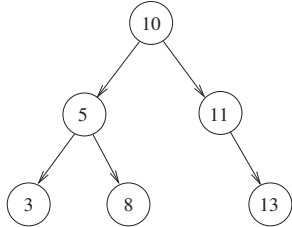
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Q.45	Consider the IEEE-754 single precision floating point numbers $P=0xC1800000$ and $Q=0x3F5C2EF4$. Which one of the following corresponds to the product of these numbers (i.e., $P \times Q$), represented in the IEEE-754 single precision format?
(A)	$0x404C2EF4$
(B)	$0x405C2EF4$
(C)	$0xC15C2EF4$
(D)	$0xC14C2EF4$

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Q.46	<p>Let A be a priority queue for maintaining a set of elements. Suppose A is implemented using a max-heap data structure. The operation $\text{EXTRACT-MAX}(A)$ extracts and deletes the maximum element from A. The operation $\text{INSERT}(A, key)$ inserts a new element key in A. The properties of a max-heap are preserved at the end of each of these operations.</p> <p>When A contains n elements, which one of the following statements about the worst case running time of these two operations is TRUE?</p>
(A)	Both $\text{EXTRACT-MAX}(A)$ and $\text{INSERT}(A, key)$ run in $O(1)$.
(B)	Both $\text{EXTRACT-MAX}(A)$ and $\text{INSERT}(A, key)$ run in $O(\log(n))$.
(C)	$\text{EXTRACT-MAX}(A)$ runs in $O(1)$ whereas $\text{INSERT}(A, key)$ runs in $O(n)$.
(D)	$\text{EXTRACT-MAX}(A)$ runs in $O(1)$ whereas $\text{INSERT}(A, key)$ runs in $O(\log(n))$.

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Q.47	<p>Consider the C function <code>foo</code> and the binary tree shown.</p> <pre> typedef struct node { int val; struct node *left, *right; } node; int foo(node *p) { int retval; if (p == NULL) return 0; else { retval = p->val + foo(p->left) + foo(p->right); printf("%d ", retval); return retval; } } </pre>  <pre> graph TD 10((10)) --> 5((5)) 10 --> 11((11)) 5 --> 3((3)) 5 --> 8((8)) 11 --> 13((13)) </pre> <p>When <code>foo</code> is called with a pointer to the root node of the given binary tree, what will it print?</p>
(A)	3 8 5 13 11 10
(B)	3 5 8 10 11 13
(C)	3 8 16 13 24 50
(D)	3 16 8 50 24 13

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Q.48	<p>Let $U = \{1, 2, \dots, n\}$, where n is a large positive integer greater than 1000. Let k be a positive integer less than n. Let A, B be subsets of U with $A = B = k$ and $A \cap B = \emptyset$. We say that a permutation of U separates A from B if one of the following is true.</p> <ul style="list-style-type: none"> - All members of A appear in the permutation before any of the members of B. - All members of B appear in the permutation before any of the members of A. <p>How many permutations of U separate A from B?</p>
(A)	$n!$
(B)	$\binom{n}{2k} (n - 2k)!$
(C)	$\binom{n}{2k} (n - 2k)! (k!)^2$
(D)	$2 \binom{n}{2k} (n - 2k)! (k!)^2$

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Q.49	<p>Let $f : A \rightarrow B$ be an onto (or surjective) function, where A and B are nonempty sets. Define an equivalence relation \sim on the set A as</p> $a_1 \sim a_2 \text{ if } f(a_1) = f(a_2) ,$ <p>where $a_1, a_2 \in A$. Let $\mathcal{E} = \{[x] : x \in A\}$ be the set of all the equivalence classes under \sim. Define a new mapping $F : \mathcal{E} \rightarrow B$ as</p> $F([x]) = f(x), \text{ for all the equivalence classes } [x] \text{ in } \mathcal{E}.$ <p>Which of the following statements is/are TRUE?</p>
(A)	F is NOT well-defined.
(B)	F is an onto (or surjective) function.
(C)	F is a one-to-one (or injective) function.
(D)	F is a bijective function.

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Q.50	<p>Suppose you are asked to design a new reliable byte-stream transport protocol like TCP. This protocol, named myTCP, runs over a 100 Mbps network with Round Trip Time of 150 milliseconds and the maximum segment lifetime of 2 minutes.</p> <p>Which of the following is/are valid lengths of the Sequence Number field in the myTCP header?</p>
(A)	30 bits
(B)	32 bits
(C)	34 bits
(D)	36 bits

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Q.51	<p>Let X be a set and 2^X denote the powerset of X. Define a binary operation Δ on 2^X as follows:</p> $A\Delta B = (A - B) \cup (B - A) .$ <p>Let $H = (2^X, \Delta)$. Which of the following statements about H is/are correct?</p>
(A)	H is a group.
(B)	Every element in H has an inverse, but H is NOT a group.
(C)	For every $A \in 2^X$, the inverse of A is the complement of A .
(D)	For every $A \in 2^X$, the inverse of A is A .

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Q.52	<p>Suppose in a web browser, you click on the <code>www.gate-2023.in</code> URL. The browser cache is empty. The IP address for this URL is not cached in your local host, so a DNS lookup is triggered (by the local DNS server deployed on your local host) over the 3-tier DNS hierarchy in an iterative mode. No resource records are cached anywhere across all DNS servers.</p> <p>Let RTT denote the round trip time between your local host and DNS servers in the DNS hierarchy. The round trip time between the local host and the web server hosting <code>www.gate-2023.in</code> is also equal to RTT. The HTML file associated with the URL is small enough to have negligible transmission time and negligible rendering time by your web browser, which references 10 equally small objects on the same web server.</p> <p>Which of the following statements is/are CORRECT about the minimum elapsed time between clicking on the URL and your browser fully rendering it?</p>
(A)	7 RTTs, in case of non-persistent HTTP with 5 parallel TCP connections.
(B)	5 RTTs, in case of persistent HTTP with pipelining.
(C)	9 RTTs, in case of non-persistent HTTP with 5 parallel TCP connections.
(D)	6 RTTs, in case of persistent HTTP with pipelining.

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Q.53	Consider a random experiment where two fair coins are tossed. Let A be the event that denotes HEAD on both the throws, B be the event that denotes HEAD on the first throw, and C be the event that denotes HEAD on the second throw. Which of the following statements is/are TRUE?
(A)	A and B are independent.
(B)	A and C are independent.
(C)	B and C are independent.
(D)	$\text{Prob}(B C) = \text{Prob}(B)$

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Q.54	<p>Consider functions Function_1 and Function_2 expressed in pseudocode as follows:</p> <div style="display: flex; justify-content: space-between;"> <div style="width: 45%;"> <p>Function_1</p> <pre> while $n > 1$ do for $i = 1$ to n do $x = x + 1$; end for $n = \lfloor n/2 \rfloor$; end while </pre> </div> <div style="width: 45%;"> <p>Function_2</p> <pre> for $i = 1$ to $100 * n$ do $x = x + 1$; end for </pre> </div> </div> <p>Let $f_1(n)$ and $f_2(n)$ denote the number of times the statement “$x = x + 1$” is executed in Function_1 and Function_2, respectively.</p> <p>Which of the following statements is/are TRUE?</p>
(A)	$f_1(n) \in \Theta(f_2(n))$
(B)	$f_1(n) \in o(f_2(n))$
(C)	$f_1(n) \in \omega(f_2(n))$
(D)	$f_1(n) \in O(n)$

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Q.55	<p>Let G be a simple, finite, undirected graph with vertex set $\{v_1, \dots, v_n\}$. Let $\Delta(G)$ denote the maximum degree of G and let $\mathbb{N} = \{1, 2, \dots\}$ denote the set of all possible colors. Color the vertices of G using the following greedy strategy: for $i = 1, \dots, n$</p> $\text{color}(v_i) \leftarrow \min\{j \in \mathbb{N} : \text{no neighbour of } v_i \text{ is colored } j\}$ <p>Which of the following statements is/are TRUE?</p>
(A)	This procedure results in a proper vertex coloring of G .
(B)	The number of colors used is at most $\Delta(G) + 1$.
(C)	The number of colors used is at most $\Delta(G)$.
(D)	The number of colors used is equal to the chromatic number of G .

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Q.56	<p>Let $U = \{1, 2, 3\}$. Let 2^U denote the powerset of U. Consider an undirected graph G whose vertex set is 2^U. For any $A, B \in 2^U$, (A, B) is an edge in G if and only if (i) $A \neq B$, and (ii) either $A \subsetneq B$ or $B \subsetneq A$. For any vertex A in G, the set of all possible orderings in which the vertices of G can be visited in a Breadth First Search (BFS) starting from A is denoted by $\mathcal{B}(A)$.</p> <p>If \emptyset denotes the empty set, then the cardinality of $\mathcal{B}(\emptyset)$ is _____.</p>

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Q.57	<p>Consider the following two-dimensional array D in the C programming language, which is stored in row-major order:</p> <pre>int D[128][128];</pre> <p>Demand paging is used for allocating memory and each physical page frame holds 512 elements of the array D. The Least Recently Used (LRU) page-replacement policy is used by the operating system. A total of 30 physical page frames are allocated to a process which executes the following code snippet:</p> <pre>for (int i = 0; i < 128; i++) for (int j = 0; j < 128; j++) D[j][i] *= 10;</pre> <p>The number of page faults generated during the execution of this code snippet is _____.</p>

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Q.58	<p>Consider a computer system with 57-bit virtual addressing using multi-level tree-structured page tables with L levels for virtual to physical address translation. The page size is 4 KB (1 KB = 1024 B) and a page table entry at any of the levels occupies 8 bytes.</p> <p>The value of L is _____.</p>

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Q.59	<p>Consider a sequence a of elements $a_0 = 1$, $a_1 = 5$, $a_2 = 7$, $a_3 = 8$, $a_4 = 9$, and $a_5 = 2$. The following operations are performed on a stack S and a queue Q, both of which are initially empty.</p> <p>I: push the elements of a from a_0 to a_5 in that order into S.</p> <p>II: enqueue the elements of a from a_0 to a_5 in that order into Q.</p> <p>III: pop an element from S.</p> <p>IV: dequeue an element from Q.</p> <p>V: pop an element from S.</p> <p>VI: dequeue an element from Q.</p> <p>VII: dequeue an element from Q and push the same element into S.</p> <p>VIII: Repeat operation VII three times.</p> <p>IX: pop an element from S.</p> <p>X: pop an element from S.</p> <p>The top element of S after executing the above operations is _____.</p>

Q.60	<p>Consider the syntax directed translation given by the following grammar and semantic rules. Here N, I, F and B are non-terminals. N is the starting non-terminal, and $\#$, $\mathbf{0}$ and $\mathbf{1}$ are lexical tokens corresponding to input letters “$\#$”, “0” and “1”, respectively. $X.val$ denotes the synthesized attribute (a numeric value) associated with a non-terminal X. I_1 and F_1 denote occurrences of I and F on the right hand side of a production, respectively. For the tokens $\mathbf{0}$ and $\mathbf{1}$, $\mathbf{0}.val = 0$ and $\mathbf{1}.val = 1$.</p> $ \begin{array}{lll} N & \rightarrow & I \# F \quad N.val = I.val + F.val \\ I & \rightarrow & I_1 B \quad I.val = (2 I_1.val) + B.val \\ I & \rightarrow & B \quad I.val = B.val \\ F & \rightarrow & B F_1 \quad F.val = \frac{1}{2}(B.val + F_1.val) \\ F & \rightarrow & B \quad F.val = \frac{1}{2}B.val \\ B & \rightarrow & \mathbf{0} \quad B.val = \mathbf{0}.val \\ B & \rightarrow & \mathbf{1} \quad B.val = \mathbf{1}.val \end{array} $ <p>The value computed by the translation scheme for the input string</p> <p style="text-align: center;">10#011</p> <p>is _____. (Rounded off to three decimal places)</p>

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Q.61

Consider the following table named **Student** in a relational database. The primary key of this table is rollNum.

Student

<u>rollNum</u>	name	gender	marks
1	Naman	M	62
2	Aliya	F	70
3	Aliya	F	80
4	James	M	82
5	Swati	F	65

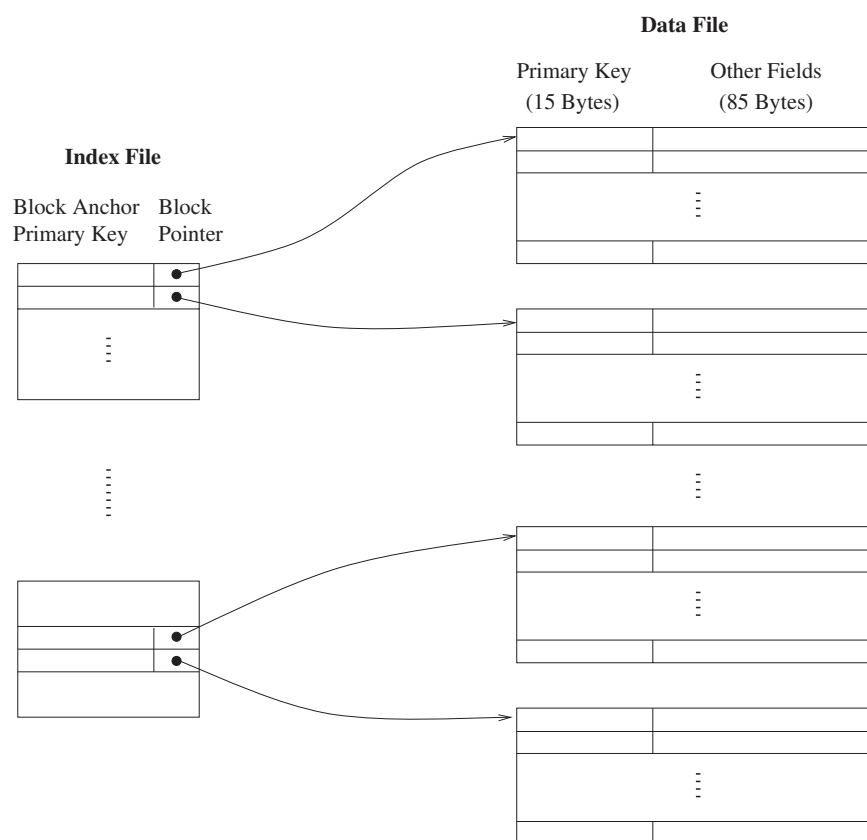
The SQL query below is executed on this database.

```
SELECT *  
FROM Student  
WHERE gender = 'F' AND  
      marks > 65;
```

The number of rows returned by the query is _____.

Q.62

Consider a database of fixed-length records, stored as an ordered file. The database has 25,000 records, with each record being 100 bytes, of which the primary key occupies 15 bytes. The data file is block-aligned in that each data record is fully contained within a block. The database is indexed by a primary index file, which is also stored as a block-aligned ordered file. The figure below depicts this indexing scheme.



Suppose the block size of the file system is 1024 bytes, and a pointer to a block occupies 5 bytes. The system uses binary search on the index file to search for a record with a given key. You may assume that a binary search on an index file of b blocks takes $\lceil \log_2 b \rceil$ block accesses in the worst case.

Given a key, the number of block accesses required to identify the block in the data file that may contain a record with the key, in the worst case, is _____.

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Q.63	<p>Consider the language L over the alphabet $\{0, 1\}$, given below:</p> $L = \{w \in \{0, 1\}^* \mid w \text{ does not contain three or more consecutive 1's}\}.$ <p>The minimum number of states in a Deterministic Finite-State Automaton (DFA) for L is _____.</p>

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Q.64	<p>An 8-way set associative cache of size 64 KB (1 KB = 1024 bytes) is used in a system with 32-bit address. The address is sub-divided into TAG, INDEX, and BLOCK OFFSET.</p> <p>The number of bits in the TAG is _____.</p>

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Q.65	The forwarding table of a router is shown below.																		
	<table><tr><th>Subnet Number</th><th>Subnet Mask</th><th>Interface ID</th></tr><tr><td>200.150.0.0</td><td>255.255.0.0</td><td>1</td></tr><tr><td>200.150.64.0</td><td>255.255.224.0</td><td>2</td></tr><tr><td>200.150.68.0</td><td>255.255.255.0</td><td>3</td></tr><tr><td>200.150.68.64</td><td>255.255.255.224</td><td>4</td></tr><tr><td>Default</td><td></td><td>0</td></tr></table>	Subnet Number	Subnet Mask	Interface ID	200.150.0.0	255.255.0.0	1	200.150.64.0	255.255.224.0	2	200.150.68.0	255.255.255.0	3	200.150.68.64	255.255.255.224	4	Default		0
	Subnet Number	Subnet Mask	Interface ID																
	200.150.0.0	255.255.0.0	1																
	200.150.64.0	255.255.224.0	2																
	200.150.68.0	255.255.255.0	3																
	200.150.68.64	255.255.255.224	4																
Default		0																	
A packet addressed to a destination address 200.150.68.118 arrives at the router. It will be forwarded to the interface with ID _____.																			

Q. No.	Session	Question Type (QT) MCQ/MSQ/NAT	Subject Name (SN)	Key/Range (KY)	Mark (MK)
1	1	MCQ	GA	B	1
2	1	MCQ	GA	MTA	1
3	1	MCQ	GA	A	1
4	1	MCQ	GA	A	1
5	1	MCQ	GA	C	1
6	1	MCQ	GA	D	2
7	1	MCQ	GA	C	2
8	1	MCQ	GA	D	2
9	1	MCQ	GA	B	2
10	1	MCQ	GA	A	2
11	1	MCQ	CS	B	1
12	1	MCQ	CS	B	1
13	1	MCQ	CS	D	1
14	1	MCQ	CS	C	1
15	1	MCQ	CS	A	1
16	1	MCQ	CS	A	1
17	1	MCQ	CS	B	1
18	1	MCQ	CS	B	1
19	1	MCQ	CS	C	1
20	1	MCQ	CS	C	1
21	1	MCQ	CS	B	1
22	1	MSQ	CS	B, C, D	1
23	1	MSQ	CS	C, D	1
24	1	MSQ	CS	A, C, D	1
25	1	MSQ	CS	A, C	1
26	1	MSQ	CS	B, C	1
27	1	MSQ	CS	A, C, D or C, D	1
28	1	MSQ	CS	B, D	1
29	1	MSQ	CS	A, C	1
30	1	NAT	CS	2 to 2 OR 4 to 4	1
31	1	NAT	CS	0 to 0	1
32	1	NAT	CS	110 to 110	1
33	1	NAT	CS	2040 to 2040	1
34	1	NAT	CS	10.2 to 10.2	1
35	1	NAT	CS	7 to 7	1
36	1	MCQ	CS	A	2
37	1	MCQ	CS	D	2
38	1	MCQ	CS	C	2
39	1	MCQ	CS	B	2
40	1	MCQ	CS	A	2
41	1	MCQ	CS	B	2
42	1	MCQ	CS	C	2
43	1	MCQ	CS	A	2
44	1	MCQ	CS	C	2
45	1	MCQ	CS	C	2

46	1	MCQ	CS	B	2
47	1	MCQ	CS	C	2
48	1	MCQ	CS	D	2
49	1	MSQ	CS	B, C, D	2
50	1	MSQ	CS	B, C, D	2
51	1	MSQ	CS	A, D	2
52	1	MSQ	CS	C, D	2
53	1	MSQ	CS	C, D	2
54	1	MSQ	CS	A, D	2
55	1	MSQ	CS	A, B	2
56	1	NAT	CS	5040 to 5040	2
57	1	NAT	CS	4096 to 4096	2
58	1	NAT	CS	5 to 5	2
59	1	NAT	CS	8 to 8	2
60	1	NAT	CS	2.374 to 2.376	2
61	1	NAT	CS	2 to 2	2
62	1	NAT	CS	6 to 6	2
63	1	NAT	CS	4 to 4	2
64	1	NAT	CS	19 to 19	2
65	1	NAT	CS	3 to 3	2

MTA = Marks to All