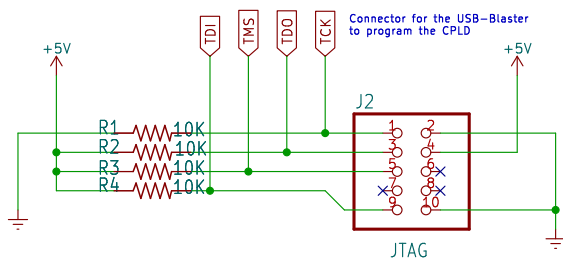
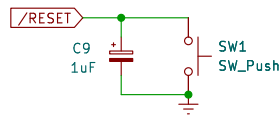
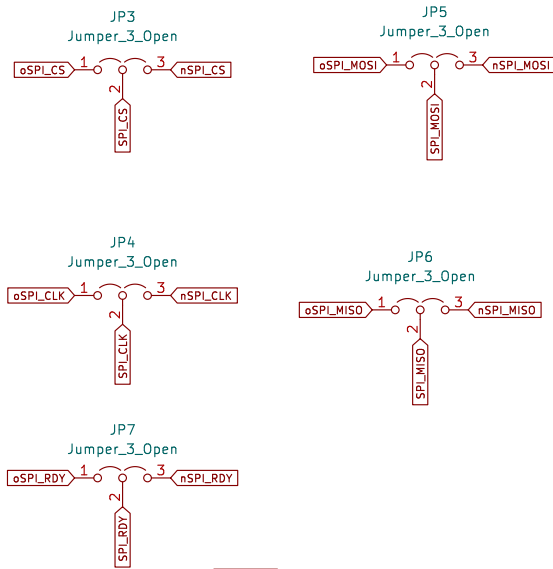
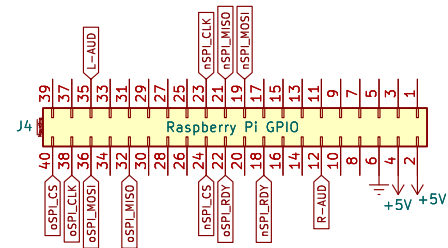
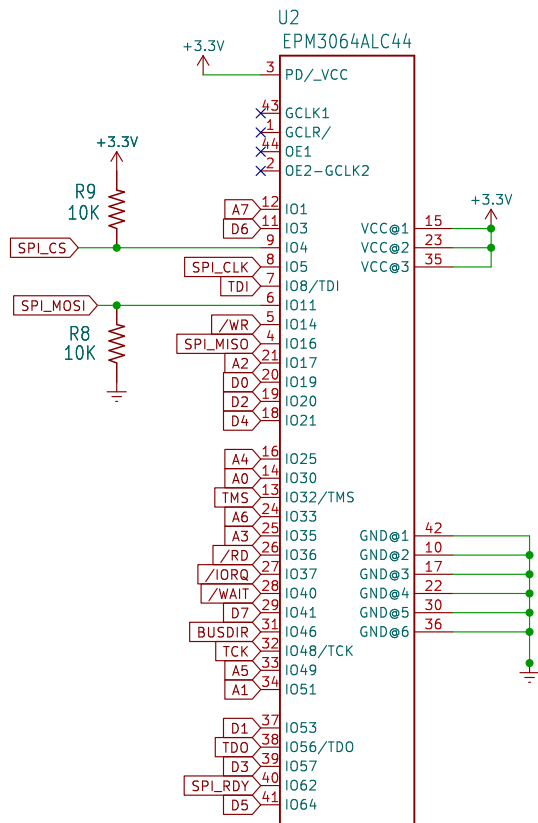
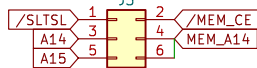


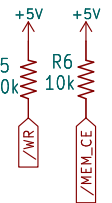
CPLD to decode MSX BUS and serial-parallel conversion between MSX and Raspberry Pi



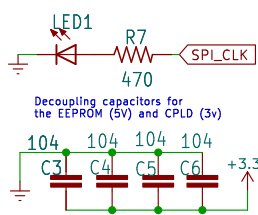
Jumpers to select ROM Bank, and enable the EEPROM for writing. J2 is optional (may be permanently closed with solder) since write protection is controlled via software.



Pullups for the EEPROM Control signals



Flashing Led, driven by the SPLClock signal.



MSXPi Interface V1.2 Rev.0  
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MSXPi interface – connect MSX computer to Raspberry Pi  
EEPROM AT28C256 re-writable from MSX-DOS  
EEPROM software: [https://github.com/costarc/msxcart\\_flash32k](https://github.com/costarc/msxcart_flash32k)

2025-09-07