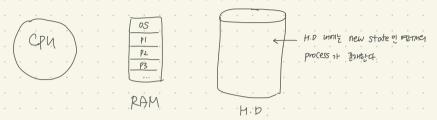
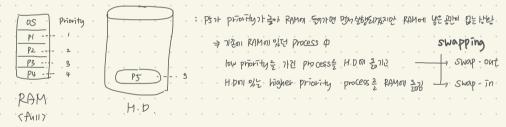
Types of scheduler, Context switching

every process has a process control black & attributes



- 1. Which I how many processors in hard disk should be moved into the RAM? by Long term scheduler
- 2. which processor in RAM should access the CPU first? by Short term scheduler (= scheduler
- 3 which thow many processors in RAM should be moved back into the H.D? by Medium term schedulet



· Context switching

CPUnin 性短过 process皆中語 prioritye processi RAMINI % 四

(2 हा केपर अपना स्पाप गाम process रिक्षितिस भाग (context मिर्ड गमार) मेरा प्रा रिक्षितिहर्दे)

Various times of a process

- 1. Arrival time: time when process has arrived RAM
- 2. Burst time (= execution time)
- 3. Completion time: time when process has completed execution & can be removed from RAM
- 4. Turn around time : time between arrival time to completion time
- 5. Waiting time : waiting time for execution or I/O (process is ide at this moment)
 - O completion time arrival time
 - burst time + waiting time + I/o time
- 6. Response time : 4501 07 7
- 7. Ilo time : amount of time spent in reading 4 writing I/o
 - ex) Various times for process 1

 IAM Pl P2 P1 P3 P1 2AN

 orrival burst time complete

 time waiting time time

point in time

arrival time, completion time

duration in time

burst time, turn-around time

waiting time, response time

Types of scheduling algorithms

CPU scheduling algorithms

- Preemtive scheduling algorithms : preempt the process

Non - preemitive scheduling algorithms: CPUIDIN Process = execute the Fig ton, RAMINI higher priority

CPU scheduling algorithms are applied only to processes which are in ready state

Process which are in I/o state will be "blocked" and will not be considered by scheduling algorithms.

I/O state = block state

PI - running state / pz.ps - ready state / p4 - I/O state 275

P49 priority7+ P2, P3 24 是GREE P1 Yayon 圣从 F1 P2, P3 多明, priority7+ 器, process7+ 经的处码

SJF scheduling algorithm

Shortest Job First scheduling algorithm

least burst time : Among the arrived process, process with the

preemptive scheduling & priority based algorithm

process Id Q) Find the average TAT, WT, Through put arrival time

burst time

arrive of process = + WT (TAT-BT) 0

L in this case we don't assume I/o time

· Schedule length = CT of last process - AT of first process

Throughput: number of processed executed in unit time = Number of processes / schedule length

Q) Find the average TAT, WT, Through put

= 5/11 = Processes/unit time

process Id arrival time burst time

through put

SRTF Scheduling algorithm

Shortest Remaining time first scheduling algorithm : preemptive version of SJF

· olol process of Ista follows는 Mist (RAMON 와는) processed 상kyon 평망는 축 Hotol team process nim 나는 Hoty 다 쌓다면, Obn 상ky 단, 오른 processor ready gwewe on 있다면 STFU 앞 바닷크 사상

- () Find the average TAT , WT , Through put and schedule length (注 N76) 乳睫 钙 识的 arrive 記 蛇 虫状炎)
- S) P1 P2 P3 P4 P3 P6 P5 P2 P1
- A). CT 19 B 6 4 9 7 < last completed process 5 to 71154 to 3ch.
 - $TAT (CT-AT) | 19 | 12 | 4 | 1 | 5 | 2 | \rightarrow 0 \text{ wg} = 45/6$ WT (TAT-BT) | 12 | 7 | 1 | 0 | 3 | 1

L in this case we don't assume I/o time

. Schedule length = CT of last process - AT of first process = 19

Throughput: number of processed executed in unit time = Number of processes / schedule length

= 6/19 = Processes/unit time

* Response time waiting time of certain process until it gets to the CPU for the first time

process Id	1	2	3	Ψ.
arrival time	0	15.	30 .	45
burst time	20	ZS .	10	15

SJF	· P1	· P2	P3	- 174	-		
	0 - 2						
SRTF	PIPI	P2 P3	P2 P2	· PÝ	•		
	D IL						

SJF RT | D 5 15 10 SRTF RT | O 5 0 10

In any non-preemptive scheduling algorithm. Response time = waiting time,
but it may not be true in preemptive scheduling algorithm.

Problem:

Consider the following CPU processes with arrival times (in milliseconds) and length of CPU bursts (in milliseconds) as given below:

If the pre-emptive shortest remaining time first scheduling algorithm is used to schedule the processes, then the average waiting time across all processes is _3_ milliseconds.

An operating system uses shortest remaining time first scheduling algorithm for pre-emptive scheduling of processes. Consider the following set of processes with their arrival times and CPU burst times (in milliseconds):

							P	. 1 .	1_		00	74
Process	Arrival Time	Burst Time	CT	TAT	WT	RT	P	1 1/2	P2	13	13	fT.
P1	0	12 10 0	27	27	15	0	0	2	3	6 8	3 12	2 (-
P2	2	4 30	6	4	D	0						
P3	3	6° 40	12	9	3	3						
P4	8	5 v	12	9	4	4						

FCFS scheduling algorithm

First Come First Served Scheduling Algorithm

The process which has the least arrival time will be scheduled first

non-preemptive # not priority based (not depending on a property of a process a burst time)

process Id	1	2	3	Ÿ	Ţ		PI	P2	1 83	P	4	Ps	1
arrival time													
burst time	4	3.	ŀ	2	5		٠						
. ÇT	.4	7.	8	ĺ	15		٠						

TAT 4 6 6 7 11

WT 0 3 5 5 6] To non-preemptive scheduling, WT-R

Context Switching: preempt the process and schedule some other process

Hist context switching in Min 2013 => decreases the efficiency of CPU = Slower [CPU efficiency = Useful ti

process Id | 1 2 3 4 3 6 Context switching : CPU is idle

arrival time | 0 | 1 2 3 4 5

burst time | 3 2 | 4 5 2 0 | 4 5 7 8 9 10 | 14 15 20 21 23

CPU efficiency = 17/23

CPU inefficiency = 16/2