
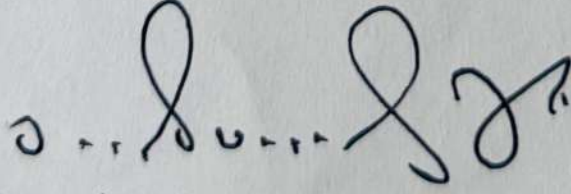


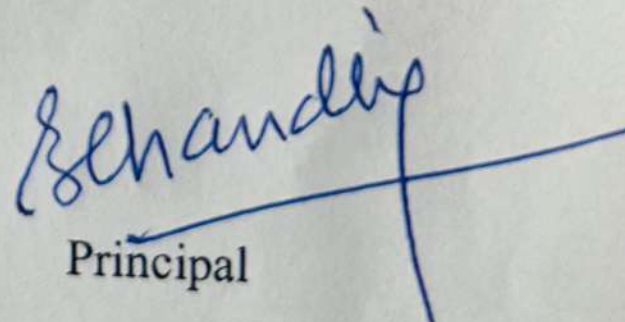


Sardar Patel Institute of Technology
Bhavan's Campus, Munshi Nagar, Andheri (West), Mumbai-400058-India
(Autonomous Institute Affiliated to University of Mumbai)
**Board of Studies (BoS) of Department of Electronics and
Telecommunication Engineering**
Year 2024-25

Sr. No.	Designation	Name
1	Chairman (Head of the Department)	Dr. Reena S. Sonkusare (Asso. Prof.)
2	Faculty as per Specialization	Dr. Satyanarayana Bheesette Dr. Y. S. Rao (Professor) Dr. Rajendra Sawant (Professor) Dr. Sukanya Kulkarni (Asso. Prof.) Dr. Rajendra G. Sutar (Asso. Prof.) Dr. Deepak C. Karia (Asso. Prof.) Dr. K. T. Talele (Asso. Prof.) Dr. Narendra A. Bhagat (Asso. Prof.)
3	Two subject experts from outside the Parent University to be nominated by the Academic Council.	Dr. Prasanna Chaporkar (IIT Bombay) Dr. Kumar Appaiah (IIT Bombay)
4	One expert nominated by the vice-chancellor	Dr. Nirmal Jagannath (KJSCE)
5	One representative from industry/corporate sector/allied area relating to placement	Mr. Ranjeeth C.V.
6	One graduate meritorious alumnus	Mr. Sagar Sawant
7	Experts from outside the college whenever special courses of studies are to be formulated	Dr. S. S. Mande (DBIT Kurla) Dr. Ashwin G Kothari (VNIT) Dr. A. S. Gandhi (VNIT)
8	Other members of staff of the same faculty	Prof. Manish Parmar (Asso. Prof.) Dr. A D Mane (Asst. Prof.) Dr. Amol Deshpande (Asst. Prof.) Prof. Milind Paraye (Asst. Prof.) Prof. Pallavi Malame (Asst. Prof.) Prof. Sneha Weakey (Asst. Prof.) Dr. Prashant Kasambe (Asst. Prof.) Prof. Govind T. Haldankar (Asst. Prof.) Dr. Payal Shah (Asst. Prof.) Dr. Manisha Bansode (Asst. Prof.) Prof. Priya Deshpande (Asst. Prof.) Prof. Najib Ghatte (Asst. Prof.)
9	Current BE Students	Kshiteej Kakirde Malay Phadke


HOD EXTC


Dean Academics R&D


Principal



Dharmatya Vidya Dharmatya
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Munshi Nagar, Andheri (W), Mumbai – 400 058.
(Empowered Autonomous Institute Affiliated to University of Mumbai)

Department of Electronics and Telecommunication

Minutes of the Meeting

Attending Members:

Sr. No.	Faculty Name	Sr. No.	Faculty Name
1	Dr. Kumar Appaiah, IITB	17	Dr. Prashant Kasambe
2	Dr. Sudhakar Mande, DBIT	18	Prof. Payal Shah
3	Dr. Abhay Gandhi, VNIT Nagpur	19	Prof. Priya Deshpande
4	Dr. Ashwin Kothari, VNIT Nagpur	20	Prof. Govind Haldankar
5	Dr. Bhalchandra Chaudhari	21	Prof. Manisha Bansode
6	Dr. Y. S. Rao	22	Dr. Deepak Karia
7	Dr. R. R. Sawant	23	Dr. Rajendra Sutar
8	Dr. Reena Sonkusare	24	Dr. Santoshkumar Sabat
9	Dr. Sukanya Kulkarni	25	Dr. Dattatray Sawant, MPSTME
10	Prof. Manish Parmar	26	Dr. Dhananjay Kalbande
11	Prof. Milind Paraye	27	Prof. Najib Ghatte
12	Dr. Amol Deshpande	28	Dr. Prasenjit Bhavtankar
13	Prof. Pallavi Nair	29	Shrutiman Mukherjee, Student
14	Dr. Kiran Talele	30	Kunal Thakur, Student
15	Dr. Anand Mane	31	Yug Harlaka, Student
16	Dr. Narendra Bhagat		



Department of Electronics and Telecommunication

Department of Electronics and Telecommunication			
Meeting Name:		Board of Studies/ DAB Meeting	
Date of Meeting:		16/05/2024	Time: 11pm - 1pm
Place:		Board room	
Attendees:		As mentioned below	
Minutes Prepared by:		Santoshkumar Sabat	
Prof. (Dr.) Reena, the Head of the Electronics and Telecommunication Dept., greeted all external experts and other attendees and delivered introductory remarks, underscoring the significance of the meeting and its agenda.			
Meeting Agenda		Discussion	
Confirmation of minutes of the BOS meeting held on 19th May 2023		1. Everyone approved the minutes of the Board of Studies (BOS) meeting held on 19th May 2023	
Approval for EXTC SPIT Curriculum with NEP Recommendations (w.e.f. 2023 -24)		There were several important discussions and considerations brought up during the presentation of the Curriculum Roadmap for the Undergraduate (UG) academic program by Dr. Reena Sonkusare to the BOS members. Discussion: 1. Academic Diversity and Credit Allocation: Dr. Reena Sonkusare presented the roadmap, which likely included plans for academic diversity and the allocation of credits across different verticals within the UG Electronics and Telecommunication Engineering (EXTC) program at S. P. I. T. 2. Concerns about Credit Assignment: Dr. Ashwin Kothari raised concerns about the assignment of credits, particularly regarding the feasibility of providing mandatory industry internships to all students simultaneously. It was suggested to split internships between semesters VII and VIII to manage this. 3. Mapping NPTEL Marks to Grading: Dr. Ashwin Kothari also questioned how the institute plans to map NPTEL/online course marks to its own grading system, emphasizing the importance of having an institute-specific approach to evaluating online courses. 4. Faculty Ratio for Multiple Verticals: Dr. Sudhakar Mande expressed concern about maintaining the faculty ratio for the various verticals within the program. 5. Example of Pass/Fail for External Exams (NPTEL): Dr. Kumar Appaih provided an example from KJ Somaiya to illustrate the practice of treating external exam results (such as NPTEL) separately from the CGPA.	



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Department of Electronics and Telecommunication

	<p>6. Inclusion of Liberal and Humanities Courses: The Principal mentioned the inclusion of liberal arts and humanities courses, agreeing to incorporate suggestions from Dr. Ashwin Kothari.</p> <p>7. Consideration for Honors and Minors: Dr. Ashwin Kothari emphasized the need for clarity regarding credit assignments for Honors and Minors, as well as the importance of determining whether the program will focus on research or non-research tracks.</p> <p>8. Overall, Ashwin sir's suggestion highlights the importance of incorporating LTP components into the curriculum and designing it in a way that caters to the diverse academic interests and goals of students.</p> <p>Resolution: After lot of discussion and deliberation in the department meeting with the Principal, it was decided to offer only two degrees (“B. Tech in Engg. /Tech. with Multidisciplinary Minor” and “B. Tech in Engg. /Tech. with Multidisciplinary Minor” and Honors by Research”). The suggestions made by Dr. Ashwin Kothari would be included in the finalization of the scheme, including considerations for liberal and humanities courses, credit assignments, and the structure of the program.</p>
Approval for Second Year (Sem III & Sem IV) Courses Syllabus	<p>Presentation of Proposed Syllabus: The HoD presented the proposed syllabus for Semester III and IV to the BoS members.</p> <p>Discussion:</p> <p>1. Dr. Ashwin Kothari sir's points:</p> <ul style="list-style-type: none">○ Coverage of GATE Syllabus: Ashwin Sir emphasized the importance of covering the GATE syllabus to ensure that students are well-prepared for the exam and are competitive in the core group of the syllabus.○ Alignment with IT Industry Requirements: Ashwin Sir stressed the need for the syllabus to incorporate topics relevant to the IT industry, such as Python, Data Structures, and Algorithms. <p>2. Principal's Response:</p> <ul style="list-style-type: none">○ Programming and DSA Courses: The Principal mentioned that the syllabus already includes Programming I, II, and III courses in each semester, along with lab courses. Data Structures and Algorithms (DSA) are also included, and Data Science will be offered as an elective. <p>3. Dr. Ashwin sir's Concern about Self-Study Credits: Ashwin Sir highlighted the issue of considering self-study as part of</p>



Department of Electronics and Telecommunication

	<p>honors, expressing concerns about credit repetition and transcript justification.</p> <p>4. Principal's Proposal on Research Internships: The Principal suggested shifting research internships to Semester VII and VIII, as a partial lot of students are already opting for research internships in Semester VI.</p> <p>Resolution:</p> <ol style="list-style-type: none"> 1. Preference for Honors through Research: The Principal indicated a preference for awarding honors through research, following the chosen sequel of program electives. 2. Approval of Semester III and IV Syllabus: The BoS members approved the syllabus for Semester III and IV. <p>This meeting resulted in the approval of the syllabus for Semester III and IV, with considerations made to ensure alignment with GATE syllabus, IT industry requirements, and the inclusion of research internships for honors.</p>
Approval for Multidisciplinary Minor (MDM) Courses Syllabus	<p>Discussion:</p> <ol style="list-style-type: none"> 1. Dr. Sudhakar Mande raised a concern about the Bloom's level indicated in the syllabus, questioning whether it is the limit to which faculty members are expected to adhere. He suggested that faculty should have the flexibility to teach up to their desired Bloom's level. <p>Resolution:</p> <ol style="list-style-type: none"> 1. Principal's Response: The Principal addressed Dr. Sudhakar Mande's concern by stating that faculty members have the autonomy to teach up to any Bloom's level during their course, depending on the time availability and other factors. 2. Approval of MDM Syllabus: The BOS members approved the syllabus for the MDM courses. <p>This meeting resulted in the approval of the MDM syllabus, with clarification provided regarding faculty autonomy in determining the Bloom's level during their courses.</p>
Approval for Revised Vision, Mission, Program Educational Objectives (PEO), and Program Specific	<p>Discussion:</p> <ol style="list-style-type: none"> 1. Presentation of Proposed Vision and Mission: The Principal suggested a new vision statement: "To graduate professionally



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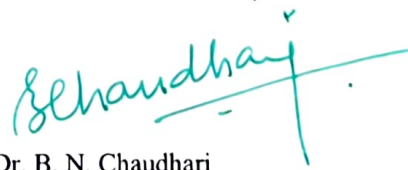
Department of Electronics and Telecommunication

Outcomes (PSO) of the EXTC Department	<p>competent electronics engineers with research aptitude and social sensitivity."</p> <p>2. Feedback from Dr. Ashwin Sir:</p> <ul style="list-style-type: none">○ Ashwin Sir raised a concern about the inclusion of time-specific components in the mission statement, suggesting that it should align with the long-term goals of the department and incorporate the purpose of the National Education Policy (NEP) instead.○ PSO1 and Department Strengths: Ashwin Sir questioned whether the specific domains mentioned in PSO1 accurately reflect the strengths of the department. This suggests a concern about ensuring that the PSOs are closely aligned with the department's expertise and focus areas.○ Justifiability in Rubrics: Ashwin Sir questioned whether PSO2 can be effectively justified in rubrics, suggesting a need for clarity and specificity in defining outcomes and assessment criteria to ensure meaningful evaluation of student achievement. <p>Resolution:</p> <ol style="list-style-type: none">1. The new vision statement was finalized to incorporate the philosophy of NEP, addressing Ashwin Sir's concern.2. The Principal agreed with Ashwin Sir's feedback on PSOs and assured that they would be revisited, taking into account the department's strengths, weaknesses, opportunities, and threats (SWOT analysis).3. The presented Vision and Mission, incorporating the suggestions, were approved by the Board of Studies (BOS).
Any other point of discussion with the permission of the chairman.	Dr. Ashwin Sir suggested to rename "Smart Goals" to "Short Term Goals" to be clear and specific in terminology.

The official conclusion of the meeting occurred at 1 pm. The Head of the Department extended appreciation to all external experts and other participants for their valuable contributions, insights, and active engagement throughout the session.


Dr. Reena Sonkusare
HoD EXTC


Dr. Y. S. Rao
Dean Academics


Dr. B. N. Chaudhari
Principal



Bhartiya Vidya Bhavan's

SARDAR PATEL INSTITUTE OF TECHNOLOGY

Munshi Nagar, Andheri(W), Mumbai.

Electronics and Telecommunication Department

Minutes of the Meeting

Meeting Name:	Board of Studies Meeting		
Date of Meeting:	23 rd Dec, 2024	Time:	11.00 a.m. – 12.00 p.m.
Online Meet Link	https://meet.google.com/cfh-pgec-tpb		
Attendees:	As per given table		
Minutes Prepared by:	Sneha Weakey		
Prof. (Dr.) Reena, the Head of the Electronics and Telecommunication Dept., welcomed all external experts and other attendees and delivered introductory remarks, underscoring the significance of the meeting and its agenda.			
Meeting Agenda	Discussion		
1. Confirmation of minutes of the BOS meeting held on 16th May 2024.	<ul style="list-style-type: none">Experts approved the minutes of the Board of Studies (BOS) meeting held on 16th May 2024.		
2. Approval (Ratification) for Minor corrections in NEP curriculum	<p>Discussion:</p> <ol style="list-style-type: none">Addition of 1 credit to HDL course (Sem III): NEP curriculum was initiated in 2024-25. After completion of ODD semester for AY 2024-25 it has been observed that Number of hours allotted for HDL programming lab are less and faculty had to take extra online lectures for the same. Dr. Reena has proposed inclusion of 1 tutorial session for HDL.Concern in number of teaching hours: Dr. Satyanarayana has suggested keeping 3-L and 1-P for HDL considering its importance in Electronics and Telecommunication domain. <p>Resolution: Inclusion of 1 tutorial to HDL course was accepted and approved by BoS members.</p>		
3. Approval (Ratification) for Minor corrections in Syllabus.	<p>Discussion:</p> <ol style="list-style-type: none">Changes in COs of few courses: After external audit, changes in COs of few courses were suggested. Auditor had given their inputs to align the COs with Bloom's Taxonomy. Modified COs of BEE & EDC courses were proposed to the BoS members.Changes in self-study components: After external audit, changes in self-study component of subject IC Technology were suggested by auditors. Modified components were proposed to BoS members. <p>Resolution: After receiving clear justification, modified COs and changed self-study components were accepted and approved by BoS Members.</p>		
4. Approval for Multidisciplinary Minor Degree Programs.	There were several important discussions and considerations brought up during the presentation of MDM syllabus for the Undergraduate (UG) academic program by Dr. Reena Sonkusare to the BOS members.		
	Discussion:		



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Electronics and Telecommunication Department

	<p>1. Time Triggered Systems: Dr. Reena presented the syllabus for Time Triggered Systems. This MDM will be offered to Computer Engineering students, syllabus is designed by Skill Universe for Sem. IV, V, VI and VII with 3, 4, 3 and 4 credits respectively. Prof. Priya provided the proper clarification and insights of the content of the course.</p> <ul style="list-style-type: none">○ Comments from Dr. Satyanarayana: Syllabus seems to be mixed, and match and it needs fine tuning. What is the idea behind introducing this course to Computer Engineering students?○ Comments from Dr. Ranjit: Revision of the syllabus is required. <p>2. Foundation of UI/UX Design: Dr. Reena presented the syllabus for foundation of UI/UX design. The syllabus of this MDM is designed by Pearl Academy which will be offered to students of all the departments.</p> <ul style="list-style-type: none">○ Comments from Dr. Satyanarayana: Syllabus should be skill centric and practical oriented. Before finalizing content of course and number of hours allotted for lectures and laboratory sessions should be discussed and deliberated at institute level. <p>Resolution: Dr. Reena will inform Dean Academics Dr. Y. S. Rao to discuss this with industry person and institute faculty before offering MDM courses to students</p>
5. Any other point of discussion with the permission of the chairman.	<p>Suggestions from Dr. Sawant:</p> <ol style="list-style-type: none">1. Topic 'Dependent Sources' should be added in syllabus of BEE instead of self-study.2. For EWE, as syllabus is aligned with GATE, tutorial should be added instead of lab session. <p>Suggestions from Dr. Nirmal:</p> <ol style="list-style-type: none">1. For EWE put theory for 3 credits and tutorial for 1 credit as it is impossible to cover the depth of subject in 28 hours. (Statement was supported by Dr. Inderkumar Kochar)2. Make the correction in DSP track and MDM course as both the names are same. Can put Discrete signal processing instead. <p>Suggestions from Dr. Amol:</p> <ol style="list-style-type: none">1. Topic No. 3.5 from EWE (Sem III) can be removed and put in the higher semester syllabus. <p>Resolution:</p> <ol style="list-style-type: none">1. 'Dependent Sources' will be added to BEE syllabus for upcoming batches of 2025-26.2. Sem III is having limitation of 23 credits, adding more credits will put a burden on students and semester will become heavy. Hence allotment of 1 credit to EWE is kept on hold for now.



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	3. Topic no 3.5 of EWE will be included in subject of higher semester.
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List of Attendees

First name	Last name	Email	Duration	Time joined	Time exited
RCV		renj*****@***.com	1 hr 7 min	10:58 AM	12:05 PM
Kumar	Appaiah	a.ku***@***.in	1 hr 7 min	10:58 AM	12:05 PM
Manisha	Bansode	manisha_bansode@spit.ac.in	1 hr 8 min	11:09 AM	12:17 PM
Satyanarayana	Bheesette	bhee*****@***.com	1 hr 6 min	10:59 AM	12:05 PM
Pankaj	Binda	pankaj.binda@spit.ac.in	1 hr 7 min	10:59 AM	12:07 PM
Dr. Amol	Deshpande	amol_deshpande@spit.ac.in	1 hr 6 min	11:01 AM	12:07 PM
Priya	Deshpande	priya.chimurkar@spit.ac.in	1 hr 2 min	11:03 AM	12:05 PM
HOD	ETRX	hode***@***.edu	5 min	10:59 AM	12:02 PM
Najib	Ghatte	najib_ghatte@spit.ac.in	1 hr 7 min	10:59 AM	12:07 PM
Kochar	Inderkumar	kochar.inderkumar@spit.ac.in	59 min	11:06 AM	12:06 PM
Deepak	Karia	deepak_karia@spit.ac.in	1 hr 7 min	10:58 AM	12:06 PM
Prashant	Kasambe	prashant_kasambe@spit.ac.in	1 hr 4 min	11:03 AM	12:07 PM
Reena	Kumbhare	reena_kumbhare@spit.ac.in	1 hr 17 min	10:50 AM	12:07 PM
Anand	Mane	anand_mane@spit.ac.in	1 hr 12 min	10:55 AM	12:07 PM
JAGANNATH	NIRMAL	jhni****@***.edu	56 min	11:05 AM	12:07 PM
Milind	Paraye	milind_paraye@spit.ac.in	1 hr 6 min	11:01 AM	12:07 PM
Manish	Parmar	manish_parmar@spit.ac.in	1 hr 6 min	10:59 AM	12:06 PM
Mrinmayi	Patil	mrinmayi.patil@spit.ac.in	50 min	11:04 AM	11:54 AM
Prof. Santoshkumar	Sabat	santoshkumar_sabat@spit.ac.in	1 hr 8 min	11:00 AM	12:07 PM
Dattatray	Sawant	dssa*****@***.com	11 min	11:24 AM	11:35 AM
Rajendra	Sawant	rajendra.sawant@spit.ac.in	57 min	11:03 AM	12:05 PM
Payal	Shah	payal_shah@spit.ac.in	36 min	11:31 AM	12:07 PM
Shanti	Swamy	shanti.swamy@spit.ac.in	1 hr 6 min	11:00 AM	12:07 PM
Sneha	Weakey	sneha_15weakey@spit.ac.in	1 hr 11 min	10:56 AM	12:07 PM



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Electronics and Telecommunication Department

Reena Kumbhare (Presenting, annotating)

	Thermal Sensors, Thermal Imager, Photo Heads, and DMD		
5	5.1 Memory: SRAM, DRAM, MRAM, Flash Working Principle, structures and fabrication steps of one-time programmable structures	4	04
	5.2 Display: AMOLED/OLED Working Principle, structures, fabrication steps	5	
6	Self-Study: Integrated circuit packages, package reliability, die size & wafer bonding, dicing, and packaging of MEMS devices; Characterization of IC using Automated Test Equipment (ATE); Characterization of MEMS devices for surface and Resonant frequency, TCR	1.3	06*
		Total (Not Included)	28

ISE Evaluation:

- 1) Fault Identification and correction of a given CMOS circuit Layout: Group Activity within Laboratory Batch (Evaluation during laboratory session, CO3 CO4 (5 Marks))
- 2) Case Study of MEMS device fabrication and characterization: Group Activity within Laboratory Batch (Evaluation during laboratory session, CO3 CO4 (5 Marks))
- 3) Visit to CEN Lab, IIT Bombay and Report on visit (CO1 CO5) (10 Marks)

Text Books:

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More options

Reena Kumbhare (Presenting, annotating)

4.2	Microstructure: working and some quantitative relationship in various devices like Camflect, Microsensors, Accelerometers and Pressure Sensors		
4.2	Materials (eg. Si, SiO ₂ , SiN, Si ₃ N ₄ , PMMA): Important properties: Young modulus, Poisson's ratio, density, piezoresistive coefficient, TCR, Thermal Conductivity, Thermal Expansion	3	
	Understanding steps involved and materials used in Fabricating MEMS: Camflects, Microsensors, Accelerometers, Pressure Sensors, Thermal Imager Photo Heads, and DMD	3	
5	5.1 Memory: SRAM, DRAM, MRAM, Flash Working Principle, structures and fabrication steps of one-time programmable structures	4	04
	5.2 Display: AMOLED/OLED Working Principle, structures, fabrication steps	5	
6	Self-Study: Integrated circuit packages, package reliability, die size & wafer bonding, dicing, and packaging of MEMS devices; Characterization of IC using Automated Test Equipment (ATE); Characterization of MEMS devices for surface and Resonant frequency, TCR	1.3	06*
		Total (Not Included)	28

ISE Evaluation:

- 1) Fault Identification and correction of a given CMOS circuit Layout: Group Activity within Laboratory Batch (Evaluation during laboratory session, CO3 CO4 (5 Marks))

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