

CONFIDENTIAL

REVISION HISTORY		CPLD CONTROL TOOL SPECIFICATION	
Rev.	Date	Description	
		Page/Article	Summary
1.0	Sep 02, 2020	—	New creation
1.3	Sep 25, 2020	1.4	Remove half-powered USB restriction, already implemented a work-around for it Add udev rule part
		4.3.1	Libconfuse: add method to fix issue config.guess
		5	Update display sample of version 1.3
1.4	Oct 30, 2020	1.4	Add notice about the CPLD firmware version for each board
		4.1	Remove file CPLD_register.txt in folder structure (this info is now in source code)
		4.3.2	Remove the restriction of the stored place of CPLD_register.txt file.
1.5	Dec 02, 2020	—	Add board V3M Starter Kit
		3.6	Add command CPLD write non-volatile
		5	Add new sample log
1.6	July 16, 2021	—	Modify the protocol for V3MSK to SMI
1.7	Aug 04, 2020	—	Add board S4 Spider