Task Name	Start	Finish	Duration	Jan 2005		Feb 2005			
				1/16 1/23	1/30	2/6	2/13	2/20	2/27
1: Interface Circuitry	1/10/2005	2/22/2005	32d					V	
1.1: Design Circuitry	1/10/2005	1/27/2005	14d						
1.2: Purchase Components	1/28/2005	2/10/2005	10d	▶ ■					
1.3: Construct & Test Circuits	2/11/2005	2/22/2005	8d			V		T	
1.3.1: Current Driver Circuitry	2/11/2005	2/14/2005	2d			>	Ь		
1.3.2: Level Offset & Gain Circuitry	2/11/2005	2/15/2005	3d			-	<u>_</u>		
1.3.3: Integrate Components	2/16/2005	2/22/2005	5d			l	>		
2: LED & Driver Circuitry	1/10/2005	2/9/2005	23d			_			
2.1 Research A/D Converters	1/10/2005	1/10/2005	1d						
2.2 Complete Hardware Design	1/11/2005	1/19/2005	7d						
2.3 Purchase LED & Driver Components	1/20/2005	2/2/2005	10d	-					
2.4: Construct & Test	2/3/2005	2/9/2005	5d		+				
3: System Integration & Test	2/23/2005	3/3/2005	7d					-	