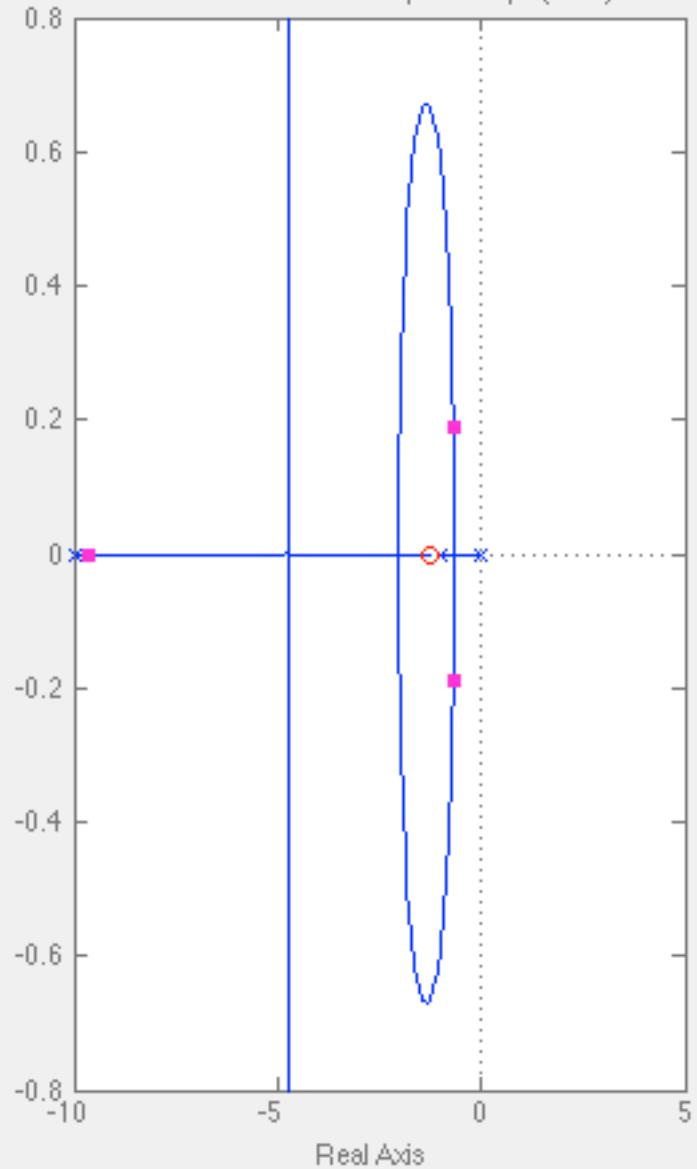


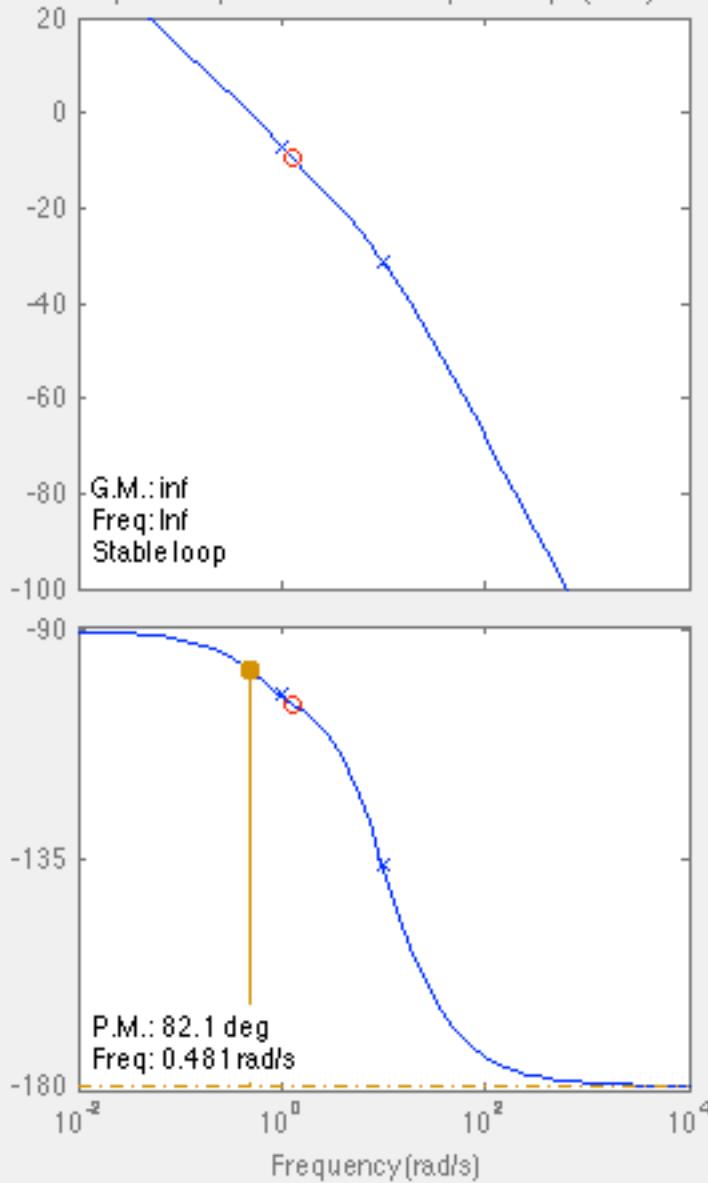
SISO Design for SISO Design Task (3)



Root Locus Editor for Open Loop 1(OL1)



Open-Loop Bode Editor for Open Loop 1(OL1)



Added real zero to C(s) at $s = -1.29$