CONNECTOR

CONNECTOR

+1.5V
VCCIO
+3.3V
VCC5V0

TOP_VCC1V5
TOP_VADJ
TOP_VCC3V3
TOP_VCC5V0

XADC_VP
XADC_VN
B13_LP[14:1]
B13_LN[14:1]
B15_IO0
B15_IO25
B15_LP[24:1]
B15_LN[24:1]
B16_IO0
B16_IO25
B16_LP[24:1]
B16_LN[24:1]
B34_L25
B34_LP[24:18]
B34_LN[24:18]
MGT_CLKP1
MGT_CLKN1
MGT_TXP[3:0]
MGT_TXN[3:0]
MGT_RXP[3:0]
MGT_RXN[3:0]

TOP_XADC_VP
TOP_XADC_VN
TOP_B13_LP[14:1]
TOP_B13_LN[14:1]
TOP_B15_IO0
TOP_B15_IO25
TOP_B15_LP[24:1]
TOP_B15_LN[24:1]
TOP_B16_IO0
TOP_B16_IO25
TOP_B16_LP[24:1]
TOP_B16_LN[24:1]
TOP_B34_L25
TOP_B34_LP[24:18]
TOP_B34_LN[24:18]
TOP_MGT_CLKP1
TOP_MGT_CLKN1
TOP_MGT_TXP[3:0]
TOP_MGT_TXN[3:0]
TOP_MGT_RXP[3:0]
TOP_MGT_RXN[3:0]

XC7A75T

XC7A75T

Clock

TOP_VCC2V5
TOP_VCC3V3

VCC2V5
VCC3V3

Clock

MGT_CLKP0
MGT_CLKN0
SYS_CLK_P
SYS_CLK_N
GCLK
RTC_CLK

TOP_MGT_CLKP0
TOP_MGT_CLKN0
TOP_SYS_CLK_P
TOP_SYS_CLK_N
TOP_GCLK
TOP_RTC_CLK

MGT_CLKP0
MGT_CLKN0
SYS_CLK_P
SYS_CLK_N
GCLK
RTC_CLK

Clock

JTAG

FPGA_TCK
FPGA_TDI
FPGA_TDO
FPGA_TMS

FPGA_TCK
FPGA_TDI
FPGA_TDO
FPGA_TMS

JTAG

USER INTERACTIVE

MCU_GPIO[31:0]
FPGA_IO[5:0]
PMU_VDDPADEN
PMU_PADRST
MCU_TMS
MCU_TCK
MCU_TDI
MCU_TDO
MCU_WAKE
MCU_RESET
SPI_SCLK
SPI_MOSI
SPI_CSn
SPI_MISO

TOP_MCU_GPIO[31:0]
TOP_FPGA_IO[5:0]
TOP_PMU_VDDPADEN
TOP_PMU_PADRST
TOP_MCU_TMS
TOP_MCU_TCK
TOP_MCU_TDI
TOP_MCU_TDO
TOP_MCU_WAKE
TOP_MCU_RESET
TOP_SPI_SCLK
TOP_SPI_MOSI
TOP_SPI_CSn
TOP_SPI_MISO

MCU_GPIO[31:0]
FPGA_IO[5:0]
PMU_VDDPADEN
PMU_PADRST
MCU_TMS
MCU_TCK
MCU_TDI
MCU_TDO
MCU_WAKE
MCU_RESET
SPI_SCLK
SPI_MOSI
SPI_CSn
SPI_MISO

USER INTERACTIVE

+3.3V
PS_POR_B

TOP_VCC3V3
TOP_PS_POR_B

USER INTERACTIVE

+1.0V
+1.5V
+1.8V
VCCIO
+3.3V
MGTAVCC
MGTAVTT
VTTREF

TOP_VCC1V0
TOP_VCC1V5
TOP_VCC1V8
TOP_VADJ
TOP_VCC3V3
TOP_MGTAVCC
TOP_MGTAVTT
TOP_DDRVREF

XC7A75T

DDR3_CLKP0
DDR3_CLKN0
DDR3_CKE0
DDR3_ODT
DDR3_RESET
DDR3_WE
DDR3_CS
DDR3_CAS
DDR3_RAS
DDR3_DQS_P[3:0]
DDR3_DQS_N[3:0]
DDR3_BA[2:0]
DDR3_DM[3:0]
DDR3_A[14:0]
DDR3_D[31:0]

TOP_DDR3_CLKP0
TOP_DDR3_CLKN0
TOP_DDR3_CKE1
TOP_DDR3_ODT
TOP_DDR3_RESET
TOP_DDR3_WE
TOP_DDR3_CS
TOP_DDR3_CAS
TOP_DDR3_RAS
TOP_DDR3_DQS_P[3:0]
TOP_DDR3_DQS_N[3:0]
TOP_DDR3_BA[2:0]
TOP_DDR3_DM[3:0]
TOP_DDR3_A[14:0]
TOP_DDR3_D[31:0]

DDR3_CKP
DDR3_CKN
DDR3_CKE
DDR3_ODT
DDR3_RESET
DDR3_WE
DDR3_CS
DDR3_CAS
DDR3_RAS
DDR3_DQS_P[3:0]
DDR3_DQS_N[3:0]
DDR3_BA[2:0]
DDR3_DM[3:0]
DDR3_A[14:0]
DDR3_D[31:0]

POWER

TOP_VCC5V0
TOP_VCC2V5
TOP_PS_POR_B

VCC5V0
VCC2V5
PS_POR_B

POWER

DDR0V75

TOP_VCC1V5
TOP_VCC1V5
TOP_DDR0V75
TOP_DDRVREF

POWER

DDR3

VCC1V5
DDR1V5
DDR0V75
DDRVREF

DDR3

DDR3

Title	<Title>		
Size A3	Document Number <Doc>		Rev <RevCo
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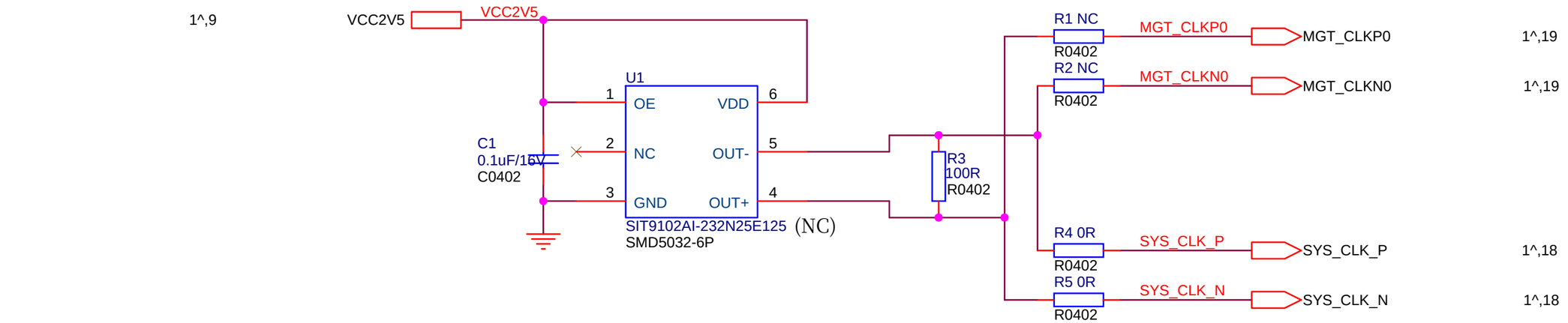

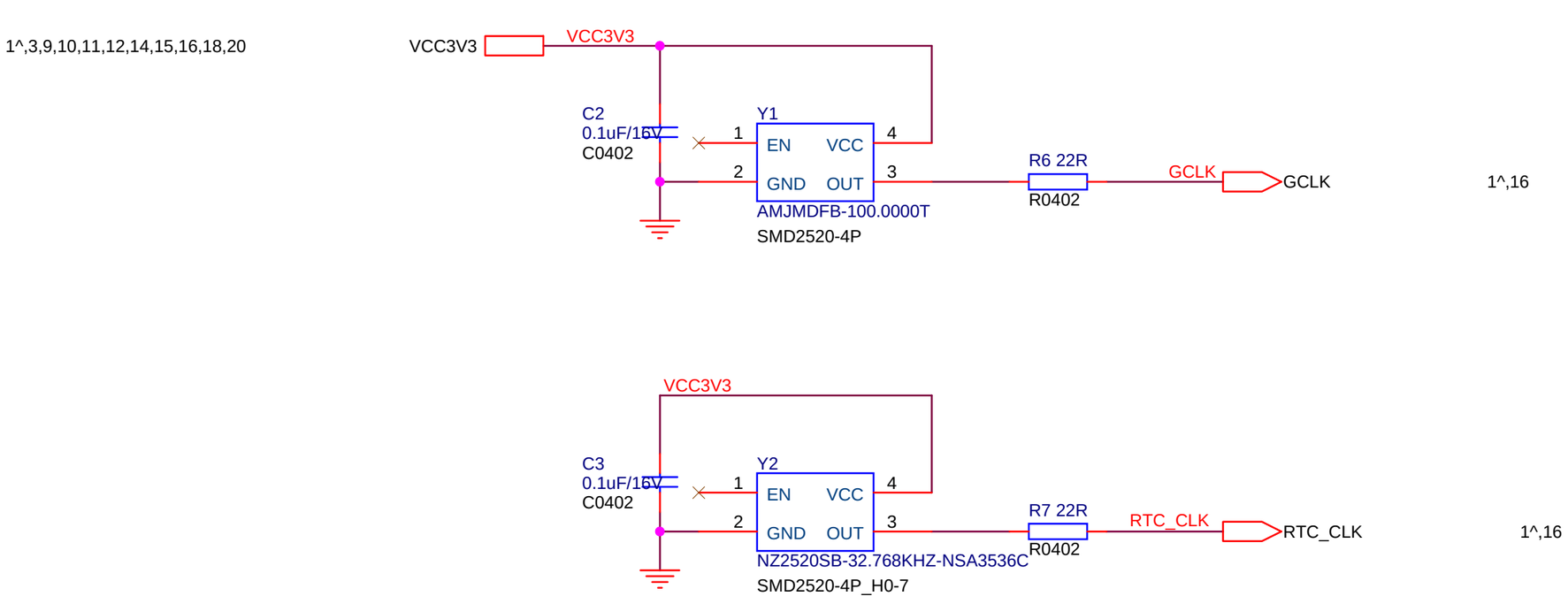

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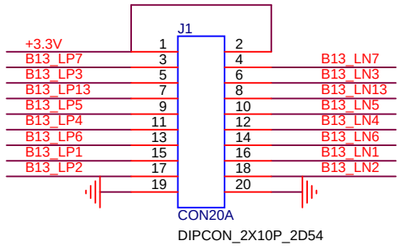

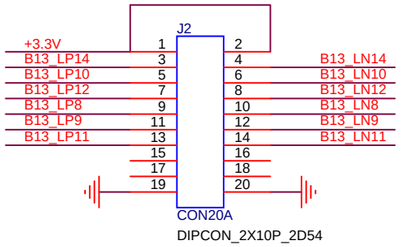

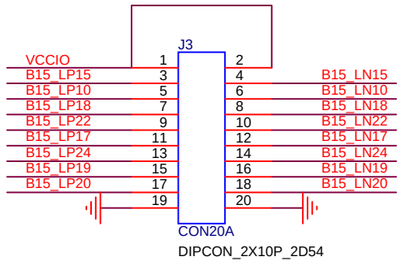

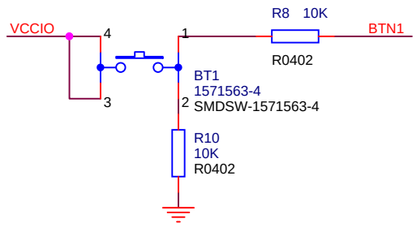

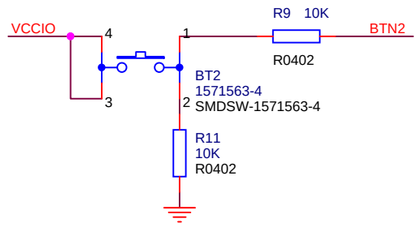

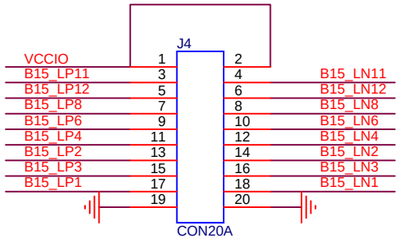

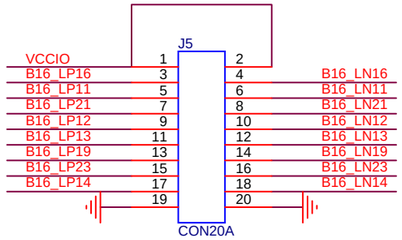

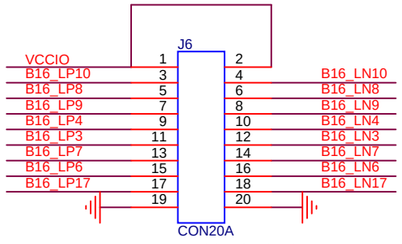

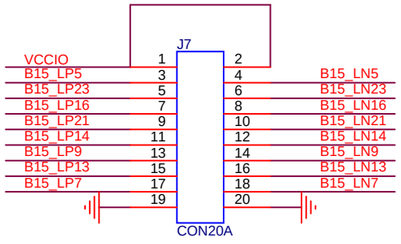

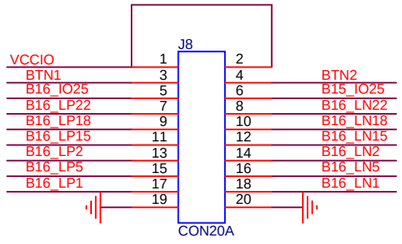

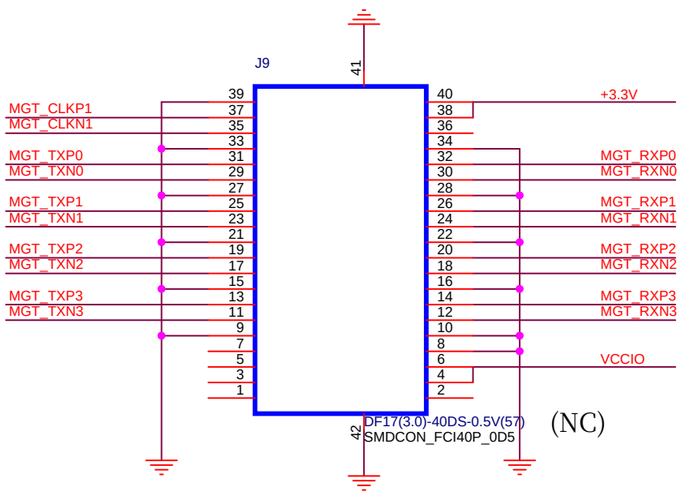

1^,12,16	B13_LP[14:1]	B13_LP[14:1]	1^,2,9,10,11,12,14,15,16,18,20	+3.3V	+3.3V
1^,12,16	B13_LN[14:1]	B13_LN[14:1]	1^,9,20	VCCIO	VCCIO
1^,17	B15_IO0	B15_IO0	1^,5,9,10,18,20	+1.5V	+1.5V
1^,17	B15_IO25	B15_IO25	1^,9	VCC5V0	VCC5V0
1^,17	B15_LP[24:1]	B15_LP[24:1]	1^,18	B34_LP[24:18]	B34_LP[24:18]
1^,17	B15_LN[24:1]	B15_LN[24:1]	1^,18	B34_LN[24:18]	B34_LN[24:18]
			1^,18	B34_L25	B34_L25
1^,17	B16_IO0	B16_IO0			
1^,17	B16_IO25	B16_IO25	1^,15	XADC_VP	XADC_VP
1^,17	B16_LP[24:1]	B16_LP[24:1]	1^,15	XADC_VN	XADC_VN
1^,17	B16_LN[24:1]	B16_LN[24:1]	1^,19	MGT_CLKP1	MGT_CLKP1
			1^,19	MGT_CLKN1	MGT_CLKN1

1^,19	MGT_TXP[3:0]	MGT_TXP[3:0]	
1^,19	MGT_TXN[3:0]	MGT_TXN[3:0]	
1^,19	MGT_RXP[3:0]	MGT_RXP[3:0]	
1^,19	MGT_RXN[3:0]	MGT_RXN[3:0]	

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1^,5,9,10,18,20	DDR1V5	DDR1V5	1,5,9,10,18,20
1^,5,9,10,18,20	VCC1V5	VCC1V5	1,5,9,10,18,20
1^,5,6,10	DDR0V75	DDR0V75	1,5,6,10
1^,5,10,18	DDRVREF	DDRVREF	1,5,10,18
1^,5,18	DDR3_D[31:0]	DDR3_D[31:0]	1,5,18
1^,5,18	DDR3_DM[3:0]	DDR3_DM[3:0]	1,5,18
1^,5,6,18	DDR3_A[14:0]	DDR3_A[14:0]	1,5,6,18
1^,5,6,18	DDR3_BA[2:0]	DDR3_BA[2:0]	1,5,6,18
1^,5,18	DDR3_DQS_P[3:0]	DDR3_DQS_P[3:0]	1,5,18
1^,5,18	DDR3_DQS_N[3:0]	DDR3_DQS_N[3:0]	1,5,18
1^,5,6,18	DDR3_RAS	DDR3_RAS	1,5,6,18
1^,5,6,18	DDR3_CAS	DDR3_CAS	1,5,6,18
1^,5,6,18	DDR3_CS	DDR3_CS	1,5,6,18
1^,5,6,18	DDR3_WE	DDR3_WE	1,5,6,18
1^,5,18	DDR3_RESET	DDR3_RESET	1,5,18
1^,5,18	DDR3_CKE	DDR3_CKE	1,5,18
1^,5,18	DDR3_CKN	DDR3_CKN	1,5,18
1^,5,18	DDR3_CKP	DDR3_CKP	1,5,18
1^,5,6,18	DDR3_ODT	DDR3_ODT	1,5,6,18

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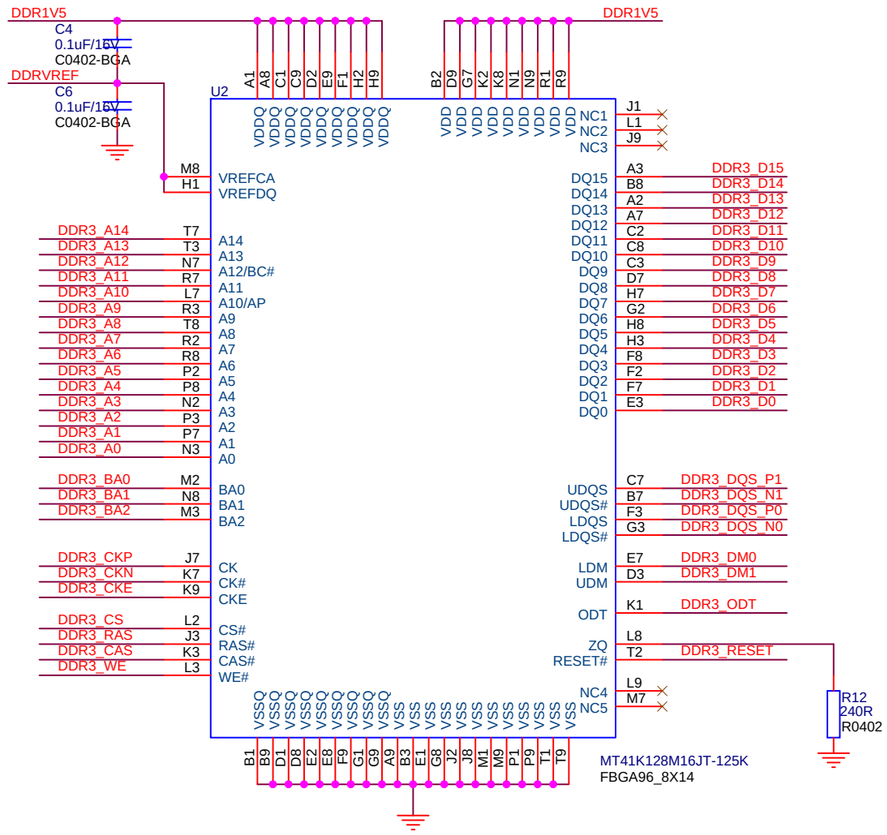

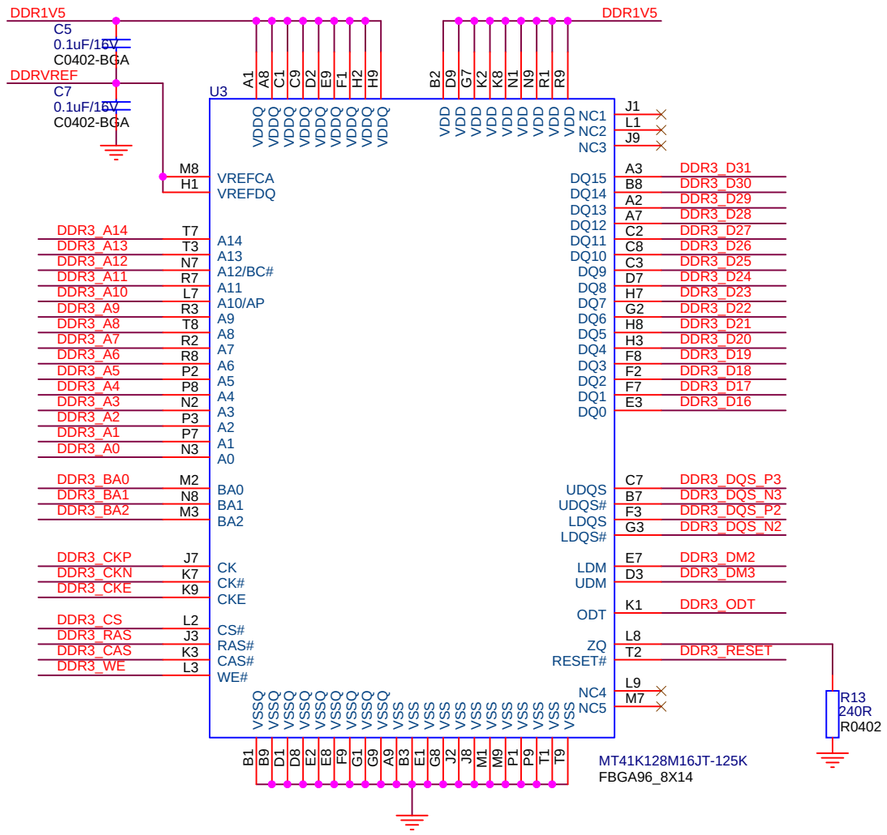

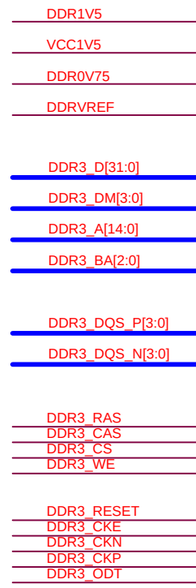

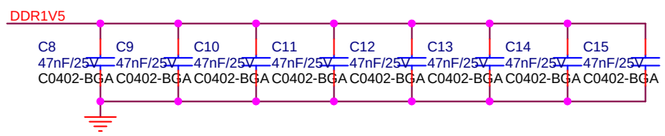

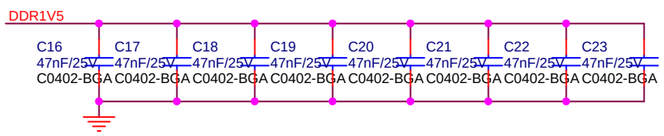

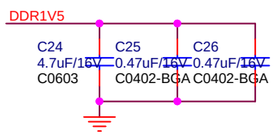

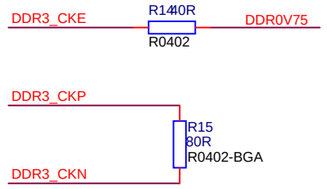

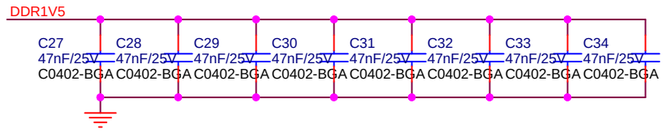

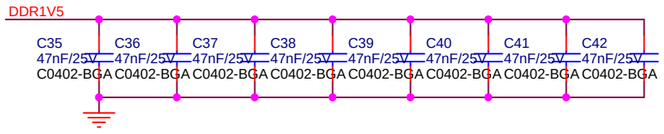

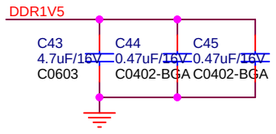

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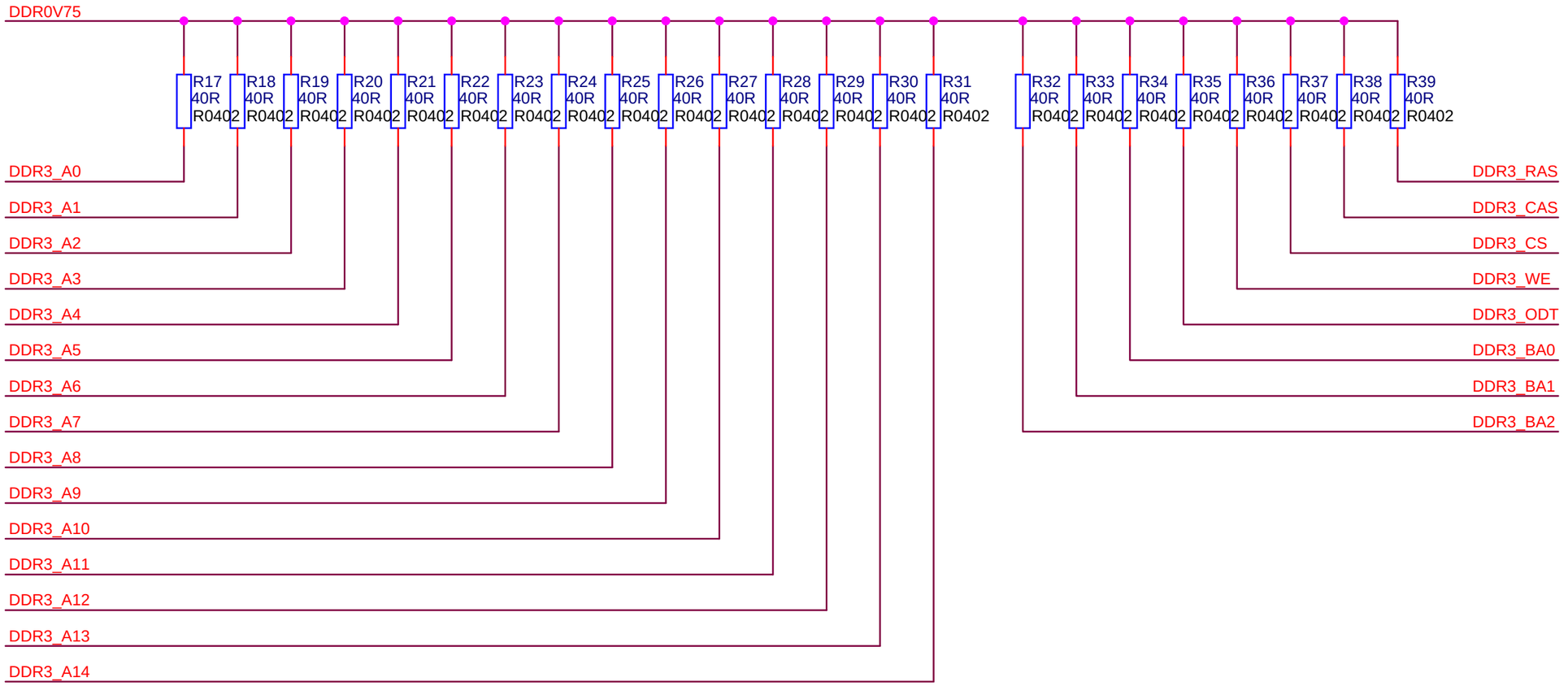

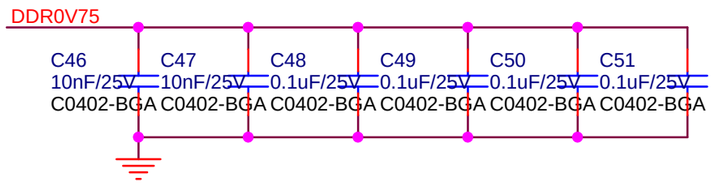

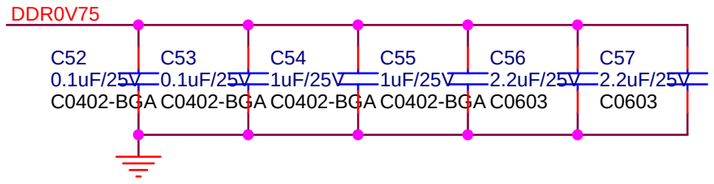

DDR0V75	DDR0V75	1,5,10
DDR3_RAS	DDR3_RAS	1,5,18
DDR3_CAS	DDR3_CAS	1,5,18
DDR3_CS	DDR3_CS	1,5,18
DDR3_WE	DDR3_WE	1,5,18
DDR3_ODT	DDR3_ODT	1,5,18
DDR3_BA[2:0]	DDR3_BA[2:0]	1,5,18
DDR3_A[14:0]	DDR3_A[14:0]	1,5,18

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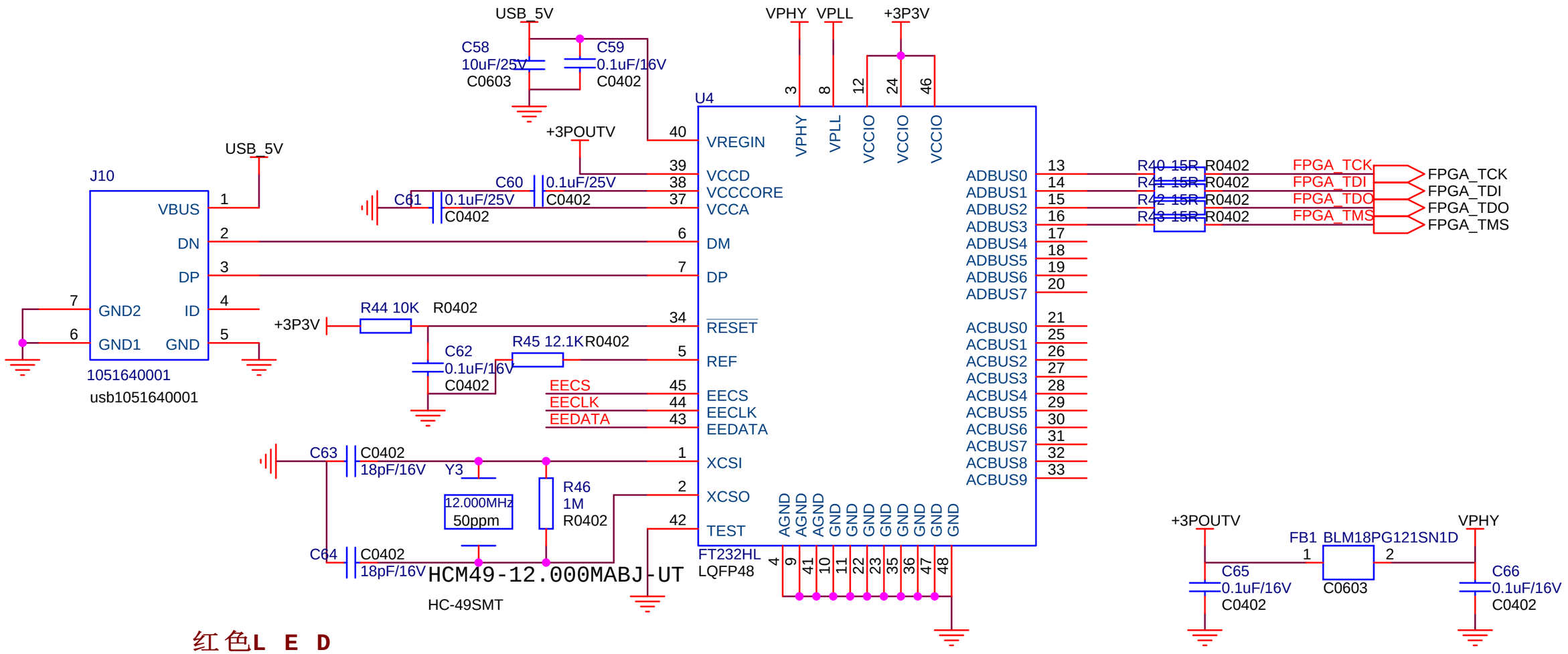
USB_5V
C58
10uF/25V
C0603
C59
0.1uF/16V
C0402
VPHY
VPLL
+3P3V
U4
3
8
12
24
46
VPHY
VPLL
VCCIO
VCCIO
VCCIO
+3POUTV
40
VREGIN
39
VCCD
38
VCCCORE
37
VCCA
6
DM
7
DP
34
RESET
5
REF
45
EECS
44
EECLK
43
EEDATA
1
XCSI
2
XCSO
42
TEST
4
9
41
10
11
22
23
35
36
47
48
AGND
AGND
AGND
GND
GND
GND
GND
GND
GND
GND
FT232HL
LQFP48
ADBUS0
ADBUS1
ADBUS2
ADBUS3
ADBUS4
ADBUS5
ADBUS6
ADBUS7
13
14
15
16
17
18
19
20
ACBUS0
ACBUS1
ACBUS2
ACBUS3
ACBUS4
ACBUS5
ACBUS6
ACBUS7
ACBUS8
ACBUS9
21
25
26
27
28
29
30
31
32
33
R40 15R R0402
R41 15R R0402
R42 15R R0402
R43 15R R0402
FPGA_TCK
FPGA_TDI
FPGA_TDO
FPGA_TMS
FPGA_TCK
FPGA_TDI
FPGA_TDO
FPGA_TMS
1^,15
1^,15
1^,15
1^,15
J10
USB_5V
VBUS
1
DN
2
DP
3
GND2
7
ID
4
GND1
6
GND
5
1051640001
usb1051640001
C60
0.1uF/25V
C0402
C61
0.1uF/25V
C0402
R44 10K R0402
+3P3V
C62
0.1uF/16V
C0402
R45 12.1K R0402
C63
C0402
18pF/16V
Y3
12.000MHz
50ppm
C64
C0402
18pF/16V
R46
1M
R0402
HCM49-12.000MABJ-UT
HC-49SMT
+3POUTV
FB1 BLM18PG121SN1D
1
2
VPHY
C65
0.1uF/16V
C0402
C0603
C66
0.1uF/16V
C0402
红色L E D
D1 LED_R LED0603
USB_5V
R47 1K
R0402
D2 LED_G LED0603
+3P3V
R48 1K
R0402
绿色L E D
+3POUTV
FB2 BLM18PG121SN1D
1
2
VPLL
C67
0.1uF/16V
C0402
C0603
C68
0.1uF/16V
C0402
+3P3V
R49
10K
R0402
R50
10K
R0402
R51
10K
R0402
U5
+3P3V
8
VCC
7
NC
6
ORG
5
VSS
CS
1
CLK
2
DI
3
DO
4
EECS
EECLK
EEDATA
C74
0.1uF/16V
C0402
R52 2K R0402
93LC56BISN
SOIC8
+3POUTV
FB3 BLM18PG121SN1D
1
2
+3P3V
C69
0.1uF/16V
C0402
C0603
C70
0.1uF/16V
C0402
C71
0.1uF/16V
C0402
C72
0.1uF/16V
C0402
C73
0.1uF/16V
C0402
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1^,9	VCC5V0	VCC5V0	VCC5V0	1,9
1^,9,20	VCC1V0	VCC1V0	VCC1V0	1,9,20
1^,9,11,15,20	VCC1V8	VCC1V8	VCC1V8	1,9,11,15,20
1^,2,3,9,10,11,12,14,15,16,18,20	VCC3V3	VCC3V3	VCC3V3	1,2,3,9,10,11,12,14,15,16,18,20
1^,5,9,10,18,20	VCC1V5	VCC1V5	VCC1V5	1,5,9,10,18,20
1^,5,6,10	DDR0V75	DDR0V75	DDR0V75	1,5,6,10
1^,5,10,18	DDRVREF	DDRVREF	DDRVREF	1,5,10,18
1^,3,9,20	VADJ	VADJ	VADJ	1,3,9,20
1^,10,12,16	PS_POR_B	PS_POR_B	PS_POR_B	1,10,12,16
1^,11,19	MGTAVTT	MGTAVTT	MGTAVTT	1,11,19
1^,11,19	MGTAVCC	MGTAVCC	MGTAVCC	1,11,19
1^,2,9	VCC2V5	VCC2V5	VCC2V5	1,2,9

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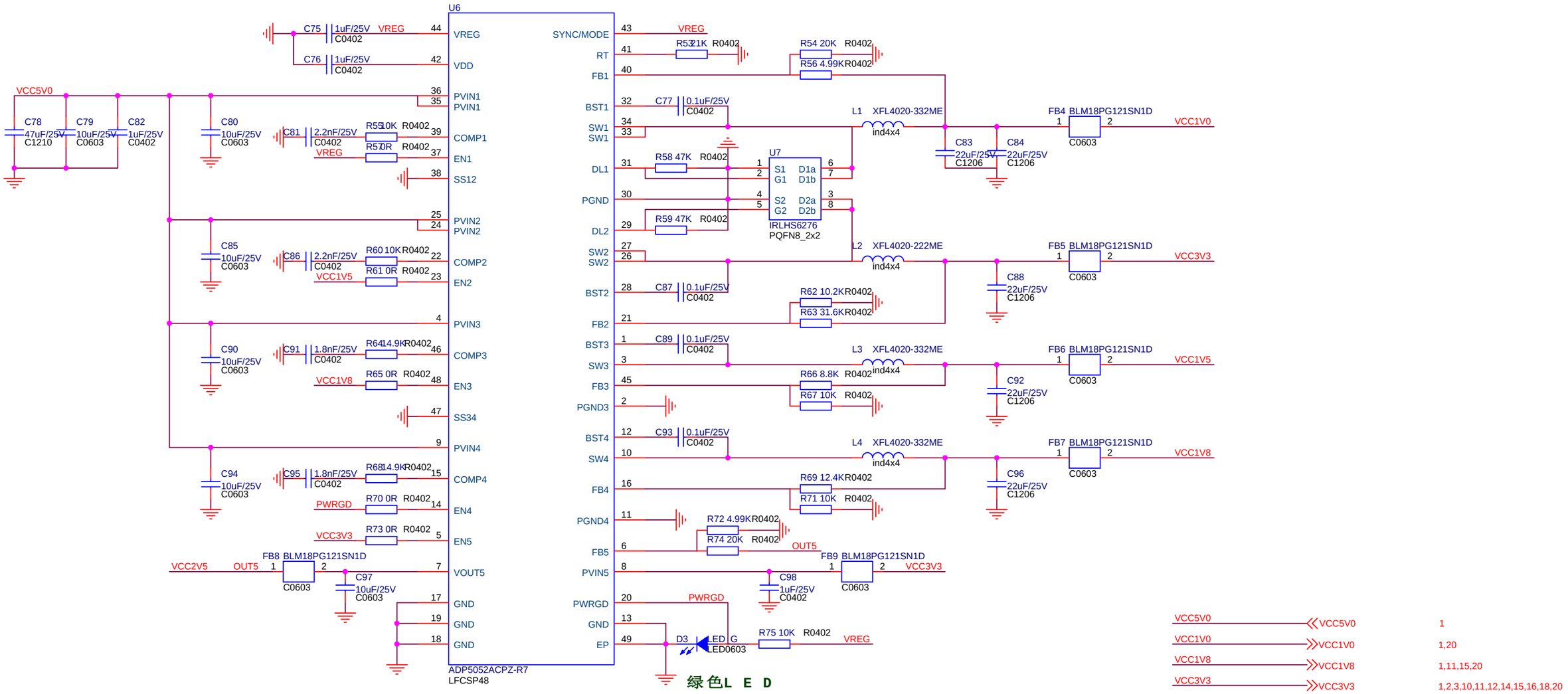
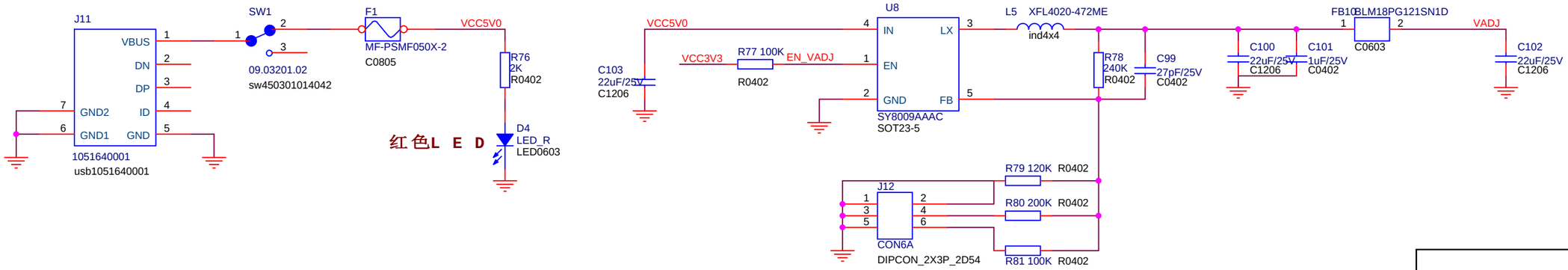


VCC5V0 — VCC5V0 — 1
VCC1V0 — VCC1V0 — 1,20
VCC1V8 — VCC1V8 — 1,11,15,20
VCC3V3 — VCC3V3 — 1,2,3,10,11,12,14,15,16,18,20
VCC1V5 — VCC1V5 — 1,5,10,18,20
VADJ — VADJ — 1,3,20
VCC2V5 — VCC2V5 — 1,2



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VCC1V5 — VCC1V5 — 1,5,9,18,20
VCC3V3 — VCC3V3 — 1,2,3,9,11,12
DDR0V75 — DDR0V75 — 1,5,6
DDRVREF — DDRVREF — 1,5,18
PS_POR_B — PS_POR_B — 1,12,16

VCC3V3
C104
1uF/25V
C0402
R82
10K
R0402
U9
VCC3V3
10
IN
2
VCC
C106
10uF/25V
C0603
PG_DDR
5
PGOOD
7
SHDN
OUT
9
DDR0V75
OUTS
6
C105
47uF/25V
C1210
C107
47uF/25V
C1210
PGND
AGND
8
3
VCC1V5
R85 10K
R0402
4
REFIN
REFOUT
1
DDRVREF
C108
1uF/25V
C0402
C109
0.1uF/25V
C0402
R86
10K
R0402
C110
1nF/25V
C0402
MAX1510ETB
TDFN10
EP
11

PG_DDR
R83 0R R0402
PS_POR_B
VCC3V3
R84
390R
R0402
D5
LED_G
LED0603
Q1
NDS331N
SOT-23
PG_DDR
R87 4.7K R0402

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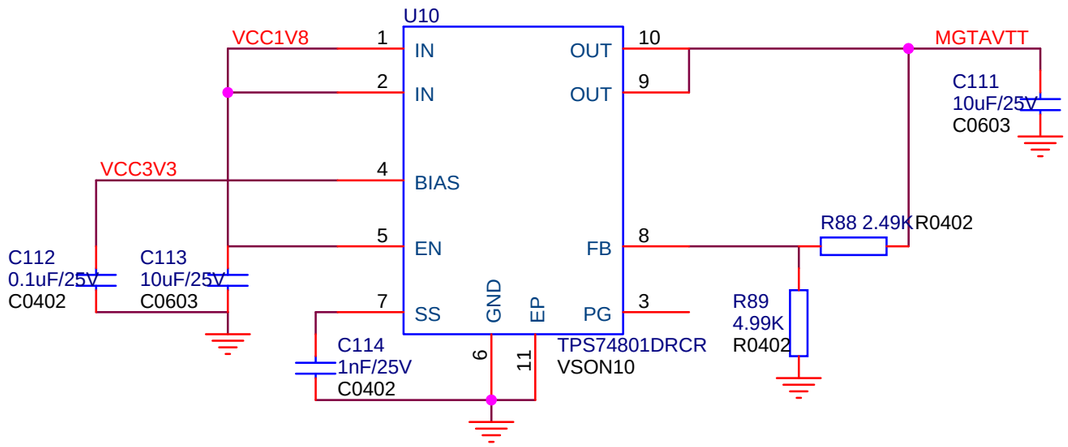

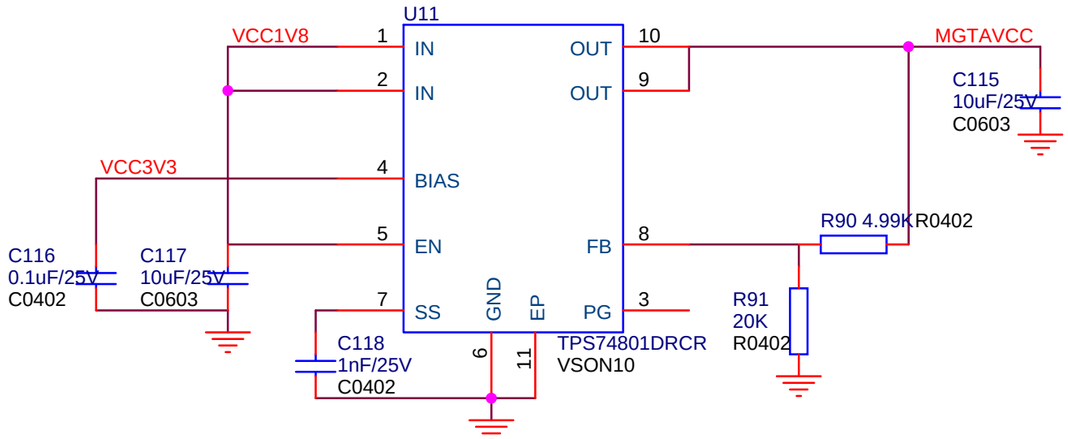

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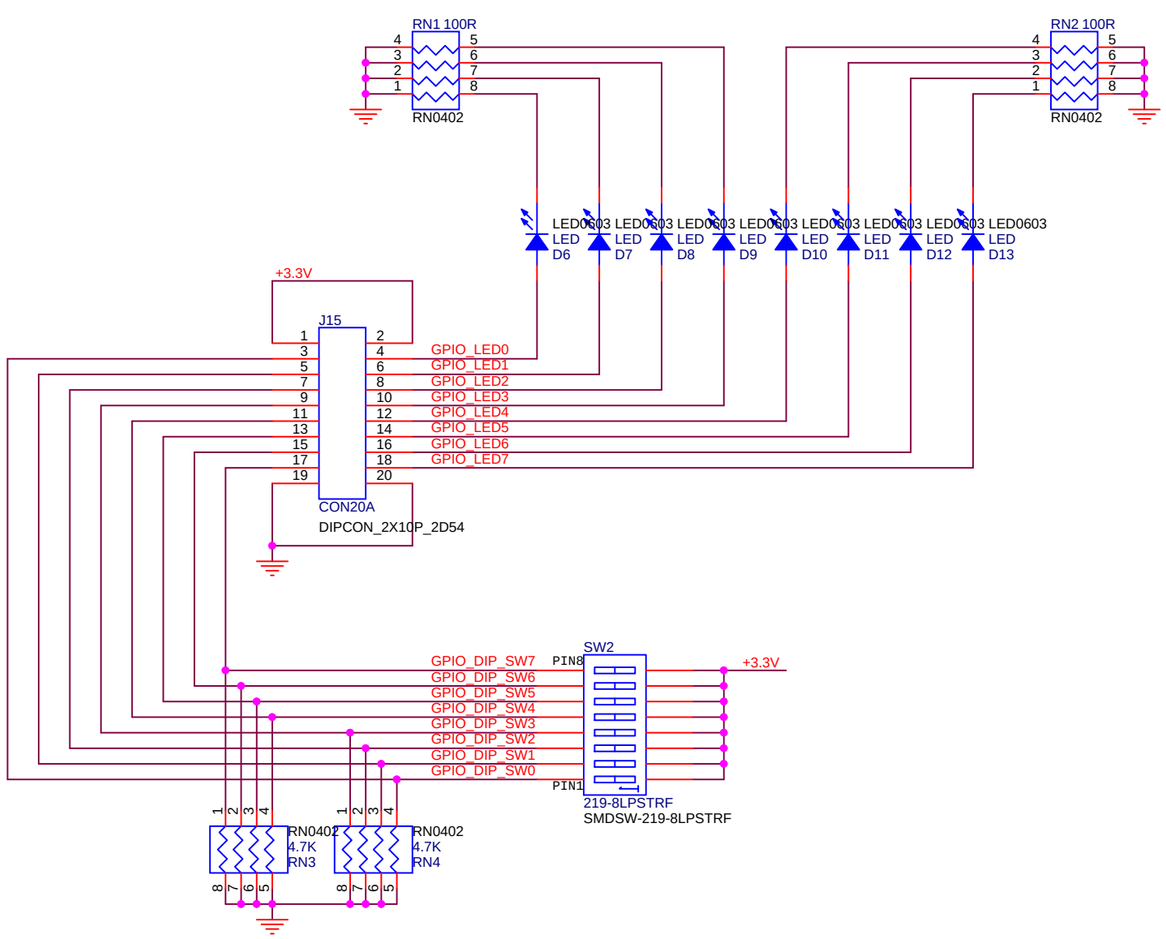

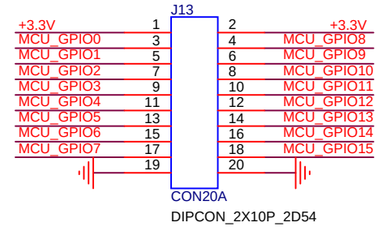

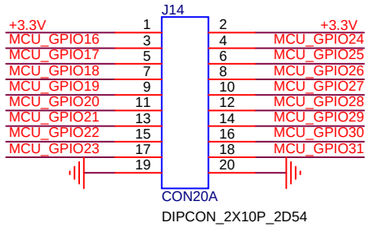

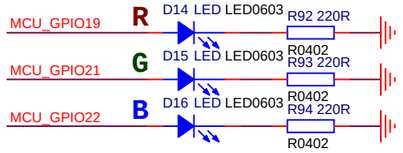

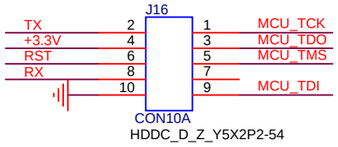

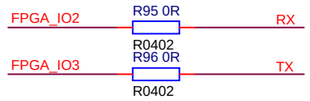

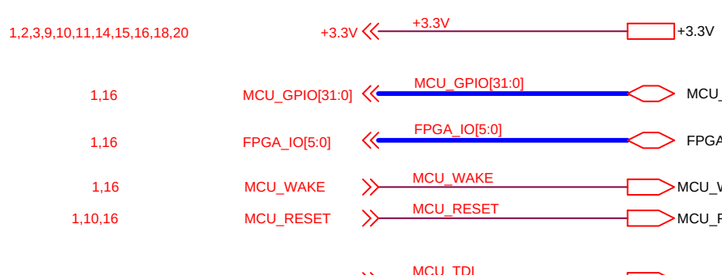

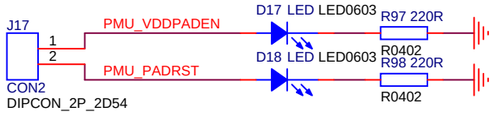

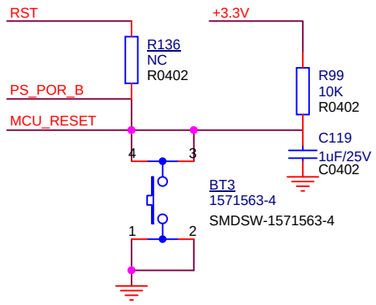

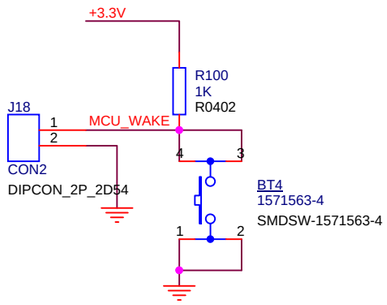

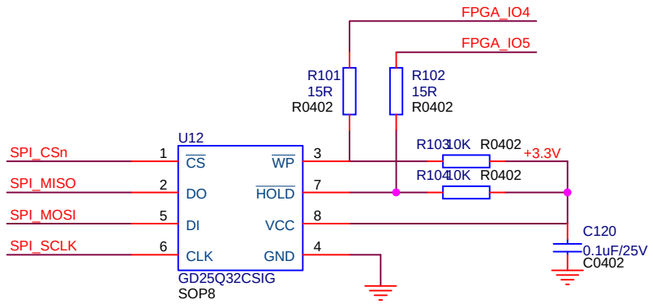

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+1.0V
+1.5V
+1.8V
+3.3V
VCCIO
MGTAVCC
MGTAVTT
VTTREF
XADC_VP
XADC_VN
MCU_GPIO[31:0]
FPGA_IO[5:0]
MCU_WAKE
MCU_RESET
MCU_TDI
MCU_TDO
MCU_TMS
MCU_TCK
MGT_TXP[3:0]
MGT_TXN[3:0]
MGT_RXP[3:0]
MGT_RXN[3:0]
MGT_CLKP0
MGT_CLKN0
MGT_CLKP1
MGT_CLKN1
B34_LP[24:18]
B34_LN[24:18]
B34_L25
SYS_CLK_P
SYS_CLK_N
DDR3_A[14:0]
DDR3_D[31:0]
DDR3_DQS_P[3:0]
DDR3_DQS_N[3:0]
DDR3_DM[3:0]
DDR3_BA[2:0]
DDR3_CLKP0
DDR3_CLKN0
DDR3_WE
DDR3_RAS
DDR3_CAS
DDR3_CKE0
DDR3_ODT
DDR3_RESET
DDR3_CS
B13_LP[14:1]
B13_LN[14:1]
B15_IO0
B15_IO25
B15_LP[24:1]
B15_LN[24:1]
GCLK
RTC_CLK
SPI_MISO
SPI_MOSI
SPI_SCLK
SPI_CSn
PMU_VDDPADEN
PMU_PADRST
B16_IO0
B16_IO25
B16_LP[24:1]
B16_LN[24:1]
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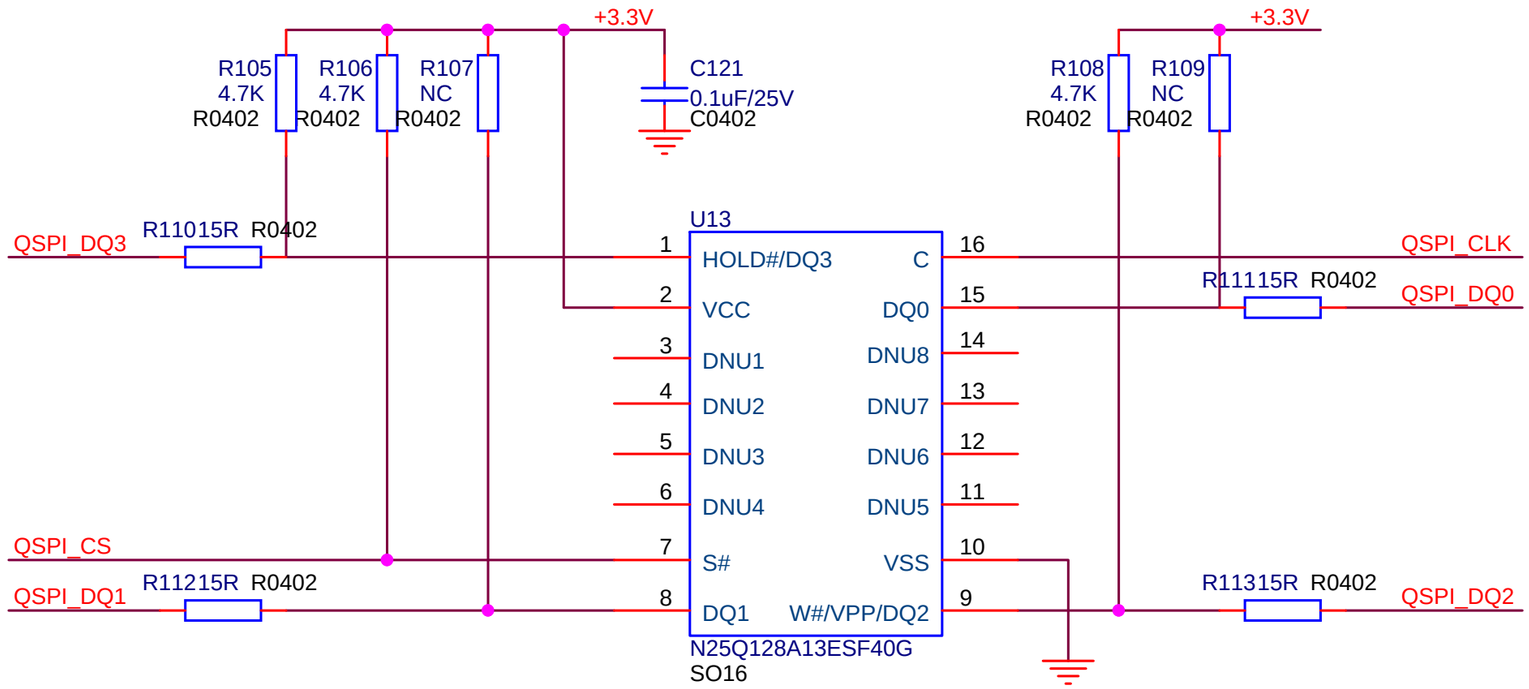

+3.3V	+3.3V	1,2,3,9,10,11,12,15,16,18,20
QSPI_CLK	QSPI_CLK	15
QSPI_DQ[3:0]	QSPI_DQ[3:0]	16
QSPI_CS	QSPI_CS	16

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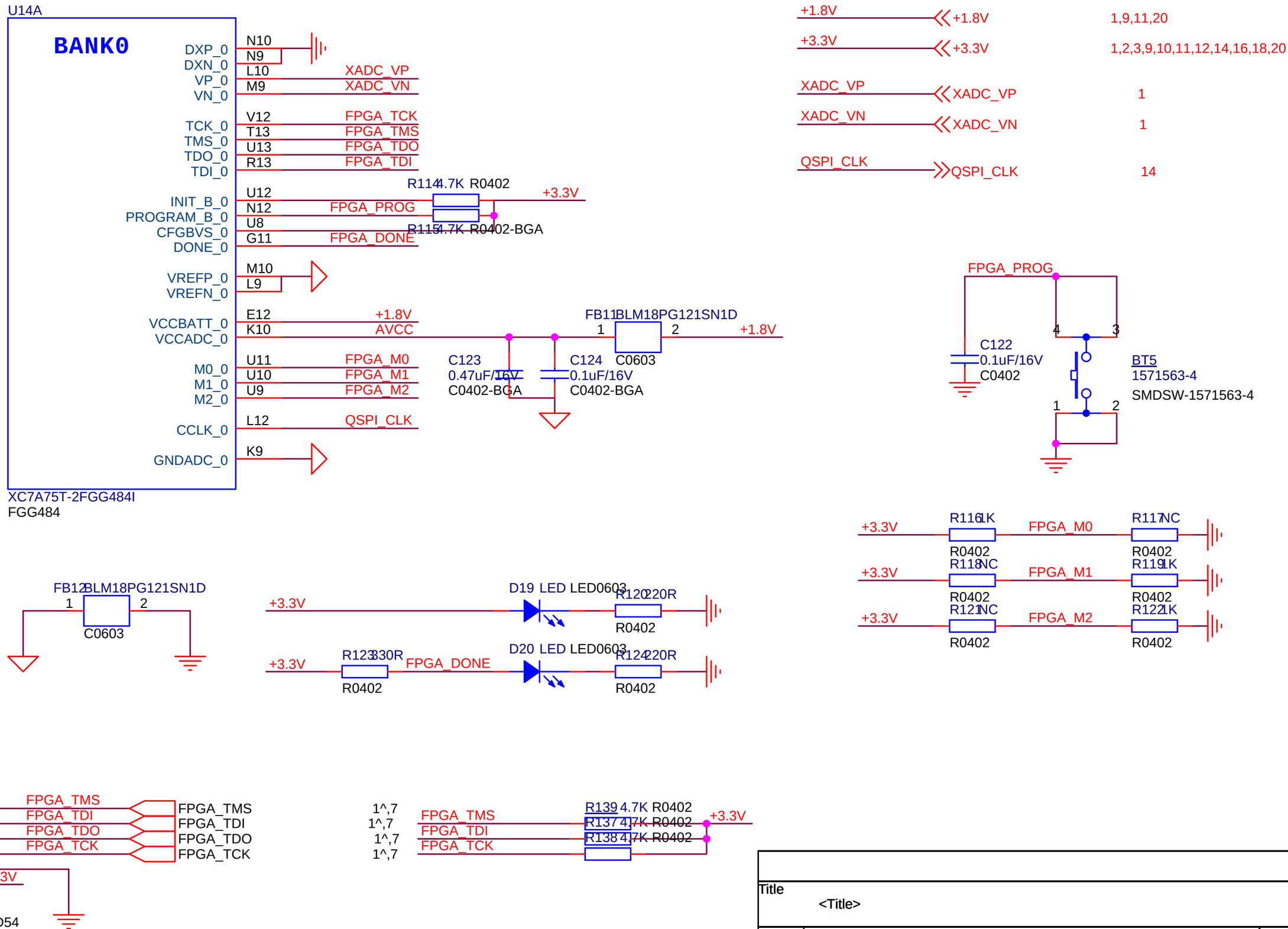
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BANK0
DXP_0
DXN_0
VP_0
VN_0
TCK_0
TMS_0
TDO_0
TDI_0
INIT_B_0
PROGRAM_B_0
CFGBVS_0
DONE_0
VREFP_0
VREFN_0
VCCBATT_0
VCCADC_0
M0_0
M1_0
M2_0
CCLK_0
GNDADC_0
N10
N9
L10
M9
V12
T13
U13
R13
U12
N12
U8
G11
M10
L9
E12
K10
U11
U10
U9
L12
K9
XADC_VP
XADC_VN
FPGA_TCK
FPGA_TMS
FPGA_TDO
FPGA_TDI
FPGA_PROG
FPGA_DONE
FPGA_M0
FPGA_M1
FPGA_M2
QSPI_CLK
+3.3V
+1.8V
AVCC
R114 4.7K R0402
R115 4.7K R0402-BGA
FB11 BLM18PG121SN1D
C123 0.47uF/16V C0402-BGA
C124 0.1uF/16V C0402-BGA
C0603
XC7A75T-2FGG484I
FGG484
+1.8V +1.8V 1,9,11,20
+3.3V +3.3V 1,2,3,9,10,11,12,14,16,18,20
XADC_VP XADC_VP 1
XADC_VN XADC_VN 1
QSPI_CLK QSPI_CLK 14
FPGA_PROG
C122 0.1uF/16V C0402
BT5 1571563-4 SMDSW-1571563-4
FB12 BLM18PG121SN1D
C0603
D19 LED LED0603
D20 LED LED0603
R120 220R
R123 330R
R124 220R
R0402
FPGA_DONE
R116 1K
R117 NC
R118 NC
R119 1K
R121 NC
R122 1K
FPGA_M0
FPGA_M1
FPGA_M2
J19
CON6
DIPCON_6P_2D54
FPGA_TMS
FPGA_TDI
FPGA_TDO
FPGA_TCK
R139 4.7K R0402
R137 4.7K R0402
R138 4.7K R0402
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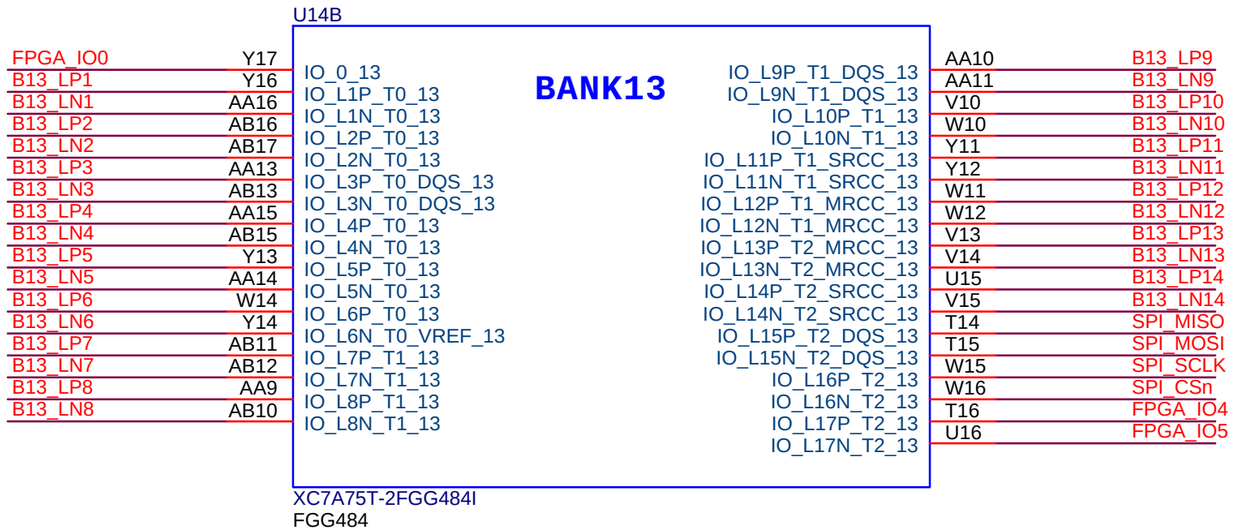

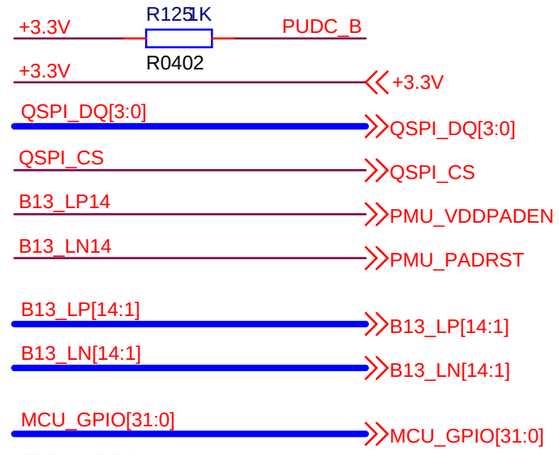

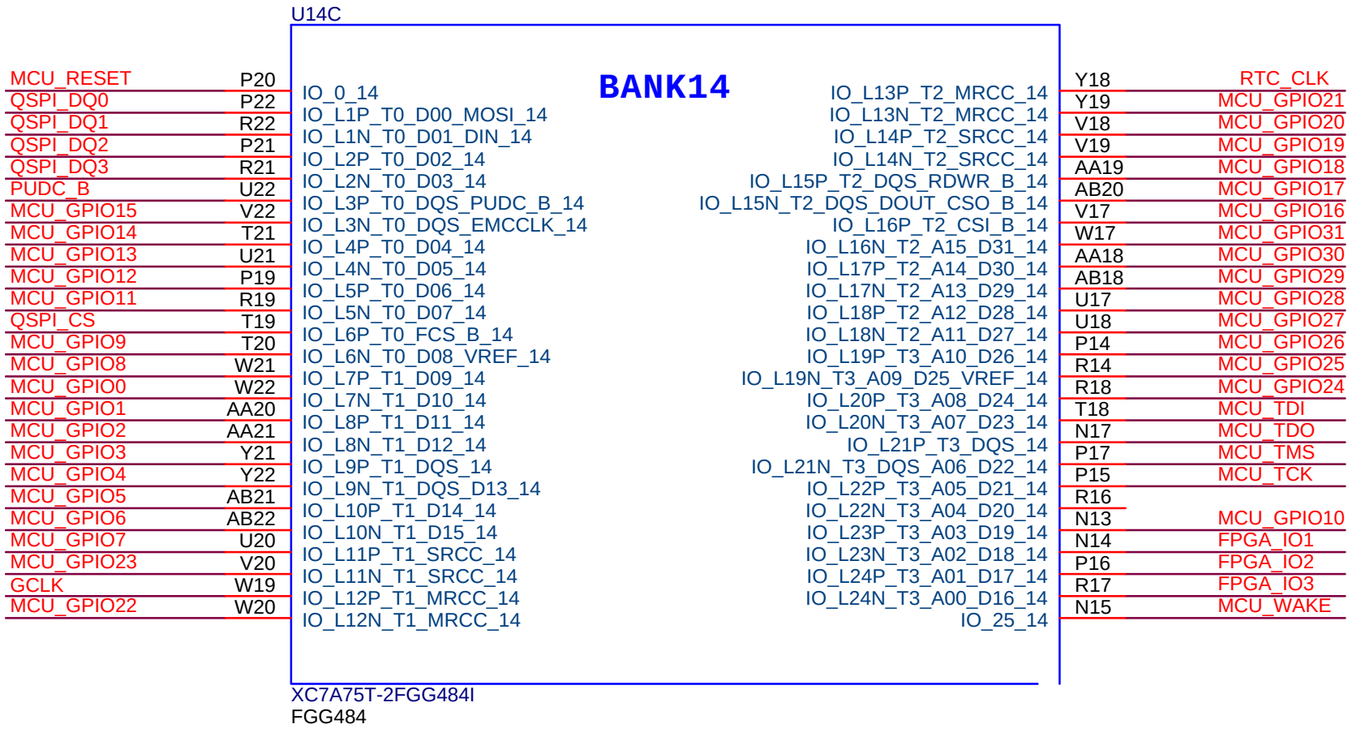

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BANK15

B15_IO0	J16	IO_0_15	
B15_LP1	H13	IO_L1P_T0_AD0P_15	
B15_LN1	G13	IO_L1N_T0_AD0N_15	
B15_LP2	G15	IO_L2P_T0_AD8P_15	
B15_LN2	G16	IO_L2N_T0_AD8N_15	
B15_LP3	J14	IO_L3P_T0_DQS_AD1P_15	
B15_LN3	H14	IO_L3N_T0_DQS_AD1N_15	
B15_LP4	G17	IO_L4P_T0_15	
B15_LN4	G18	IO_L4N_T0_15	
B15_LP5	J15	IO_L5P_T0_AD9P_15	
B15_LN5	H15	IO_L5N_T0_AD9N_15	
B15_LP6	H17	IO_L6P_T0_15	
B15_LN6	H18	IO_L6N_T0_VREF_15	
B15_LP7	J22	IO_L7P_T1_AD2P_15	
B15_LN7	H22	IO_L7N_T1_AD2N_15	
B15_LP8	H20	IO_L8P_T1_AD10P_15	
B15_LN8	G20	IO_L8N_T1_AD10N_15	
B15_LP9	K21	IO_L9P_T1_DQS_AD3P_15	
B15_LN9	K22	IO_L9N_T1_DQS_AD3N_15	
B15_LP10	M21	IO_L10P_T1_AD11P_15	
B15_LN10	L21	IO_L10N_T1_AD11N_15	
B15_LP11	J20	IO_L11P_T1_SRCC_15	
B15_LN11	J21	IO_L11N_T1_SRCC_15	
B15_LP12	J19	IO_L12P_T1_MRCC_15	
B15_LN12	H19	IO_L12N_T1_MRCC_15	
		IO_L13P_T2_MRCC_15	K18	B15_LP13
		IO_L13N_T2_MRCC_15	K19	B15_LN13
		IO_L14P_T2_SRCC_15	L19	B15_LP14
		IO_L14N_T2_SRCC_15	L20	B15_LN14
		IO_L15P_T2_DQS_15	N22	B15_LP15
		IO_L15N_T2_DQS_ADV_B_15	M22	B15_LN15
		IO_L16P_T2_A28_15	M18	B15_LP16
		IO_L16N_T2_A27_15	L18	B15_LN16
		IO_L17P_T2_A26_15	N18	B15_LP17
		IO_L17N_T2_A25_15	N19	B15_LN17
		IO_L18P_T2_A24_15	N20	B15_LP18
		IO_L18N_T2_A23_15	M20	B15_LN18
		IO_L19P_T3_A22_15	K13	B15_LP19
		IO_L19N_T3_A21_VREF_15	K14	B15_LN19
		IO_L20P_T3_A20_15	M13	B15_LP20
		IO_L20N_T3_A19_15	L13	B15_LN20
		IO_L21P_T3_DQS_15	K17	B15_LP21
		IO_L21N_T3_DQS_A18_15	J17	B15_LN21
		IO_L22P_T3_A17_15	L14	B15_LP22
		IO_L22N_T3_A16_15	L15	B15_LN22
		IO_L23P_T3_FOE_B_15	L16	B15_LP23
		IO_L23N_T3_FWE_B_15	K16	B15_LN23
		IO_L24P_T3_RS1_15	M15	B15_LP24
		IO_L24N_T3_RS0_15	M16	B15_LN24
		IO_25_15	M17	B15_IO25

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FGG484

U14E

BANK16

B16_IO0	F15	IO_0_16	
B16_LP1	F13	IO_L1P_T0_16	
B16_LN1	F14	IO_L1N_T0_16	
B16_LP2	F16	IO_L2P_T0_16	
B16_LN2	E17	IO_L2N_T0_16	
B16_LP3	C14	IO_L3P_T0_DQS_16	
B16_LN3	C15	IO_L3N_T0_DQS_16	
B16_LP4	E13	IO_L4P_T0_16	
B16_LN4	E14	IO_L4N_T0_16	
B16_LP5	E16	IO_L5P_T0_16	
B16_LN5	D16	IO_L5N_T0_16	
B16_LP6	D14	IO_L6P_T0_16	
B16_LN6	D15	IO_L6N_T0_VREF_16	
B16_LP7	B15	IO_L7P_T1_16	
B16_LN7	B16	IO_L7N_T1_16	
B16_LP8	C13	IO_L8P_T1_16	
B16_LN8	B13	IO_L8N_T1_16	
B16_LP9	A15	IO_L9P_T1_DQS_16	
B16_LN9	A16	IO_L9N_T1_DQS_16	
B16_LP10	A13	IO_L10P_T1_16	
B16_LN10	A14	IO_L10N_T1_16	
B16_LP11	B17	IO_L11P_T1_SRCC_16	
B16_LN11	B18	IO_L11N_T1_SRCC_16	
B16_LP12	D17	IO_L12P_T1_MRCC_16	
B16_LN12	C17	IO_L12N_T1_MRCC_16	
		IO_L13P_T2_MRCC_16	C18	B16_LP13
		IO_L13N_T2_MRCC_16	C19	B16_LN13
		IO_L14P_T2_SRCC_16	E19	B16_LP14
		IO_L14N_T2_SRCC_16	D19	B16_LN14
		IO_L15P_T2_DQS_16	F18	B16_LP15
		IO_L15N_T2_DQS_16	E18	B16_LN15
		IO_L16P_T2_16	B20	B16_LP16
		IO_L16N_T2_16	A20	B16_LN16
		IO_L17P_T2_16	A18	B16_LP17
		IO_L17N_T2_16	A19	B16_LN17
		IO_L18P_T2_16	F19	B16_LP18
		IO_L18N_T2_16	F20	B16_LN18
		IO_L19P_T3_16	D20	B16_LP19
		IO_L19N_T3_VREF_16	C20	B16_LN19
		IO_L20P_T3_16	C22	B16_LP20
		IO_L20N_T3_16	B22	B16_LN20
		IO_L21P_T3_DQS_16	B21	B16_LP21
		IO_L21N_T3_DQS_16	A21	B16_LN21
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		IO_L22N_T3_16	D22	B16_LN22
		IO_L23P_T3_16	E21	B16_LP23
		IO_L23N_T3_16	D21	B16_LN23
		IO_L24P_T3_16	G21	B16_LP24
		IO_L24N_T3_16	G22	B16_LN24
		IO_25_16	F21	B16_IO25

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B15_IO25	B15_IO25	1,3
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B15_LN[24:1]	B15_LN[24:1]	1,3
B16_IO0	B16_IO0	1
B16_IO25	B16_IO25	1,3
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B16_LN[24:1]	B16_LN[24:1]	1,3

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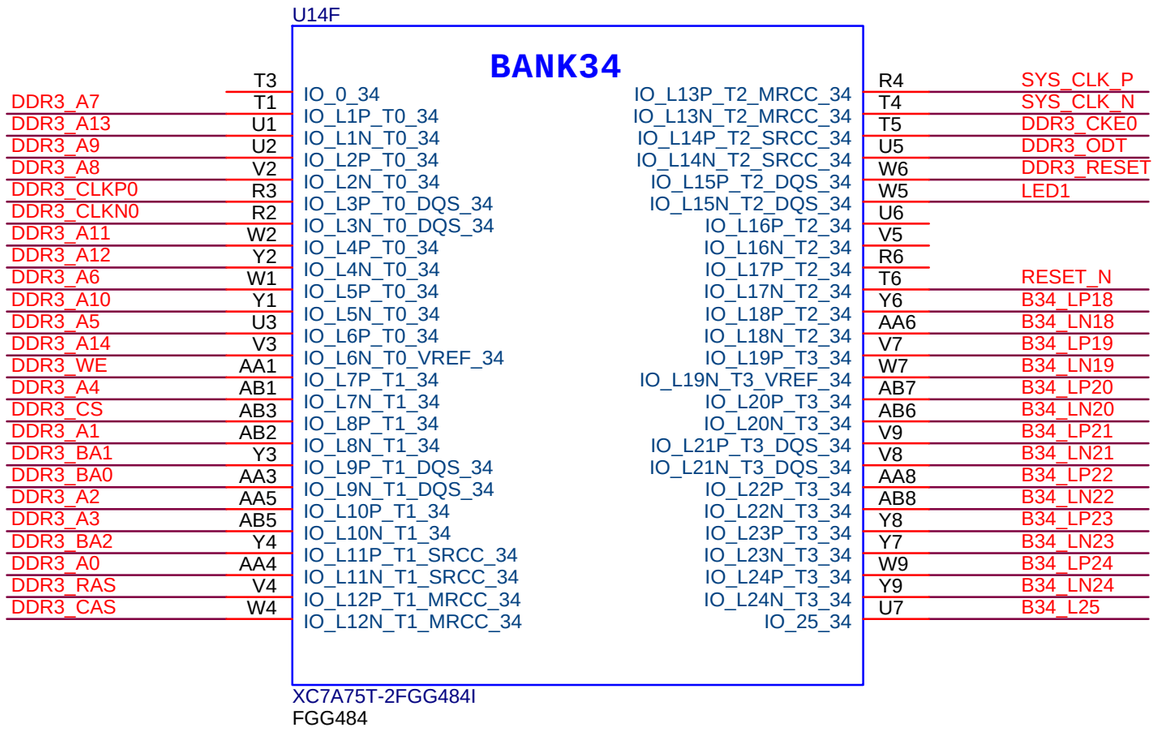

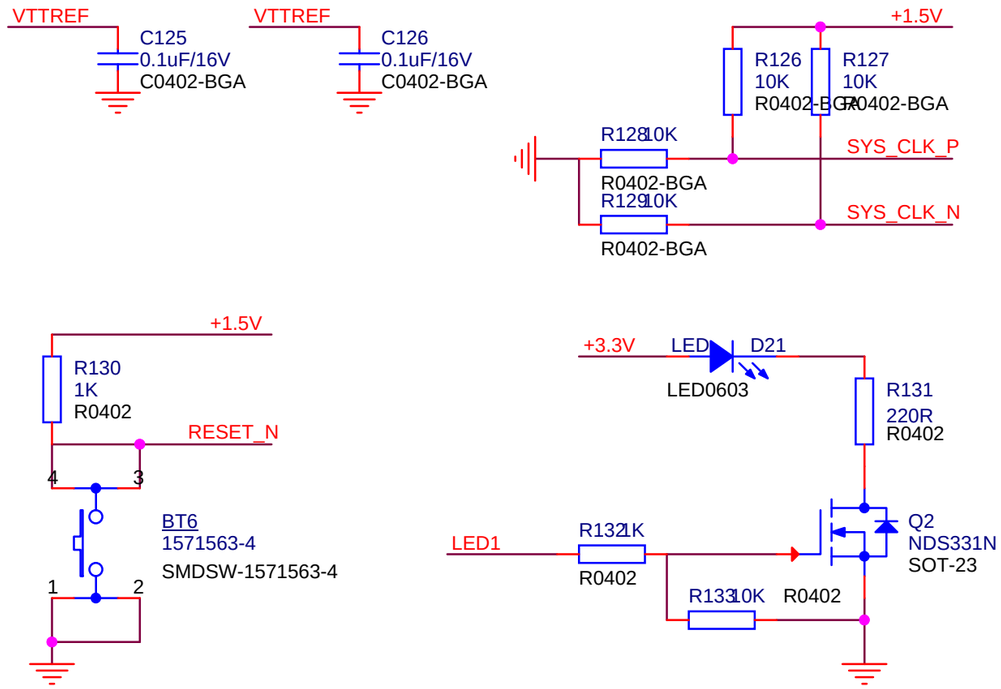

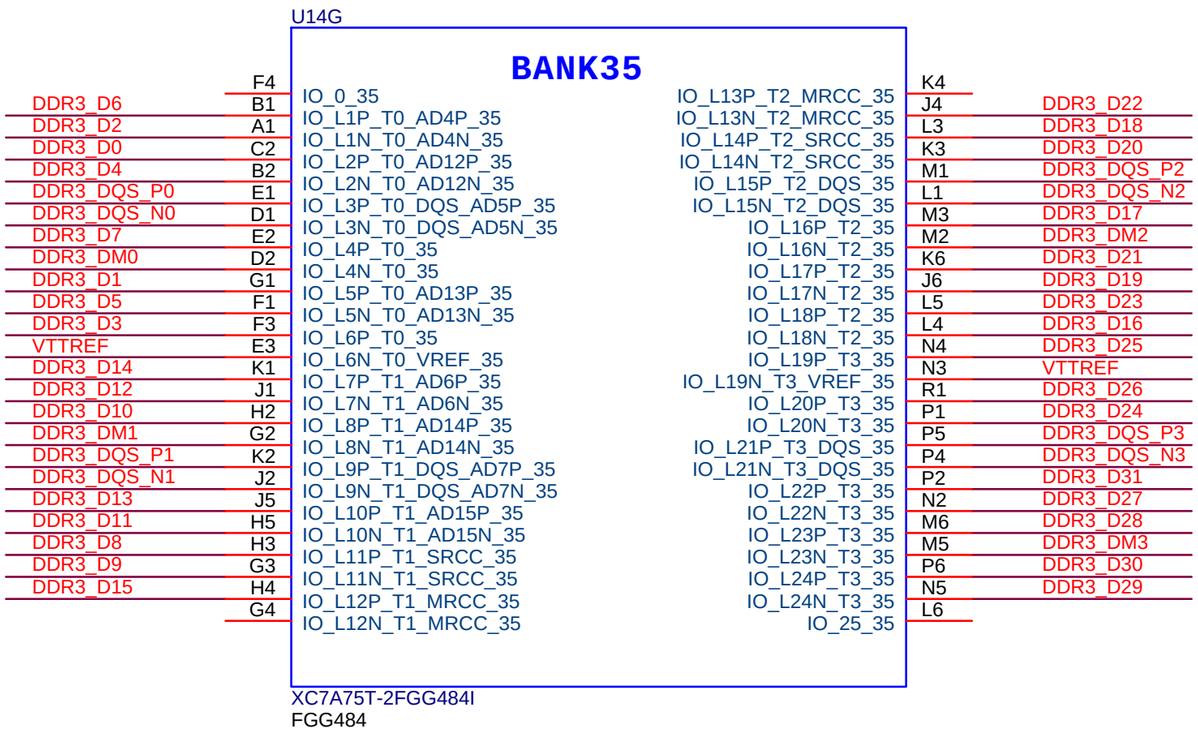

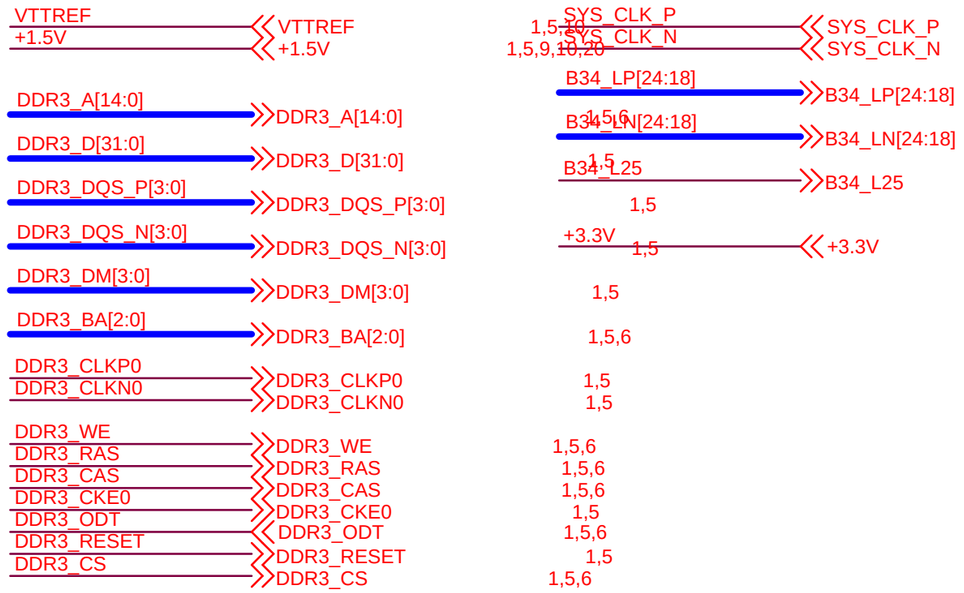

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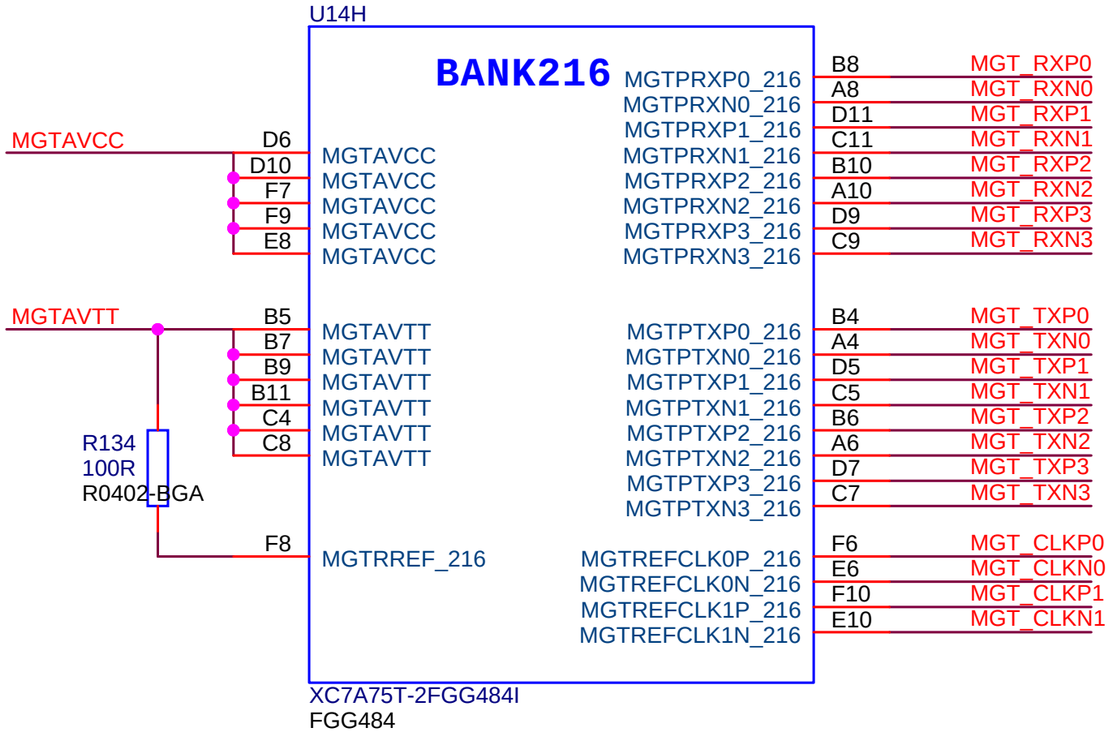

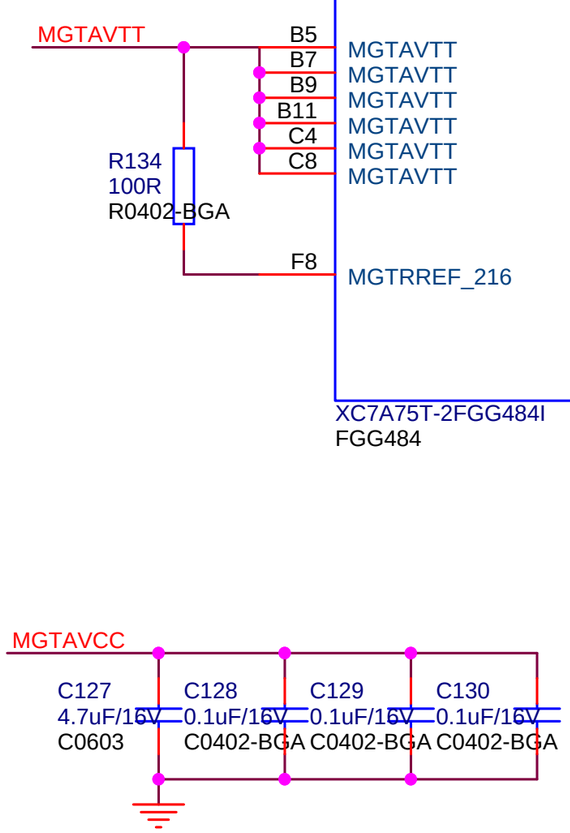

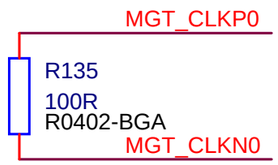

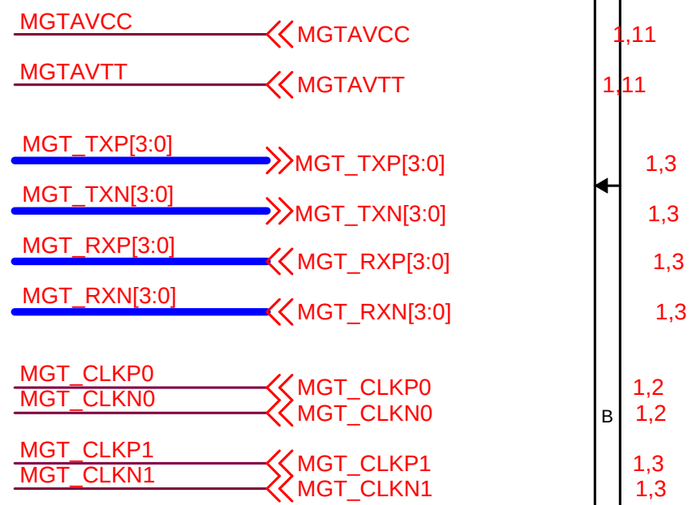

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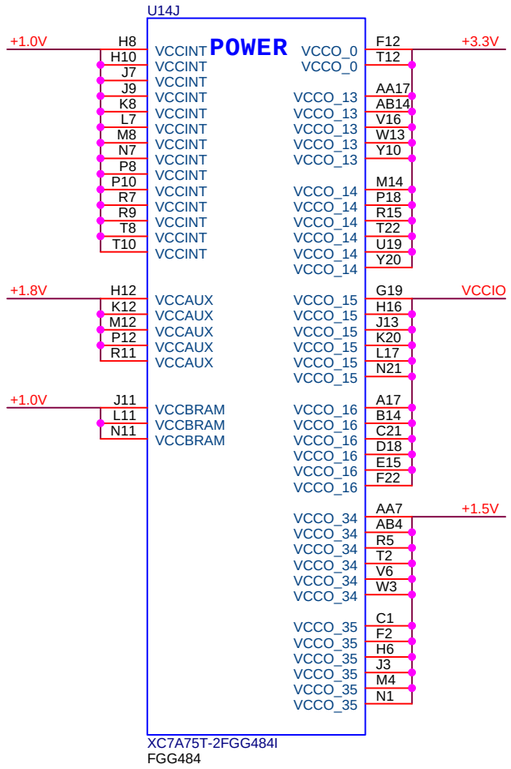

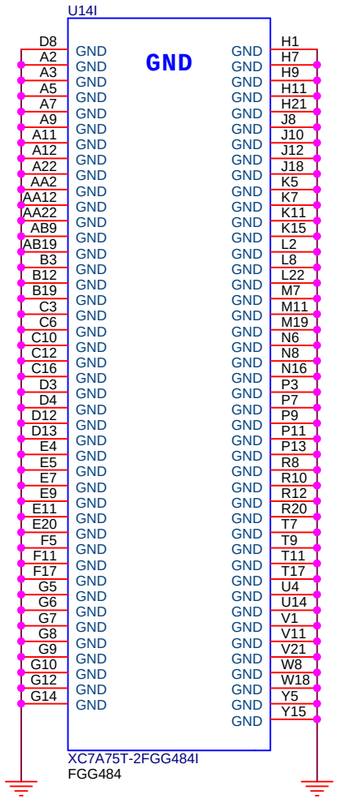

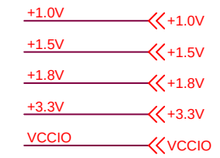

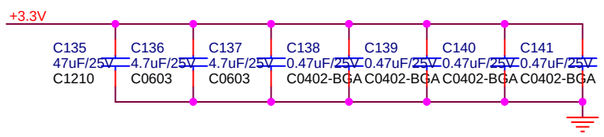

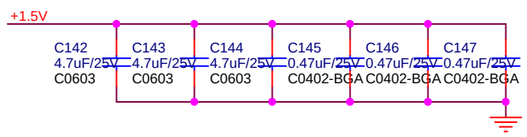

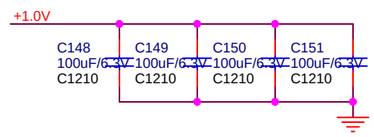

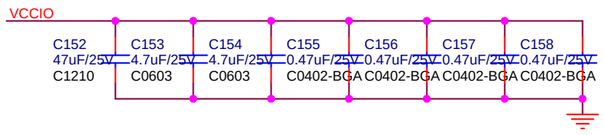

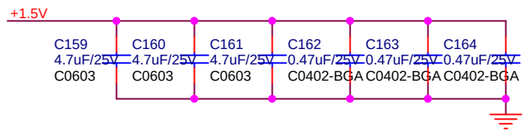

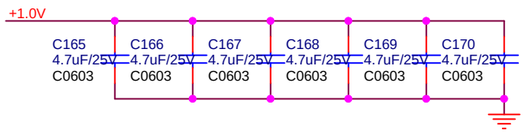

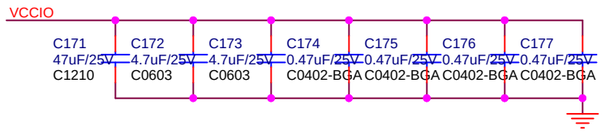

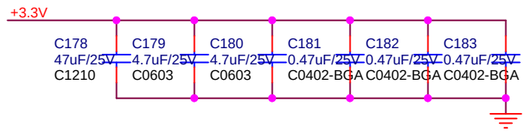

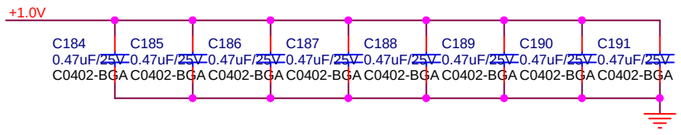

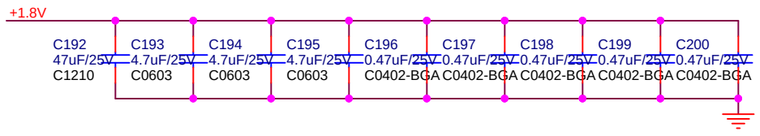

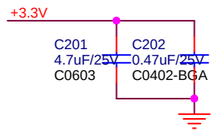

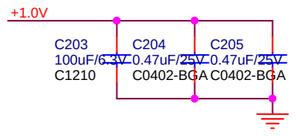

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