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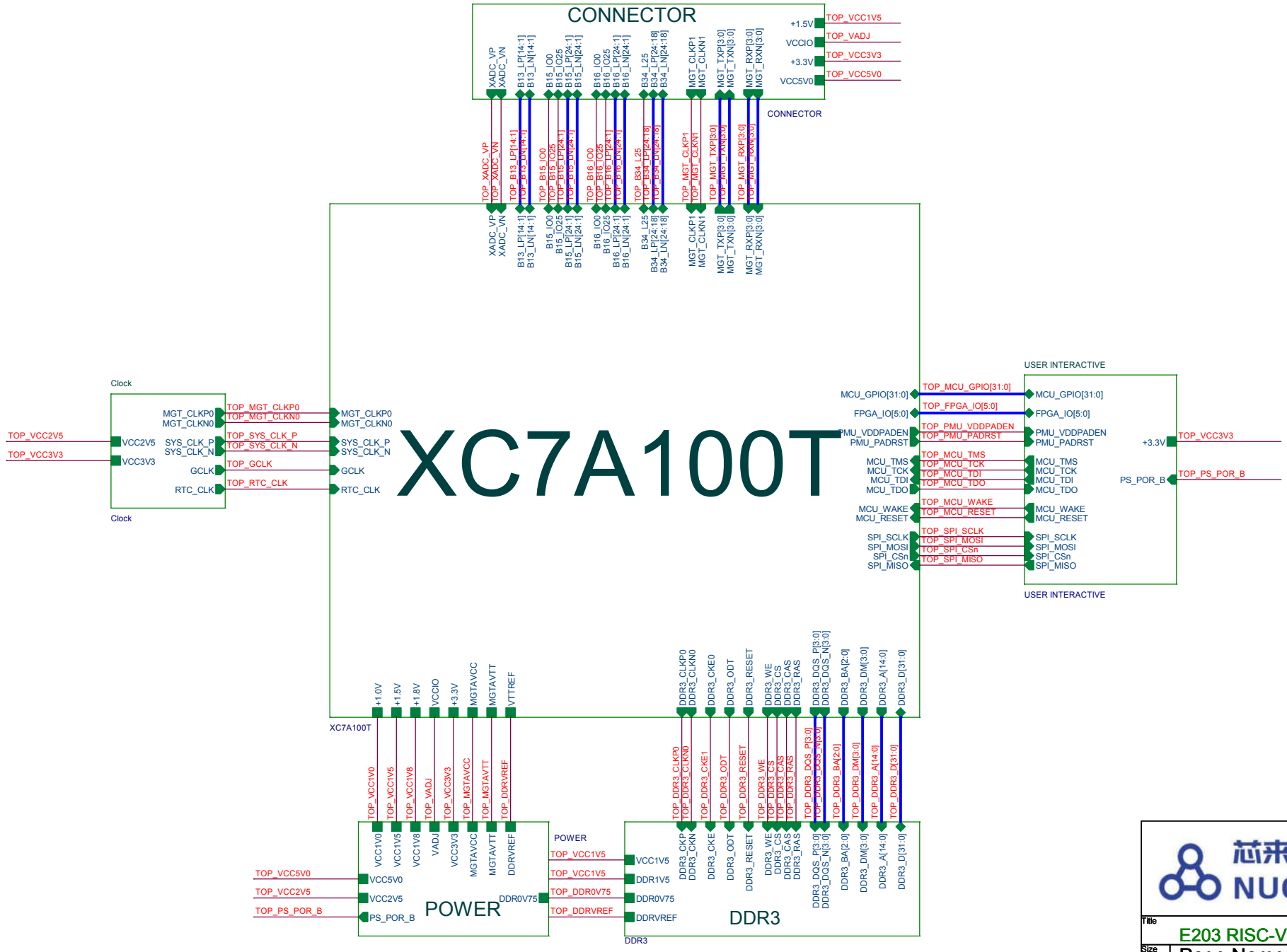

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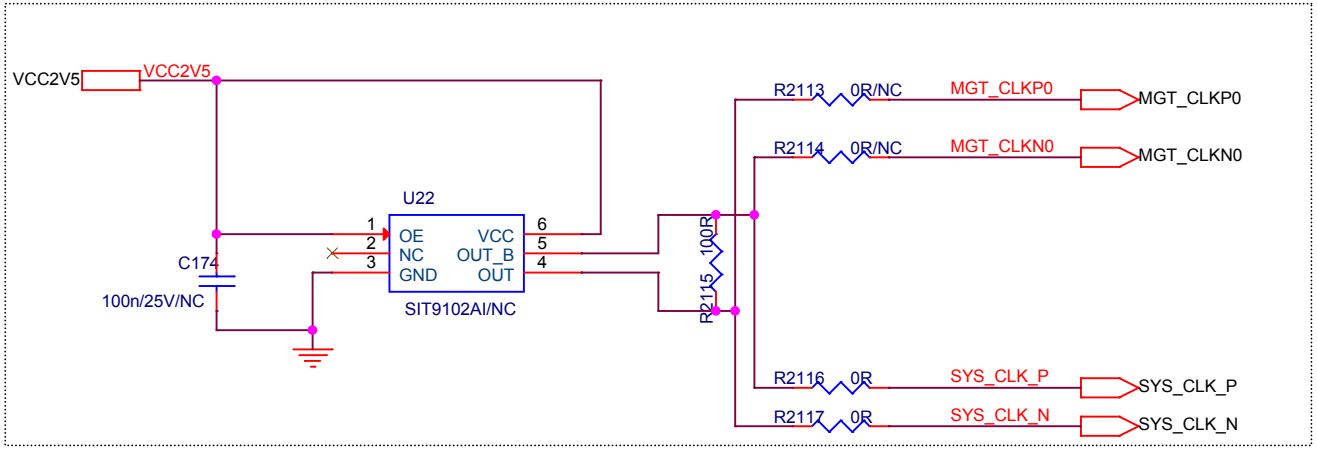
CLOCK



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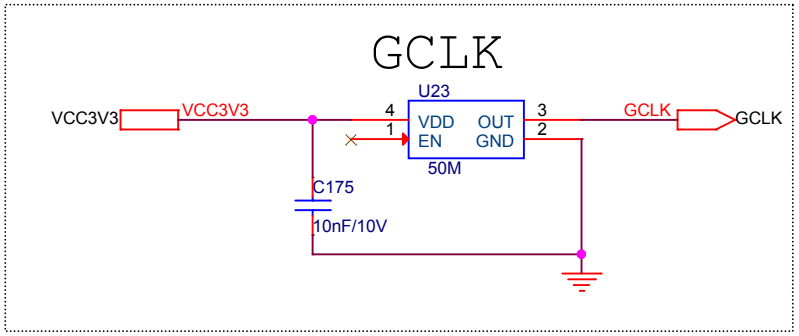

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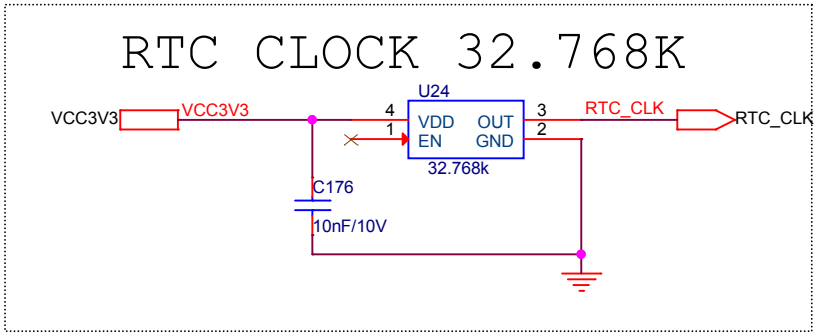

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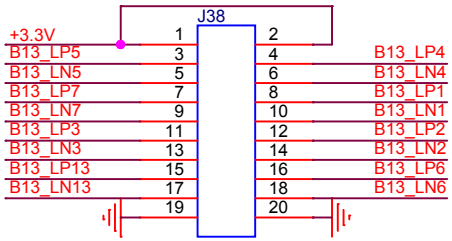
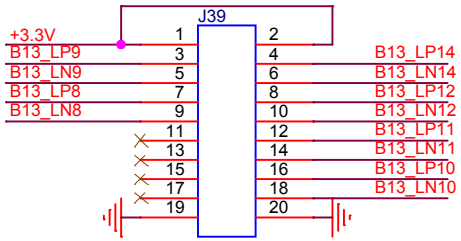
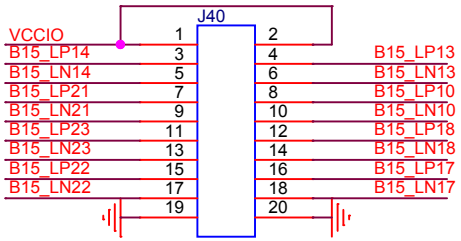
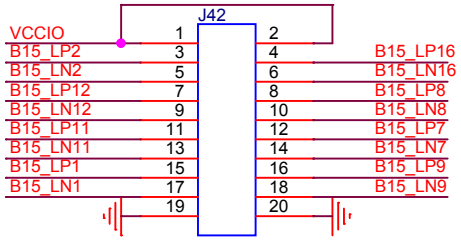
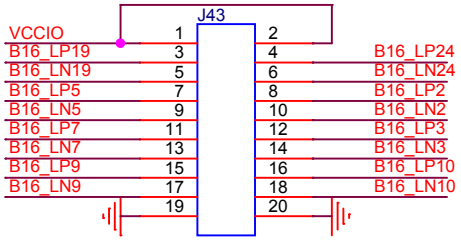
CONNECTOR

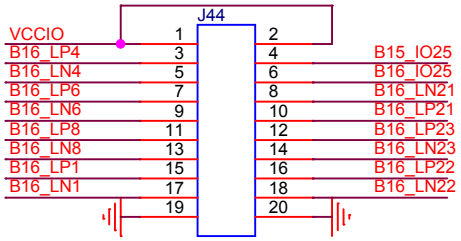


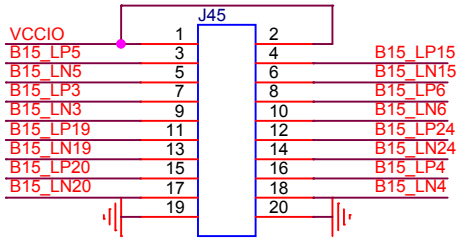


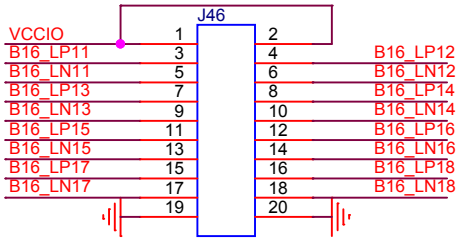


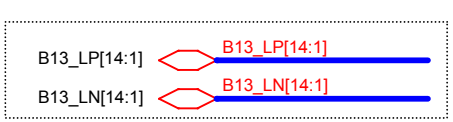


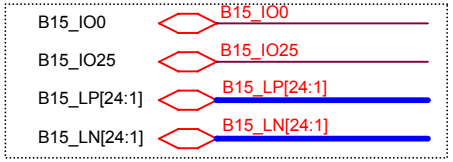


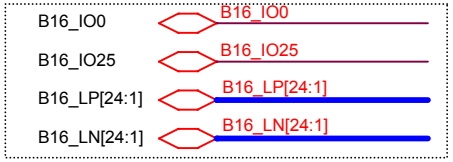


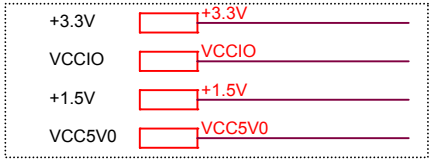


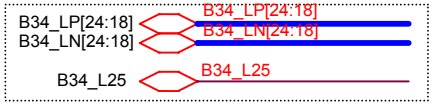


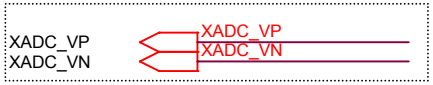


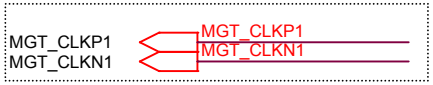


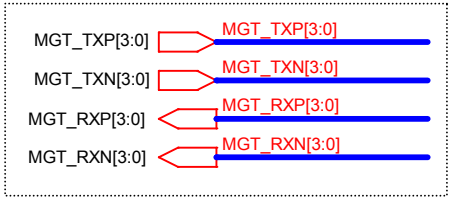












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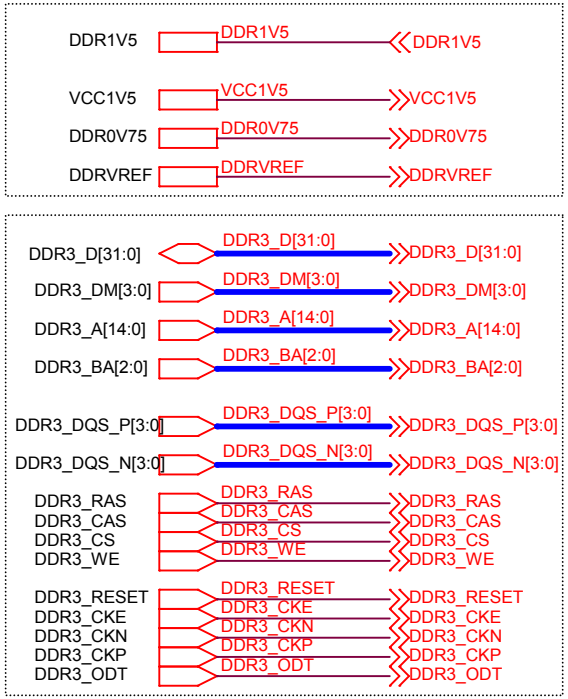
DDR3 TO TOP PORT



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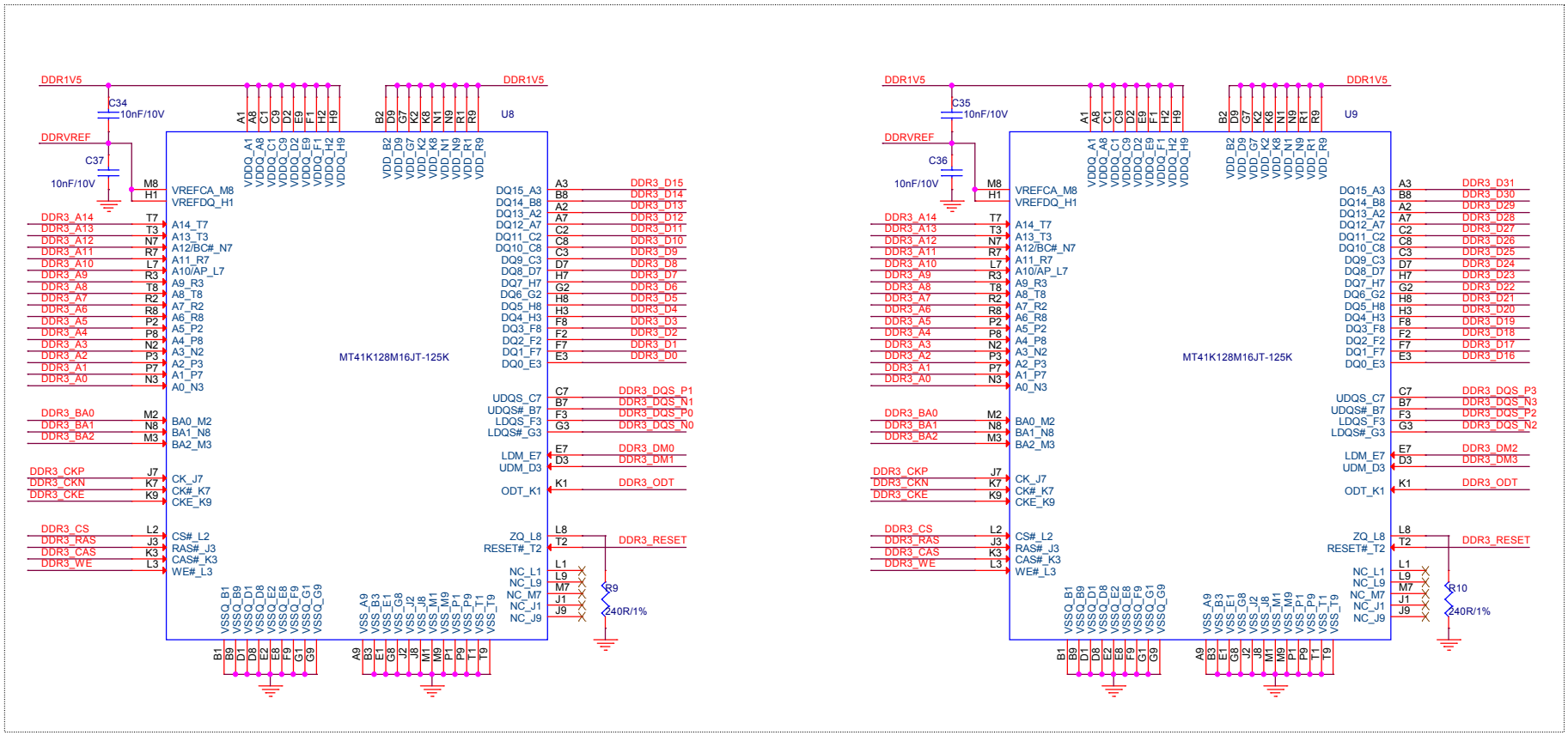
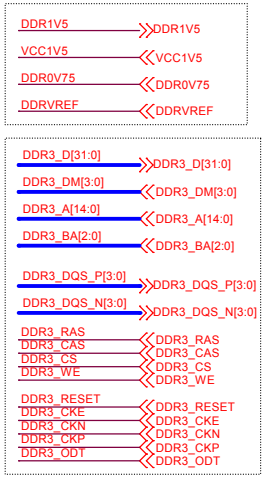
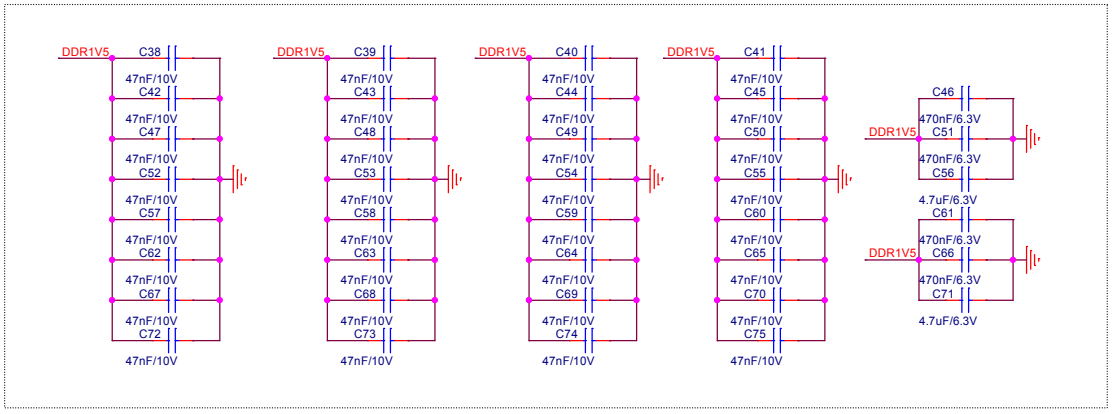
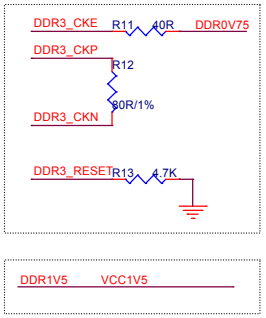
DDR3 SDRAM









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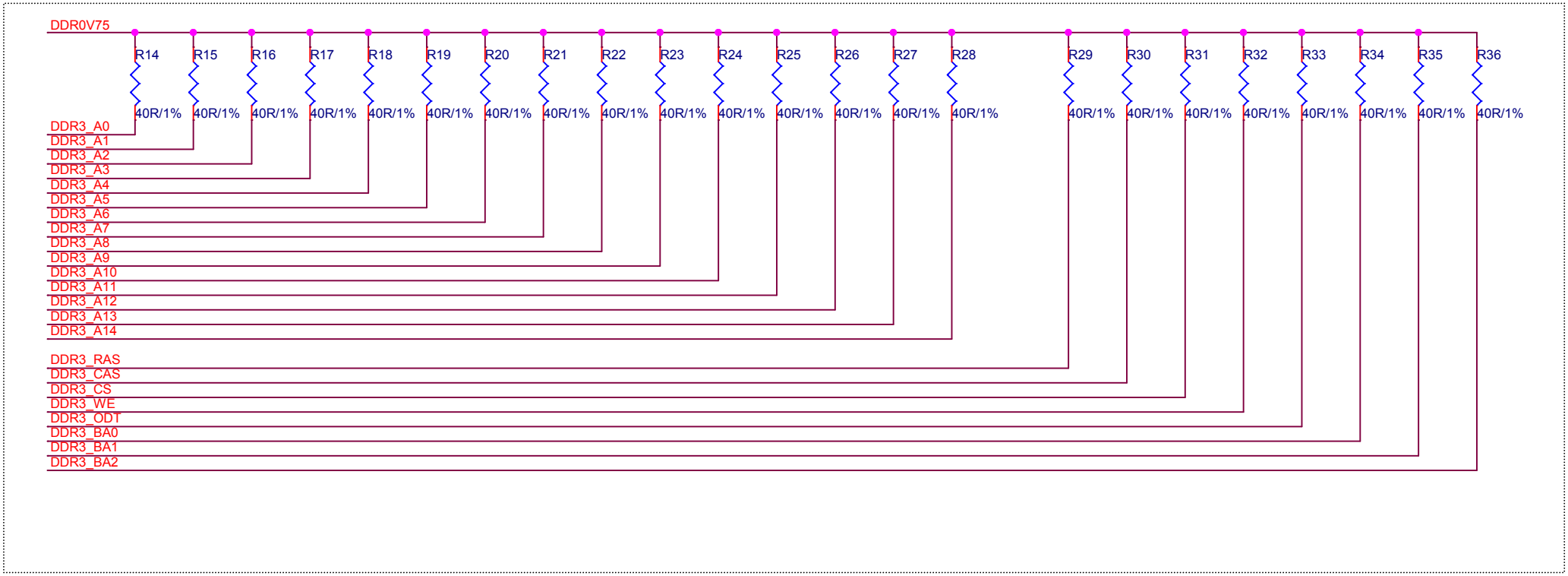
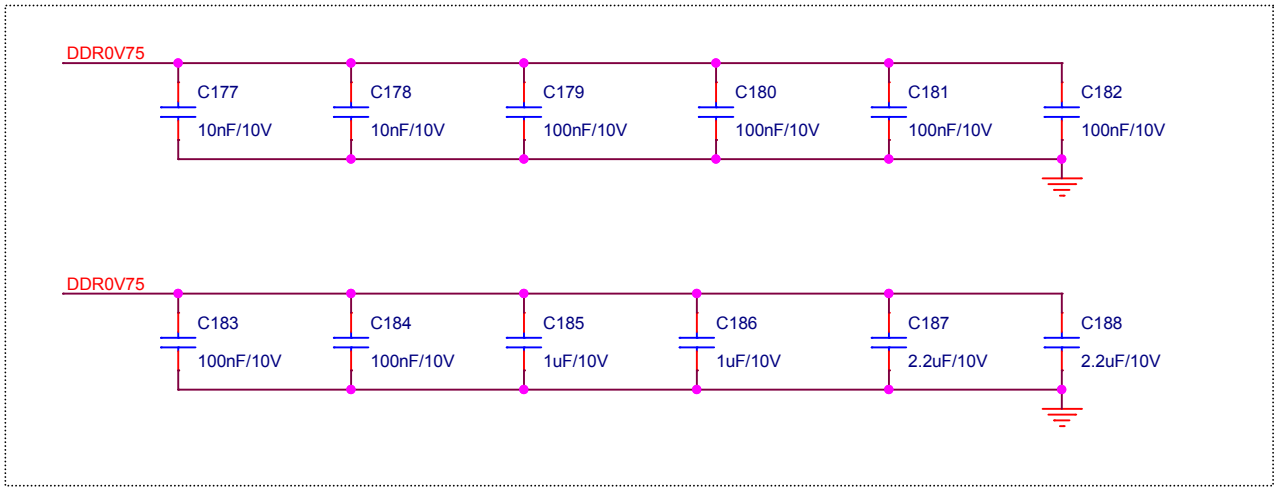
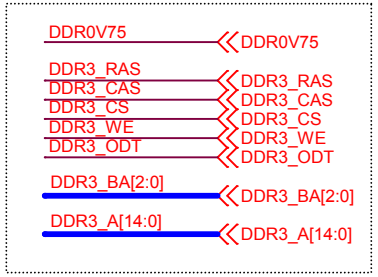
DDR3 TERMINATION







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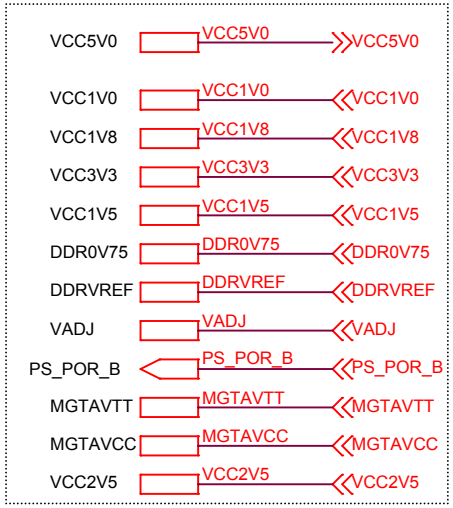
POWER TO TOP



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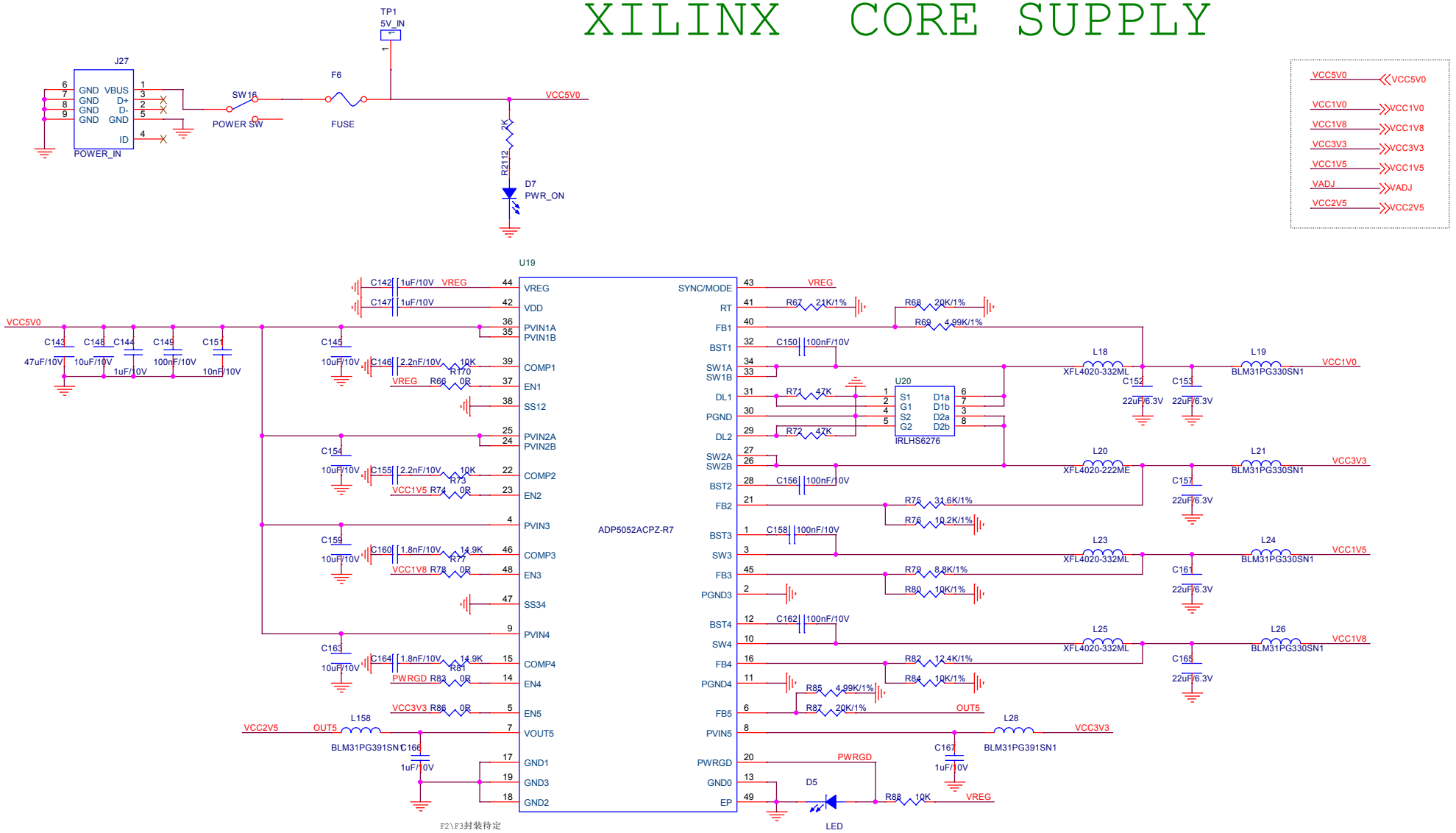
XILINX CORE SUPPLY



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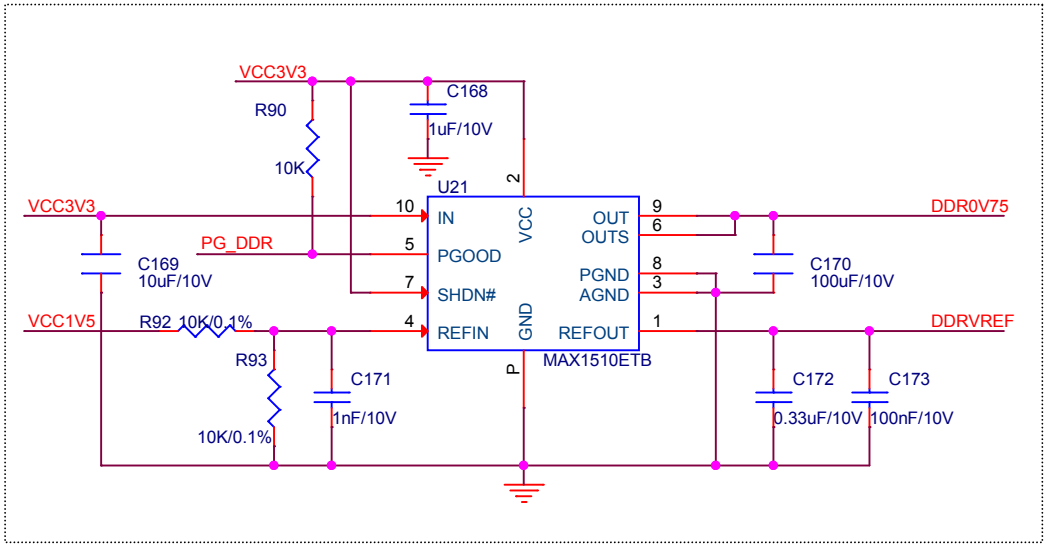
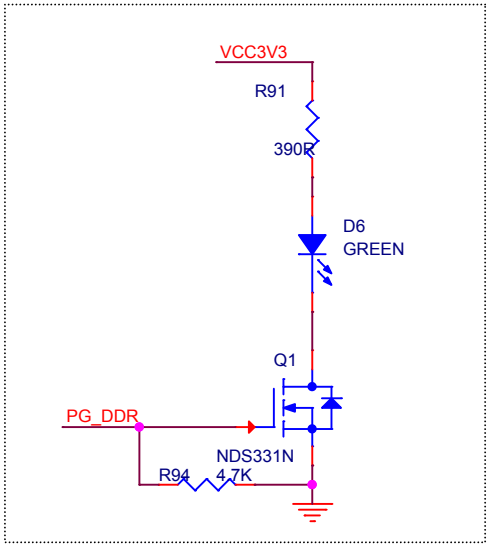
DDR3 SUPPLY

VCC1V5 <<VCC1V5
VCC3V3 <<VCC3V3

DDR0V75 >>DDR0V75
DDRVREF >>DDRVREF
PS_POR_B >>PS_POR_B

PG_DDR R89 0R PS_POR_B





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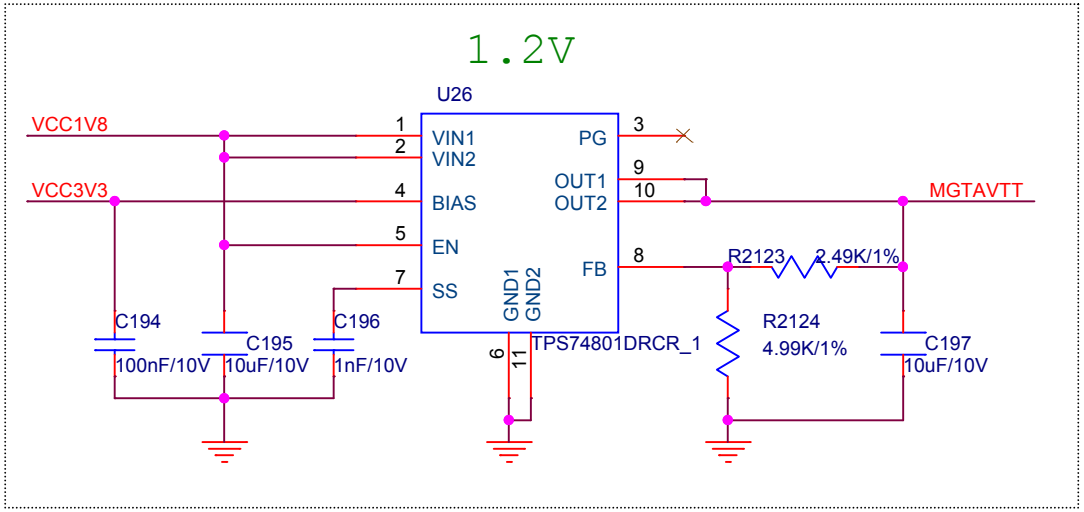
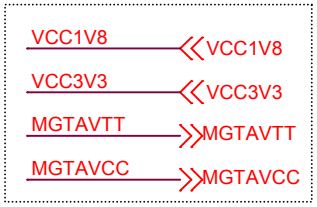
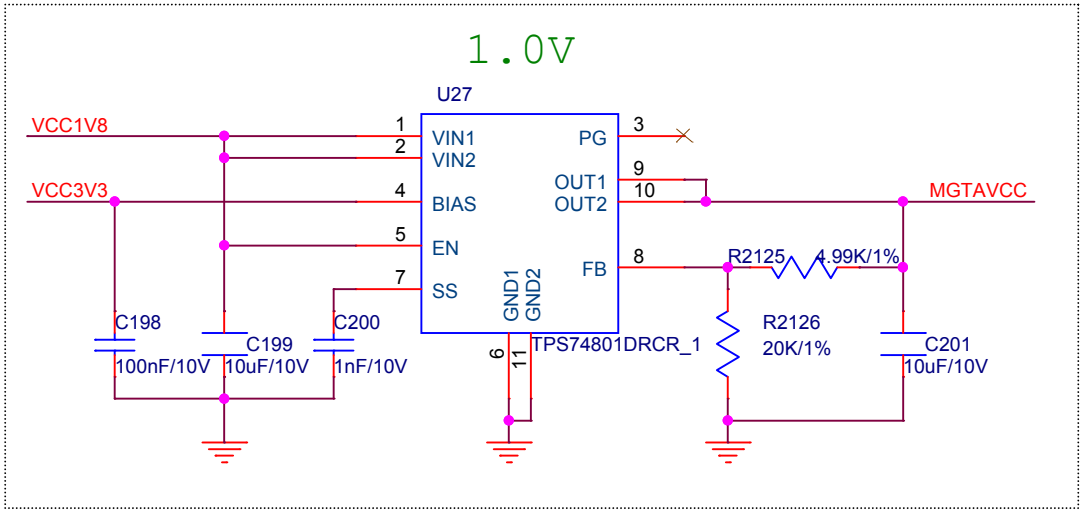
MGT POWER







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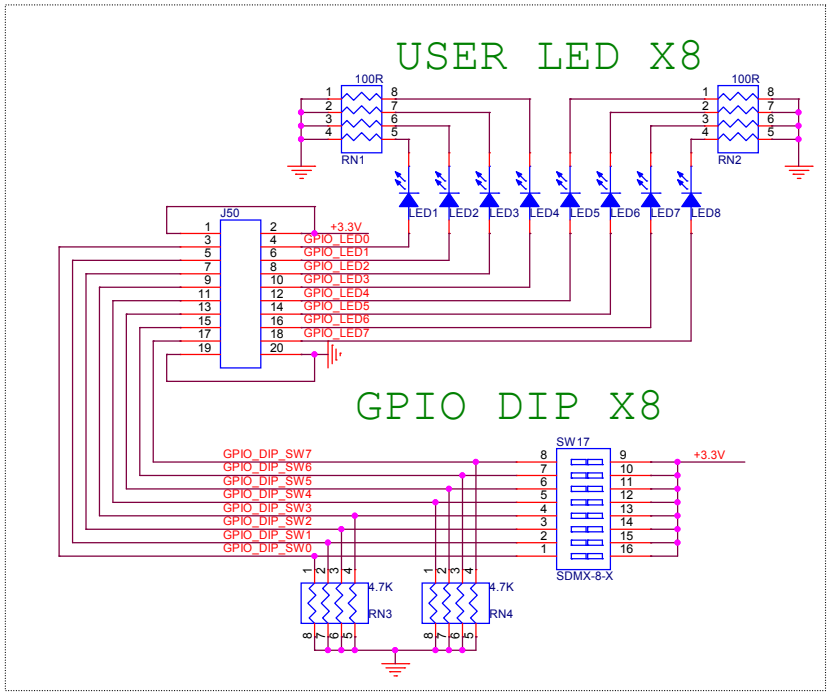
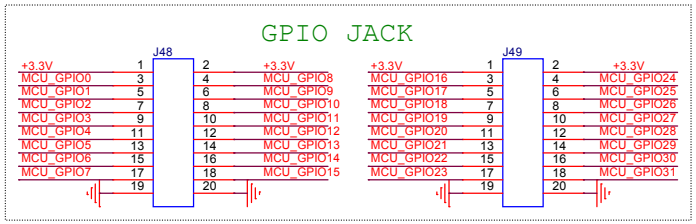
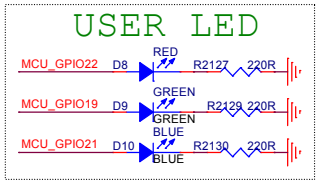
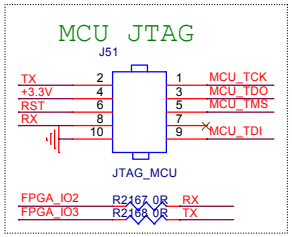
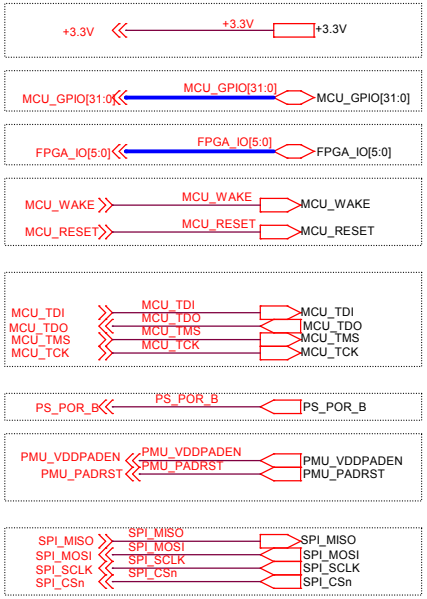
USER INTERACTIVE

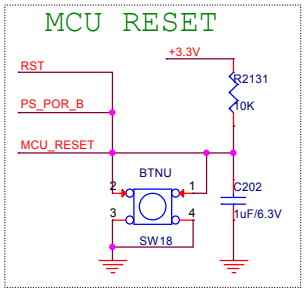


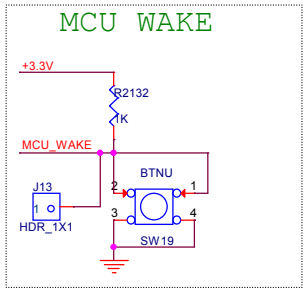


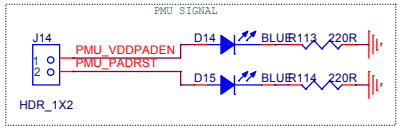


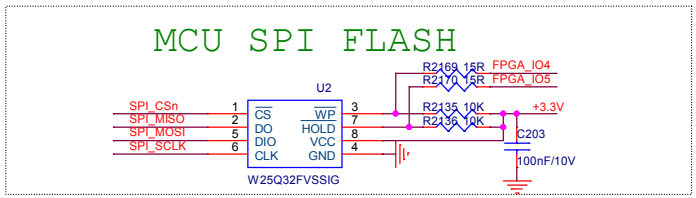












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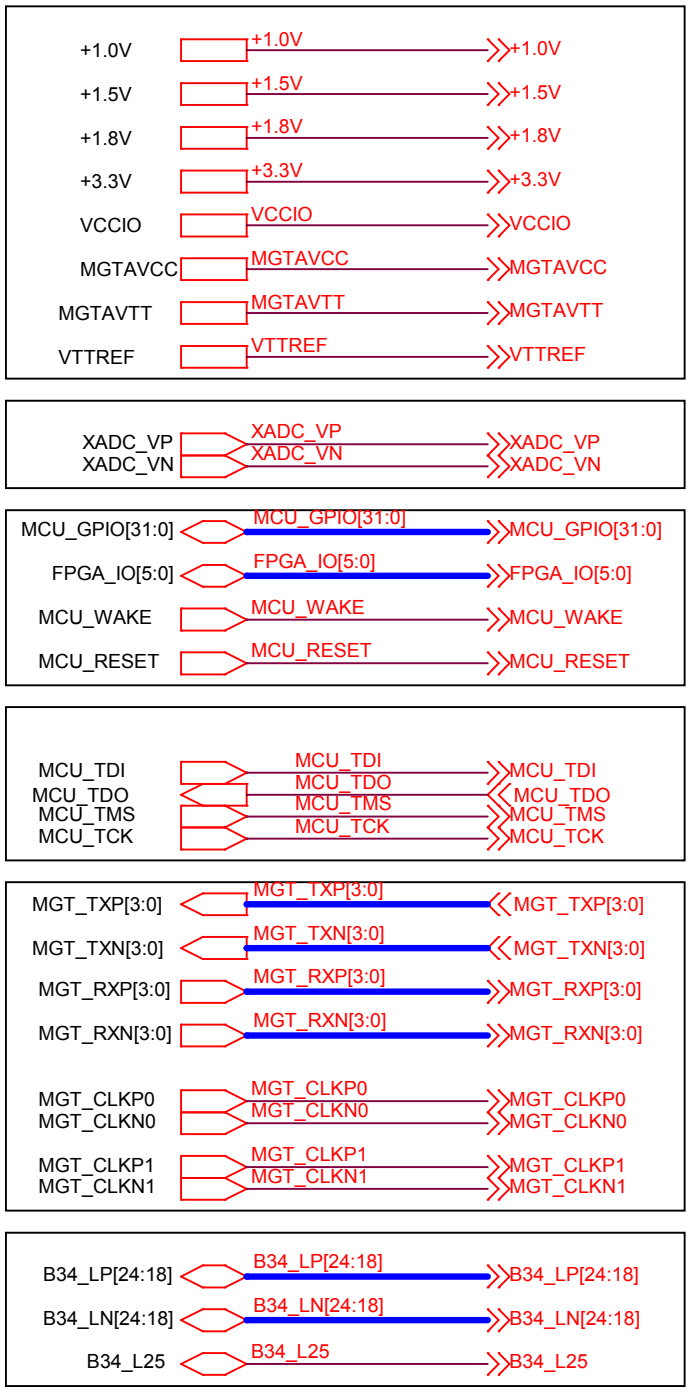
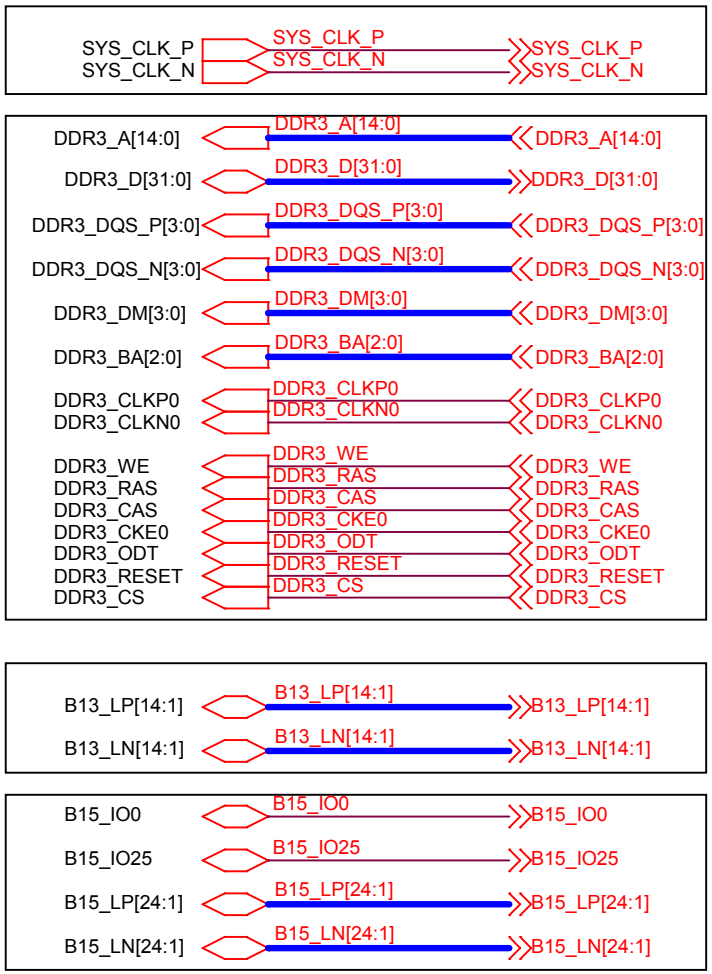
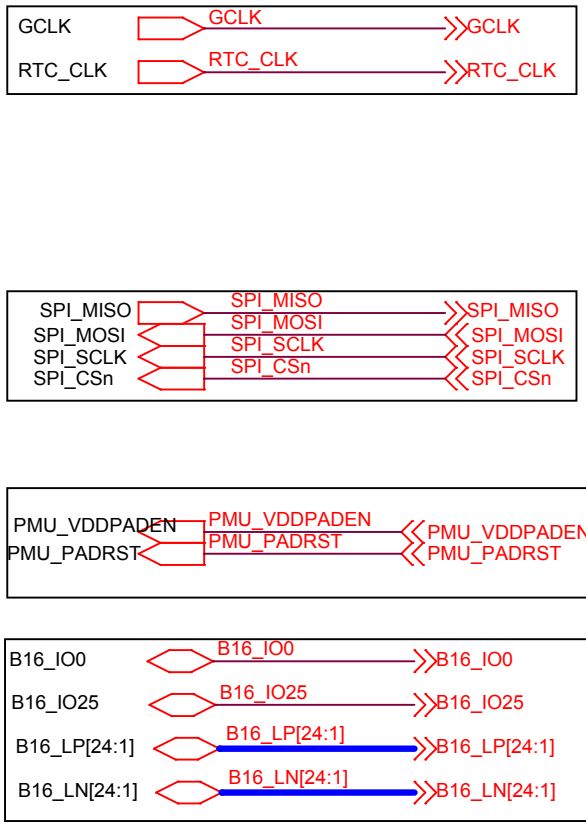
XC7A100T PORT TO TOP







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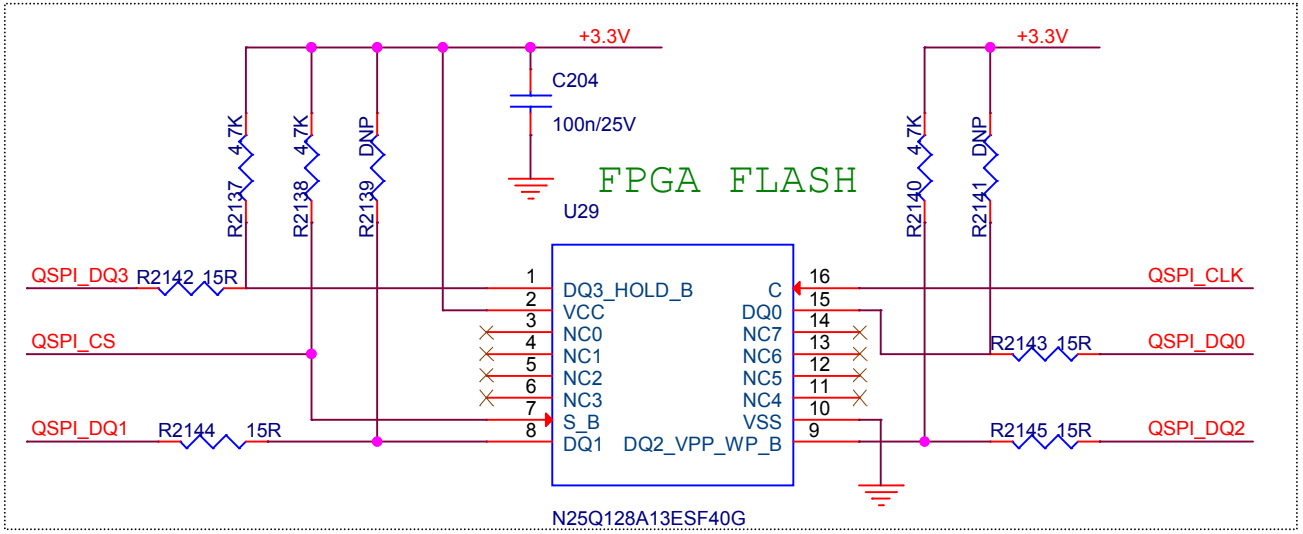
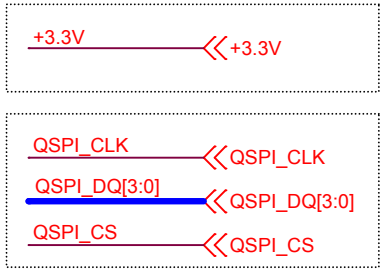
FPGA & MCU CONFIG





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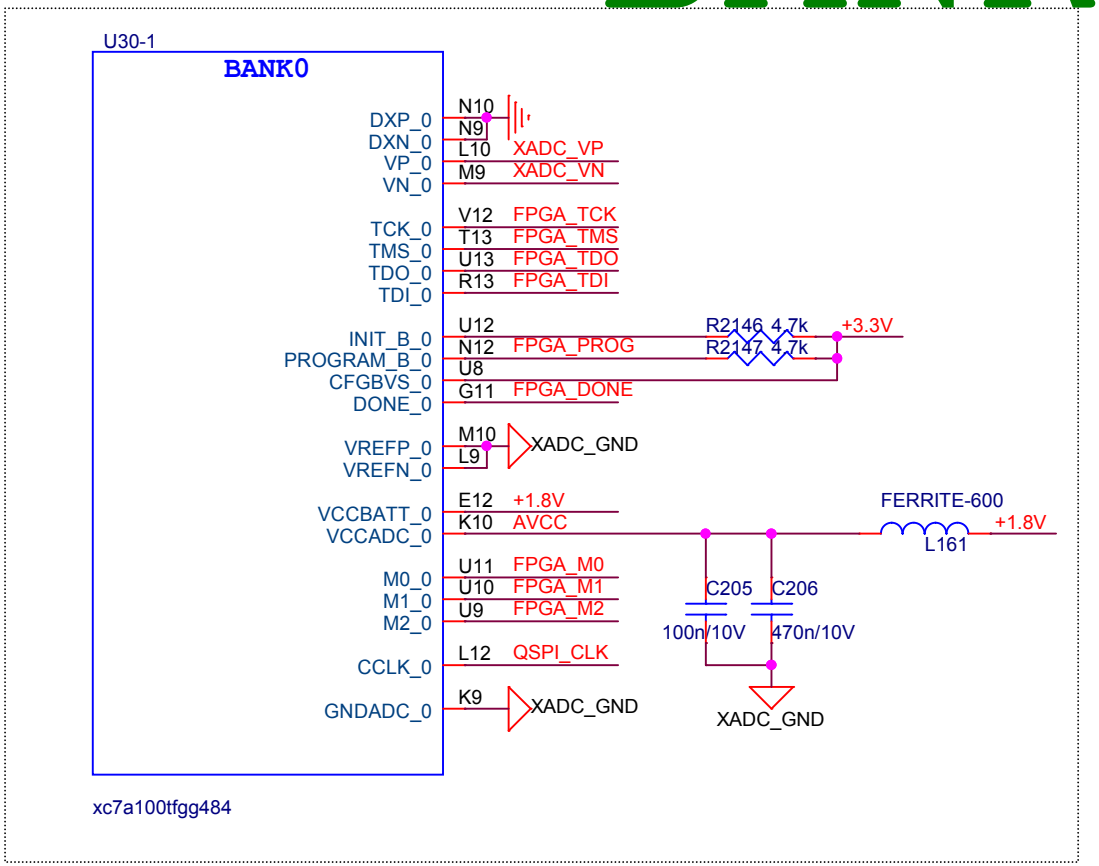

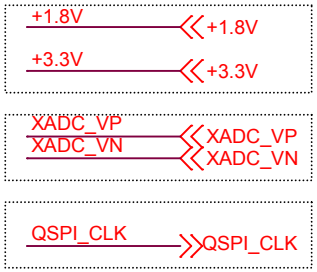

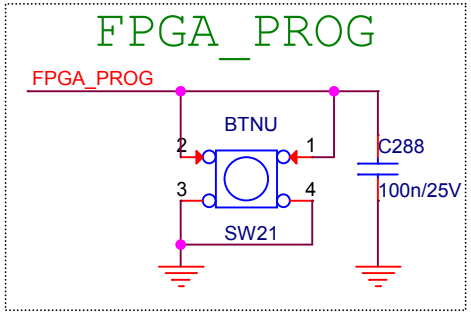

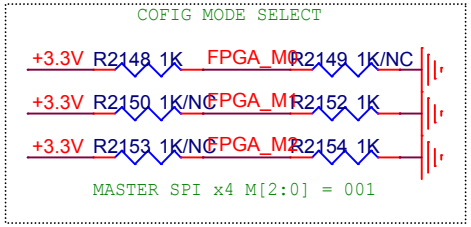

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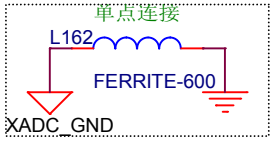

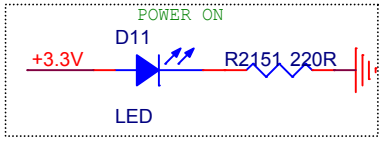

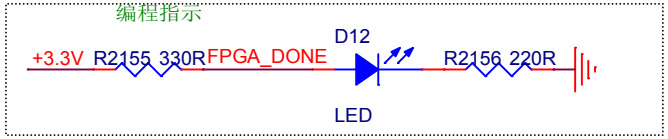

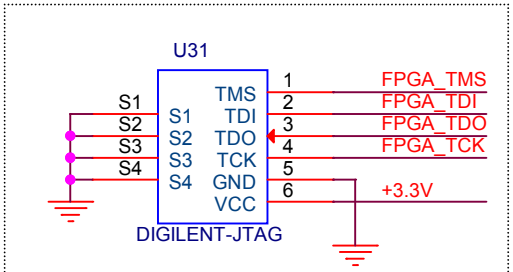

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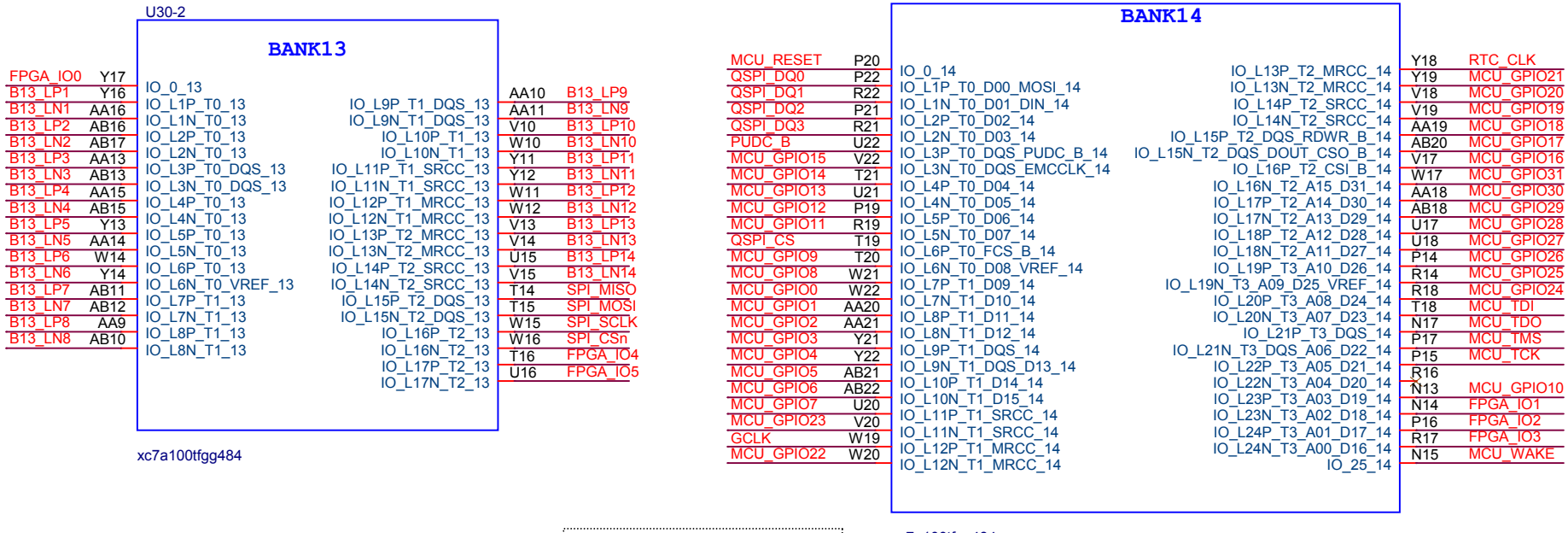

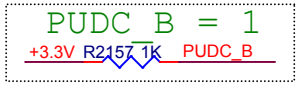

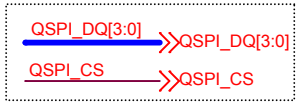

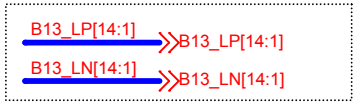

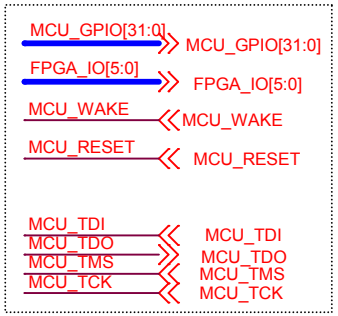

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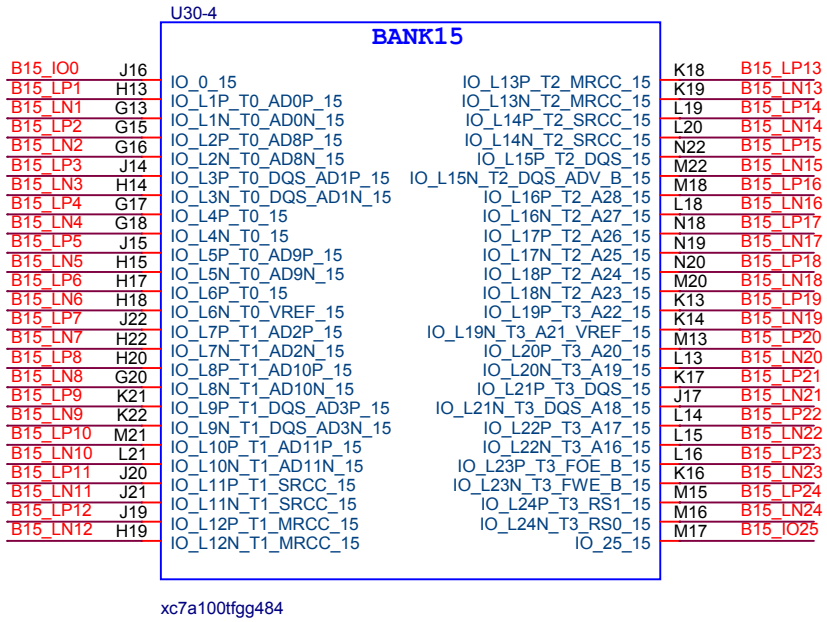

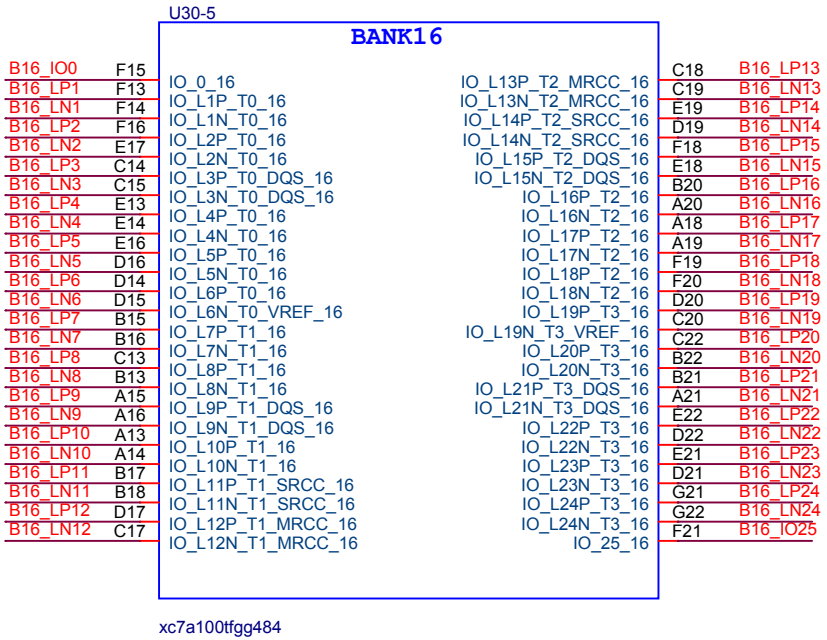

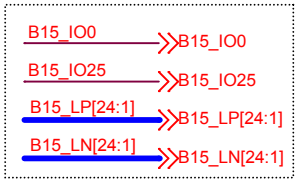

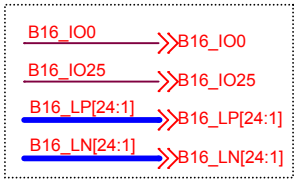

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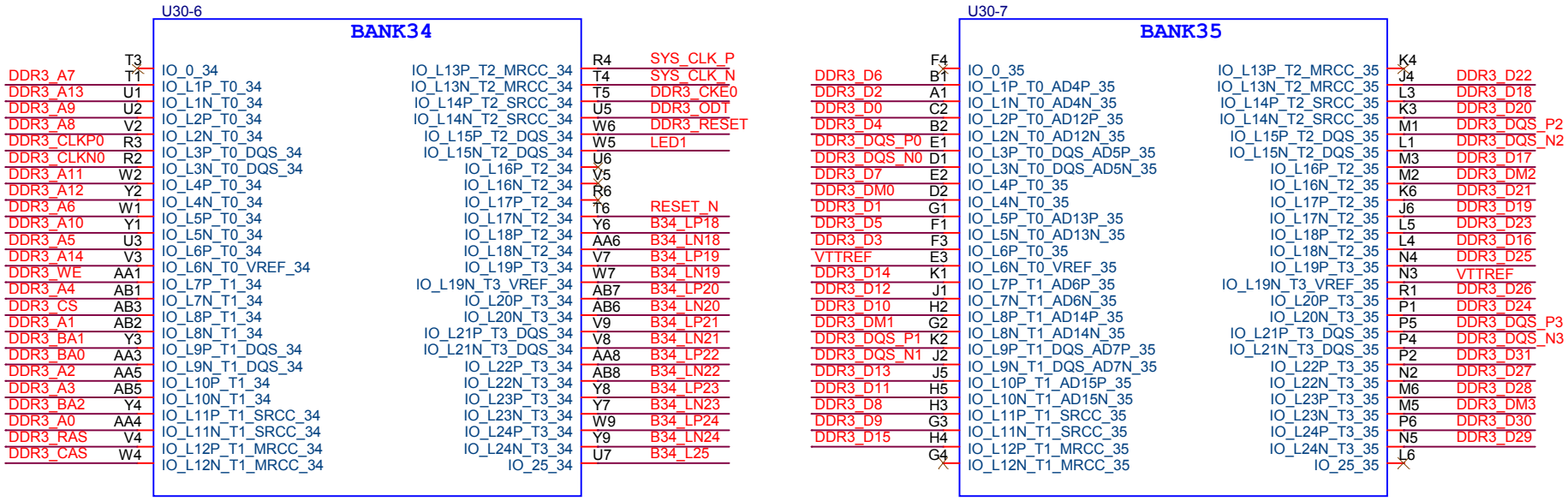

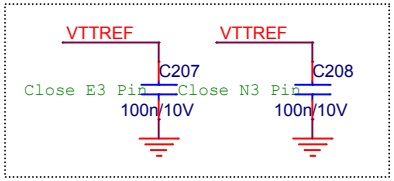

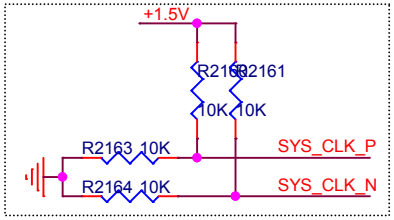

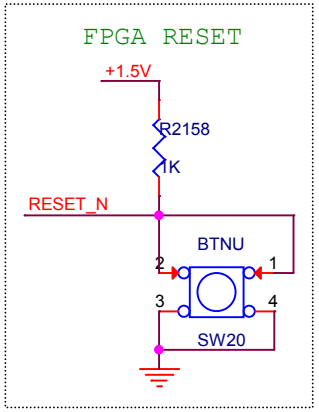

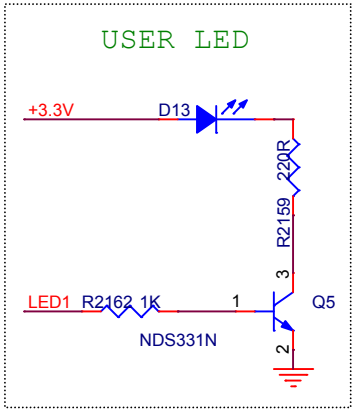

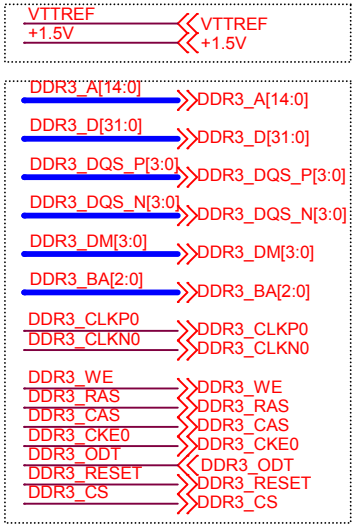

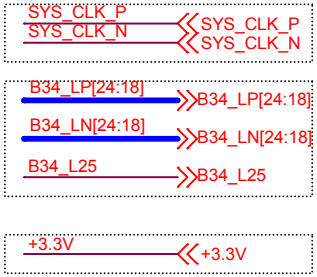

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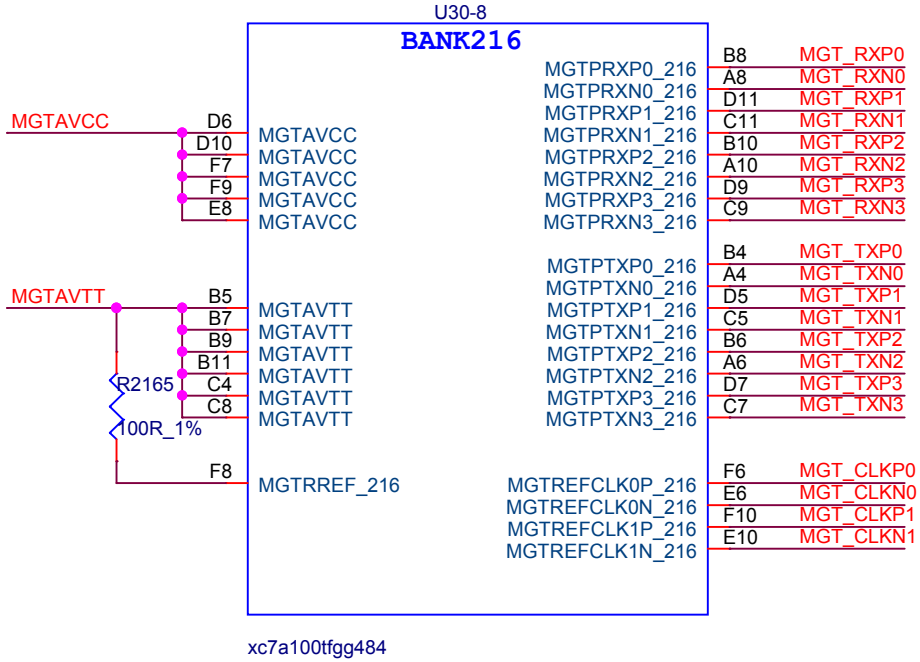

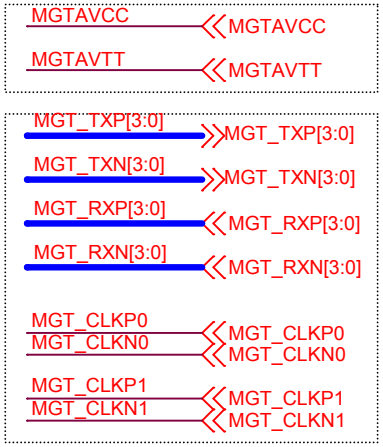

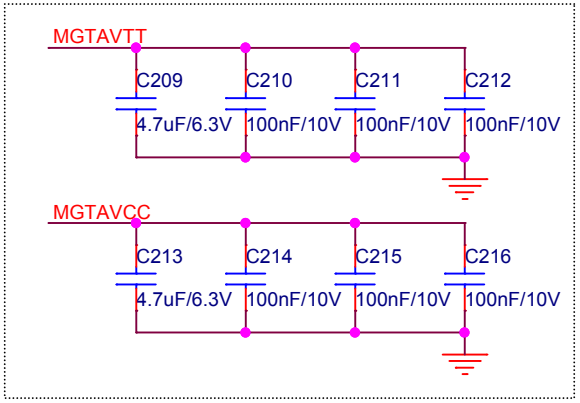

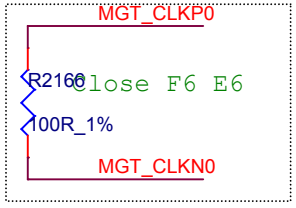

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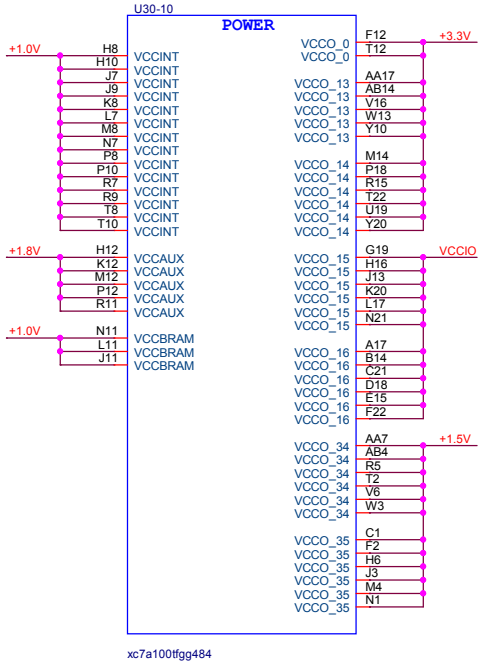

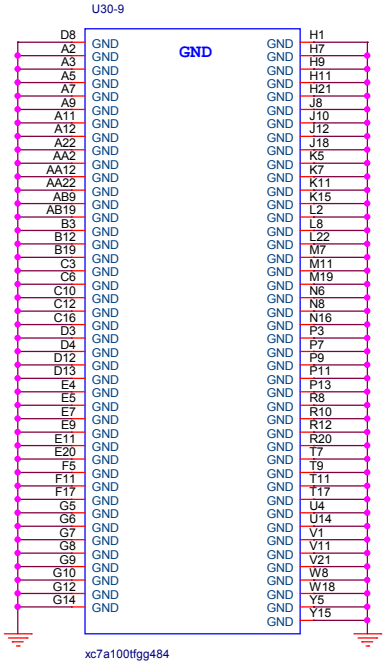

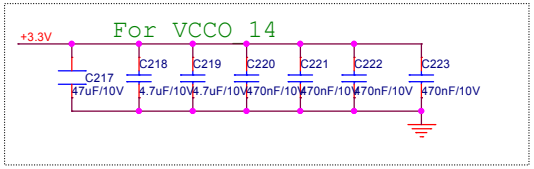

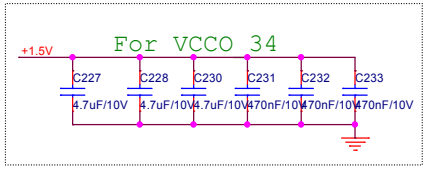

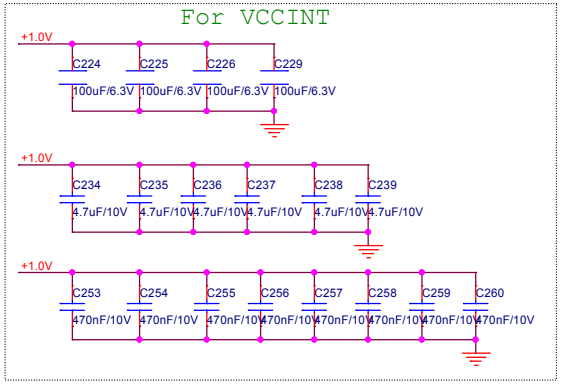

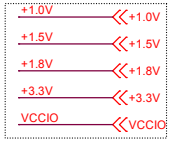

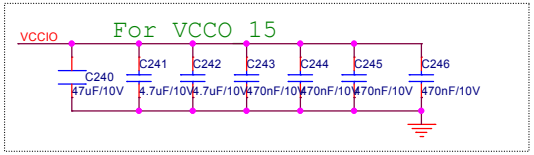

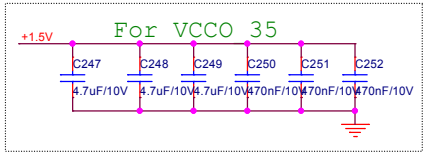

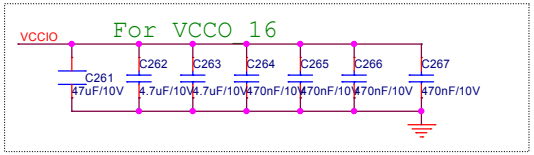

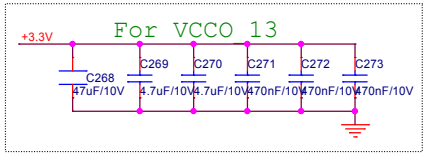

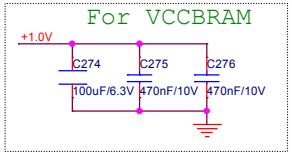

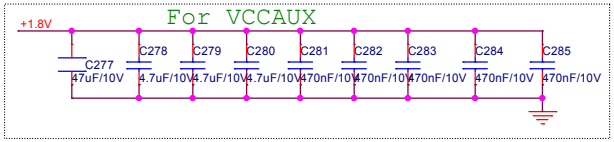

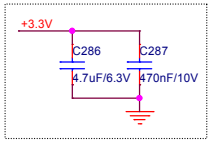

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