3-10 Traversability

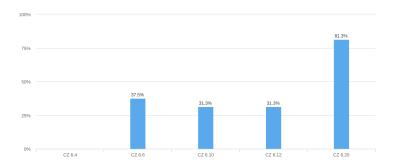
(Part I: Eulerian Graphs)

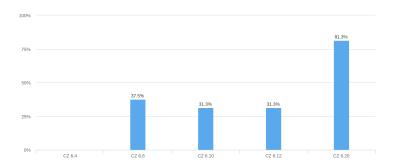
Hengfeng Wei

hfwei@nju.edu.cn

December 03, 2018







CZ 6.20 (Next Class)

这次习题相对简单【因为是必选的所以随机选了一个】,希望老师可以多担顾一下课本内容,比如哈密尔顿医的各种充分条件和证明,对你密尔顿医加敦达现的用法做一些拓展	
改拉爾和汉密尔朝德的联系 就是在建模时如何确定图的节点和边	
g-cage 对于不同大小而言都是唯一的吗?(书上只给到n=8) Kirkman triple system 和 factorization 的等价推导 Theorem 8.10的证明	暫无, 我就来抽个奖
5.3,不想看书,看白闭了,希望老鲜可以直接讲一下	none
如何打印联拉周路成拉速哈密顿回路	定理6.12的证明
定劑6.5	瓘(^-x-^)
$\overline{\mathcal{X}}$	希望能讲一下fluery算法●
智无	可以总结一下证明的方法,其实每次都可以这样,不一定要课上讲,可以整理之后做成讲义课后发,比如怎么证明有败拉回路等等
${f x}$	

FLEURY

2次习题相对简单【因为是必选的所以随机选了一个】,希望老鲜可以多问顾一下谋本内容,比如哈密尔顿图的各种充分条件和证明。 特密尔·顿图和政位则的用法做一些拓展	
放放图和汉密尔顿图的联系 就是在建模时如何确定图的节点和边	
r-cage 对于不同大小而言都是唯一的吗?(书上只给到n=8) Kirkman triple system 和 factorization 的等价推导 Theorem 8.10的证明	暫无,我就来抽个奖
13, 不想看书,看白闭了,希望老师可以直接讲一下	none
11/9打印欧拉国路	定理6.12的证明
定理6.5	曜(^, , , ^)
E	希望能讲一下和uery算法●
開 无	可以总结一下证明的方法,其实每次都可以这样,不一定要课上讲,可以整理之后做成讲文课后发,比如怎么证明有收拉回路等等

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13, 不想看书,看白闭了,希望老师可以直接讲一下	none
即科打印欧拉国路·欧拉连哈密顿国路	定理6.12的证明
5州6.5	職(^-, -, ^)
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FLEURY (HIERHOLZER)

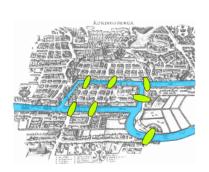
Chinese Postman Problem (Next Class)

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FLEURY (HIERHOLZER)

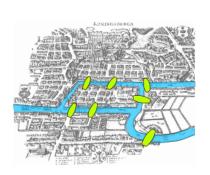
Chinese Postman Problem (Next Class)

6.3 Exploration & 6.4 Excursion (Not Required)





Leonhard Euler (1707 – 1783)





Leonhard Euler (1707 – 1783)

Graph Theory Topology













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By strong mathematical induction on the number m of edges of G.

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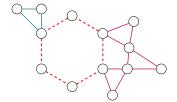
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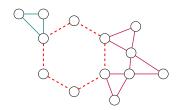
$$H = G - E(C) = \bigcup H_i$$

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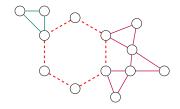
$$H = G - E(C) = \bigcup H_i$$

- (I) $\forall v \in H : \deg(v)$ is even
- (II) $\forall i : \left| E(H_i) \right| < m$



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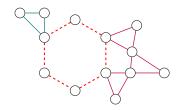
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By I.H., each H_i has an Eulerian circuit C_i .

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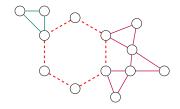


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$$\forall i: V(H_i) \cap V(C) \neq \emptyset$$

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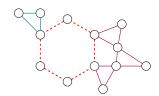


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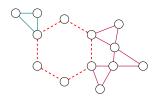
$$\forall i: V(H_i) \cap V(C) \neq \emptyset$$

Glue together each C_i with C to get an Eulerian circuit of G.

- $u \in V(G)$
- 3: $C \leftarrow \text{ any circuit } u \sim u \text{ in } G$
- 4: while $\exists v \in C : \deg(v) > 0$ do
- 5: $H \leftarrow G E(C)$
- 6: $v \leftarrow \text{any vertex in } V(C) \text{ such that } \deg(v) > 0$
- 7: $C' \leftarrow \text{ any circuit } v \sim v \text{ in } H$
- 8: $C \leftarrow C \otimes C'$ \triangleright Glue $C' = v \sim v$ with C via v
- 9: $\mathbf{return} \ C$

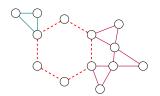


- $2: u \in V(G)$
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Q: Time Complexity?

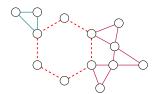
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Q: Time Complexity?

Q: Data Structures?

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Q: Time Complexity?

Q: Data Structures?

O(m): Using doubly linked list

- (I) $v_0 \in V(G)$; $C_0 = v_0$
- (II) Suppose $C_i = v_0 e_1 v_1 e_2 \cdots e_i v_i$.

Choose e_{i+1} from $E(G) - \{e_1, e_2, \cdots, e_i\}$ as follows:

- (a) e_{i+1} is incident with v_i
- (b) Unless there is no alternative, e_{i+1} is not a bridge of $G \{e_1, e_2, \dots, e_i\}$
- (III) Stop when step (II) can no longer be implemented

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At any stage, v_i is incident with ≤ 1 bridge in $E(G) - \{e_1, e_2, \cdots, e_i\}$.

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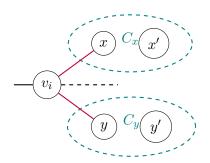
By Contradiction.

Suppose that v_i is incident with ≥ 2 bridges in $E(G) - \{e_1, e_2, \cdots, e_i\}$.

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By Contradiction.

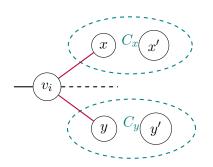
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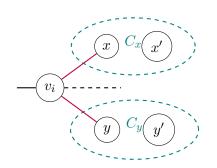


 $\exists x' \in C_x : \deg(x) \text{ is odd}$

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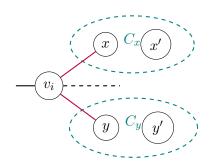


 $\exists x' \in C_x : \deg(x) \text{ is odd}$ $\exists y' \in C_y : \deg(y) \text{ is odd}$

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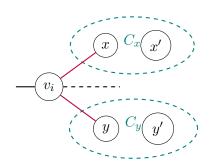
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We have found 2 odd vertices.

At any stage, v_i is incident with ≤ 1 bridge in $E(G) - \{e_1, e_2, \cdots, e_i\}$.

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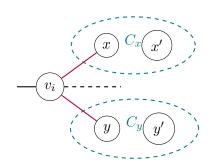
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Q: What is the contradiction?

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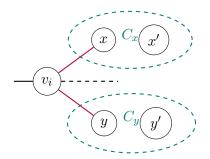


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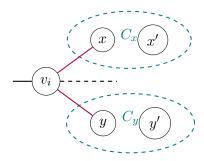
We have found 2 odd vertices.

Q: What is the contradiction?

Is $deg(v_i)$ odd or even?

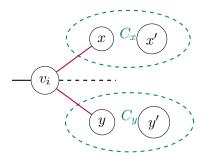


Is $deg(v_i)$ odd or even?



Case I: $deg(v_i)$ is odd.

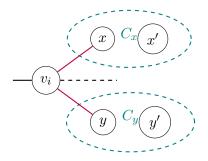
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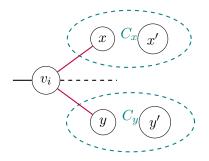


Is $deg(v_i)$ odd or even?

Case I: $deg(v_i)$ is odd.

Contradiction:

Only v_0 and v_i can have odd degrees!



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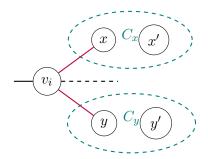
Case I: $deg(v_i)$ is odd.

CHOL II.

CASE II: $deg(v_i)$ is even.

Contradiction:

Only v_0 and v_i can have odd degrees!



Is $deg(v_i)$ odd or even?

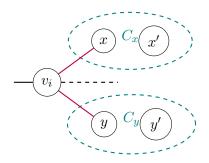
Case I: $deg(v_i)$ is odd.

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CASE II: $deg(v_i)$ is even.

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Is $deg(v_i)$ odd or even?

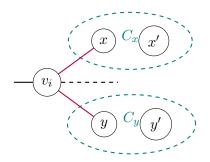
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Is $deg(v_i)$ odd or even?

Case I: $deg(v_i)$ is odd.

Contradiction:

CASE II: $deg(v_i)$ is even.

 $v_i = v_0$

Only v_0 and v_i can have odd degrees!

Contradiction: No odd vertices!

1: **procedure** FLEURY(G)

2: $v_0 \in V(G)$

3: $C \leftarrow v_0$

4: $i \leftarrow 0, \ V_0 \leftarrow V(G), \ E_0 \leftarrow E(G)$

▶ Choose any starting vertex

 \triangleright Keep track of the circuit

1: **procedure** FLEURY(G)

2: $v_0 \in V(G)$

3: $C \leftarrow v_0$

4: $i \leftarrow 0, \ V_0 \leftarrow V(G), \ E_0 \leftarrow E(G)$

5: while $deg(v_i) > 0$ in E_i do

 \triangleright Choose any starting vertex

▶ Keep track of the circuit

 \triangleright Stop otherwise

15: $\mathbf{return} \ C$

```
1: procedure FLEURY(G)
```

 $2: v_0 \in V(G)$

▶ Choose any starting vertex

 $C \leftarrow v_0$

- ▶ Keep track of the circuit
- 4: $i \leftarrow 0, \ V_0 \leftarrow V(G), \ E_0 \leftarrow E(G)$
- 5: while $deg(v_i) > 0$ in E_i do

- ▶ Stop otherwise
- if $deg(v_i) = 1$ in E_i then
- ▶ No alternative: go the bridge

- 7: $e_{i+1} \triangleq v_i v_{i+1}$
- 8:

6:

9: else

- ▶ Have alternatives: don't go the bridge
- Choose $e_{i+1} \triangleq v_i v_{i+1}$ that is not a bridge of (V_i, E_i)

10: 11:

15: $\mathbf{return} \ C$

```
1: procedure FLEURY(G)
```

 $2: v_0 \in V(G)$

Choose any starting vertexKeep track of the circuit

- $C \leftarrow v_0$
 - $i \leftarrow 0, V_0 \leftarrow V(G), E_0 \leftarrow E(G)$
- 5: while $deg(v_i) > 0$ in E_i do
- ▶ Stop otherwise▶ No alternative: go the bridge
- if $deg(v_i) = 1$ in E_i then
 - $e_{i+1} \triangleq v_i v_{i+1}$

7: 8:

6:

4:

9: else

- ▶ Have alternatives: don't go the bridge
- Choose $e_{i+1} \triangleq v_i v_{i+1}$ that is not a bridge of (V_i, E_i)

- 10: 11:
- 12: $C \leftarrow Ce_{i+1}v_{i+1}$
- 13: $E_{i+1} \leftarrow E_i \{e_{i+1}\}$
- 14: $i \leftarrow i + 1$
- 15: $\mathbf{return} \ C$

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        while deg(v_i) > 0 in E_i do
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             if deg(v_i) = 1 in E_i then
                                                   ▶ No alternative: go the bridge
 6:
                 e_{i+1} \triangleq v_i v_{i+1}
 7:
                 V_{i+1} \leftarrow V_i - \{v_i\}
                                                     \triangleright Delete the isolated vertex v_i
 8:
             else
                                        ▶ Have alternatives: don't go the bridge
 9:
                 Choose e_{i+1} \triangleq v_i v_{i+1} that is not a bridge of (V_i, E_i)
10:
                 V_{i\perp 1} \leftarrow V_i
                                                     ▶ No isolated vertex produced
11:
             C \leftarrow Ce_{i+1}v_{i+1}
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             E_{i+1} \leftarrow E_i - \{e_{i+1}\}
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             i \leftarrow i + 1
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return C

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We need to prove that

We need to prove that C returned by Fleury is an Eulerian circuit.

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Definition (Eulerian Circuit)

A connected graph is Eulerian if there exists a closed trail that includes every edge of G.

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Trail

Include every edge of G

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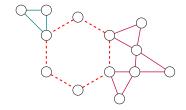
Closed

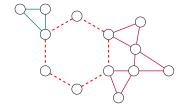
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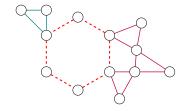
By Contradiction.





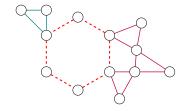
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Include every edge of GBy Contradiction.



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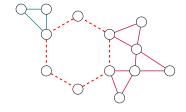
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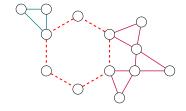
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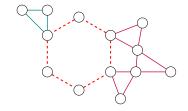


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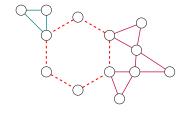
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Impossible:





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 $G|_{E'}$ is disconnected from v_0

Impossible:

- Don't go the bridge unless there is no alternative.
- (II) Delete the isolated vertex left by going the bridge.





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