

Sheet1

GENERAL BUS LAYOUT					
A-row			C-row		
1	GND		1	GND	
2	GND		2	GND	
3	AUX#1		3	AUX#2	
4	B_SYS		4	XFEXX	
5	DATA 0		5	DATA 1	
6	DATA 2		6	DATA 3	
7	DATA 4		7	DATA 5	
8	DATA 6		8	DATA 7	
9	ADDRESS 0		9	ADDRESS 1	
10	ADDRESS 2		10	ADDRESS 3	
11	ADDRESS 4		11	ADDRESS 5	
12	ADDRESS 6		12	ADDRESS 7	
13	ADDRESS 8		13	ADDRESS 9	
14	ADDRESS 10		14	ADDRESS 11	
15	ADDRESS 12		15	ADDRESS 13	
16	ADDRESS 14		16	ADDRESS 15	
17	ADDRESS 16		17	ADDRESS 17	
18	ADDRESS 18		18	ADDRESS 19	
19	GND		19	GND	
20	E CLOCK		20	R/ \bar{W}	
21	RESET		21	VMA	
22	BS		22	BA	
23	Q CLOCK		23	AUX#5	
24	R/ \bar{W} DMA(IN)		24	MRDY	
25	\bar{HALT}		25	BREQ	
26	\bar{FIRQ}		26	IRQ	
27	\bar{NMI}		27	VMA_DMA(IN)	
28	AUX#6		28	AUX#7	
29	SIGNAL#1		29	SIGNAL#1	
30	SIGNAL#2 (SIG30)		30	SIGNAL#2 (SIG30)	
31	+5V		31	+5V	
32	+5V		32	+5V	
Note:	SIGNAL#1 and SIGNAL#2 may be used to pass +12V/-12V/+3V3				
	or master BAUD clock over the bus				
	Signals Aux#x are per backplane slot defineable				
	Be careful when you have a mix of these signals on the backplane!!				

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[illegible]