



J6: OFF no effect, ON disable addressdecode on board when bus C4 is pulled low

J7: OFF no effect, mid with SYS, DIV4 (bus A4) represents 'SYSTEM' state, mid with DIV4, DIV4 (bus A4) is the IO decoding

J5: RA1 is PROM(U8) A12. Open: A12 is high, closed: A12 is low

J4: RA2 is PROM(U8) A11. Open: A11 is high, closed: A11 is low

J8: OFF no effect, ON disable access to F000-FFFF in 'USER' state

J3: OFF, ACTA CTS is controlled from com port, ON, CTS is always active