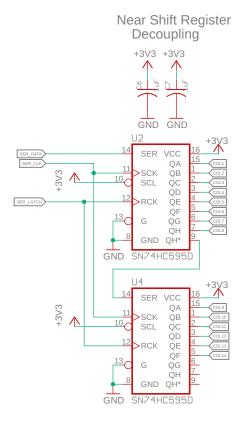
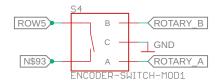


## Shift Regtisters & MISC

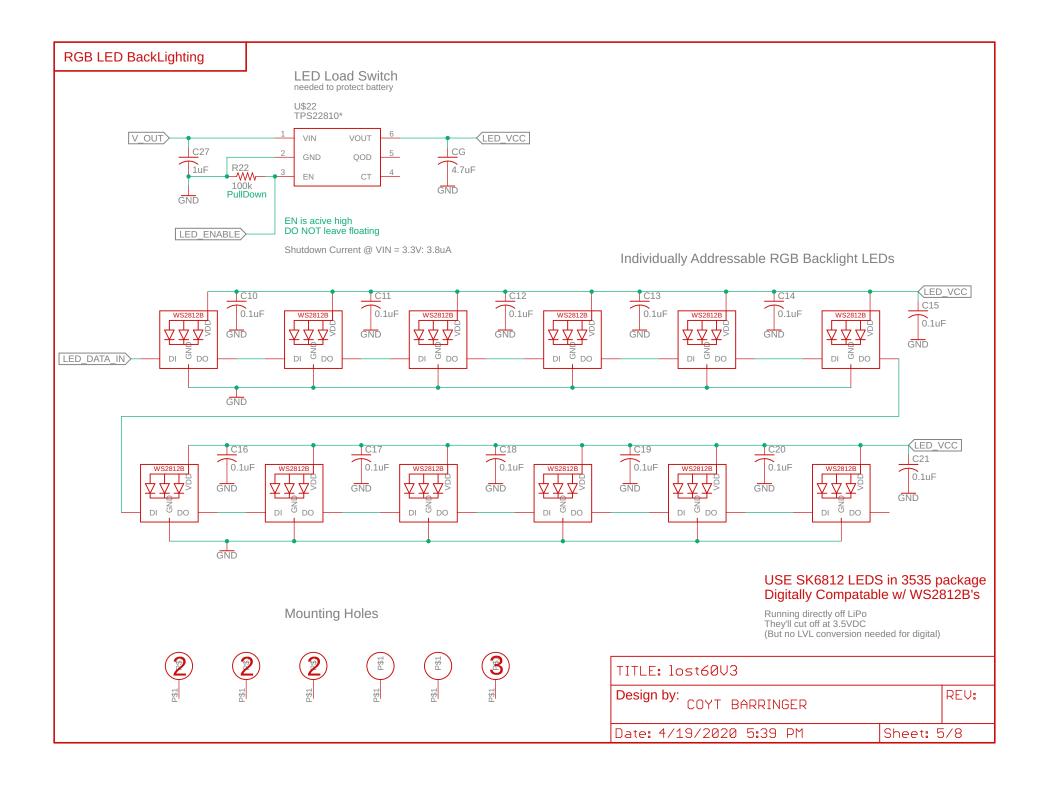




Worst case low power: System ON Mode, full RAM retention = 2.35uA

All unused I/O broken out to test pads for testing, other uses, etc...

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## **Battery & Power Handling**

## Lithium Battery Charger & PowerPath Management

#### Connect an actual voltage divider here R23 = 49.9k Ohm and CORRECTED\_R1 = 200k Ohm IN OUT V\_RAW BAT TS EN2 EN1 SYSOFF TMR SYSOFF ISET CE ILIM TMR: Safety Time GND to Disable

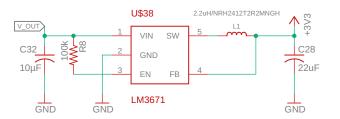
VSS EP

BQ24075

CHG

PGOOD

## Main Voltage Regulator

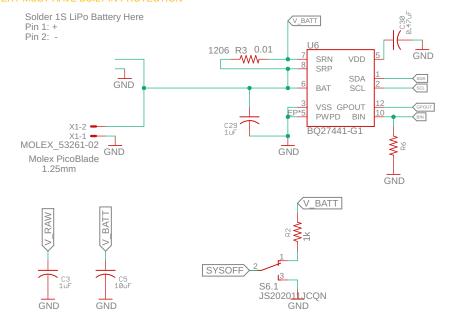


#### BATTERY MUST HAVE BUILT IN PROTECTION

GND

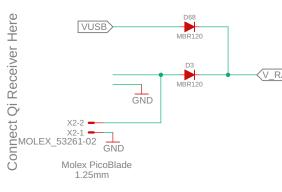
R24 0

Lithium Batt State Of Charge Gauge



# MBR120 D3 MBR120 V RAW

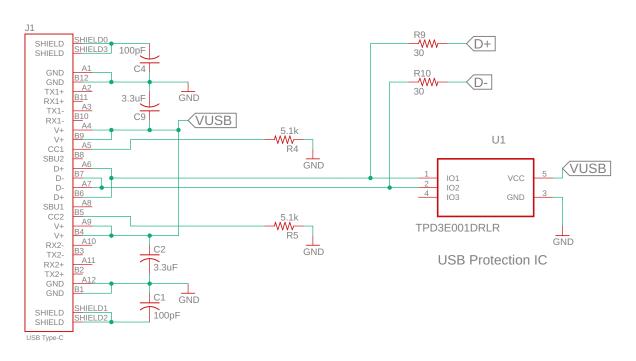
External Power Source Combination





## USB C Interface

# Connector: JAE DX07S024JJ2R1300



See "Tristan Robitaille" website for interface details

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## **LED Backlight Driver** T1 BC848 T2 BC848 T3 BC848 T4 BC848 T5 BC848 5.1k **-WW** R14 MCU\_LR4 5.1k R15 5.1k **WW** R16 MCU\_LR1 MCU\_LR3 MCU\_LR5 U5 VCC OUT0 OUT1 OUT2 OUT3 OUT4 OUT5 OUT6 OUT7 OUT8 OUT9 OUT10 SCLK SIN LAT BLANK IREF GND OUT11 OUT12 OUT13 OUT14 OUT15 GND GND 22 SOUT TLC59281DBQR TITLE: lost60V3 Design by: COYT BARRINGER REV:

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