

18CSC203J_COA_CT1

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MCQ Questions (25*1=25)

Answer all the Questions

When adding 0 1 1 1 0 0 1 1 and 1 1 1 0 1 1 0 0, as a result what will be values of the status flag? *

- ☐ Z=0,c=0,s=0,v=1
- ☐ Z=1,c=0,s=0,v=0
- ☐ Z=0, c=1,s=0, v=0
- ☐ Z=0, c=1, s=0, v=1



32MB of memory in a computer would require how many bits to address any single byte in aMemory? *

- ☐ 32 bits
- ☒ 25 bits
- ☐ 24 bits
- ☐ 25 bytes

Which addressing mode is being used in the given instruction?MOV AX, [1234H] *

- ☐ Register Addressing Mode
- ☐ Base Addressing Mode
- ☐ Direct Addressing Mode
- ☒ Immediate Addressing Mode



In Load R1,LOCA. Add R0, R1. Contents that would be overwritten are : *

- ☐ R1
- ☐ R0
- ☐ LOCA
- ☐ R0 and R1

When the ARM processor running in thumb state, what will be the size of the instructions? *

- ☐ 24
- ☒ 32
- ☐ 6
- ☐ 16



When you do division operation on two 16 bits numbers in 8086 microprocessor, which of the following registers are used to store the quotient and remainder? *

- ☐ AX and CX
- ☐ BX and DX
- ☐ AX and BX
- ☐ AX and DX

How many registers ARM processor possess? *

- ☐ 42
- ☐ 30
- ☐ 28
- ☐ 37



INC AX instruction increments the AX register contents by *

- ☐ 8
- ☐ 4
- ☐ 2
- ☐ 1

Which of the following segment is used for string operation and storing extra bit of data *

- ☐ Stack segment
- ☐ Data Segment
- ☐ Extra Segment
- ☐ Code Segment



In Big Endian format, of 64 bit data, what is the starting byte address of the aligned words? *

- ☐ 0,4,8
- ☐ 2,4,6
- ☐ 0,8,16
- ☐ 0,2,4

You are given the following instruction: ADD AX, [2024] You are provided the following data: DS = 3400H ; SS = 1200H ; CS = 4000H Find the effective address location for the given instruction. *

- ☐ 42024 H
- ☐ 36024 H
- ☐ 14024 H
- ☐ 44470 H



Which memory unit is used for reducing the access time from memory? *

- ☐ Secondary
- ☒ Cache
- ☐ DRAM
- ☐ SRAM

How many one address instructions are needed for evaluating $(A+B) \square (C+D)$? *

- ☐ 3
- ☐ 5
- ☐ 6
- ☐ 7



If AX=10001001 SHL AX,01 what will be output? *

- ☐ 00010000
- ☐ 00010010
- ☒ 00010011
- ☐ 10001001

Which is an example of auto decrement mode addressing? *

- ☐ SUB (A)+,R2
- ☐ Add 1000(R1),R2
- ☐ Add (R3)-,R2
- ☐ Add -(R3),R2



1 Megabytes is equal to *

- ☐ 240
- ☐ 210
- ☐ 220
- ☐ 230

Which instruction is used for moving the data from accumulator to memory? *

- ☐ LOAD D
- ☒ MOVE D
- ☐ ADD D
- ☐ STORE C



SUM EQU 200 does the following *

- ☐ Value 200 is replaced with every occurrence of sum
- ☐ 200 value is assigned after the first occurrence of sum
- ☐ Address of Sum is reassigned by adding 200 to original address
- ☐ 200 bytes of memory is assigned starting from the location of sum

considering when the value of a number exceeds the range specified by the size of bit field, Which bit in status flag is updated? *

- ☐ Zero
- ☐ Negative
- ☐ Carry
- ☐ Overflow



Each instruction in ARM are encoded into how many words *

- ☐ 2 bytes
- ☐ 4 bytes
- ☐ 8 bytes
- ☐ 3 byte

A DB 30 DUP ('HI'), how many bytes of memory are defined and reserved by this instruction? *

- ☐ 32
- ☐ 40
- ☐ 60
- ☐ 30



What is the operation of the instruction MOVE (PC),(MAR)? *

- ☐ Move the data from Ro to PC to MAR
- ☐ Move the address from MAR to PC
- ☐ Move the contents from MAR to PC
- ☐ Move the contents of PC to MAR

In which type of addressing there is no memory related operation? *

- ☐ Direct addressing
- ☐ Register indirect addressing
- ☐ Indirect addressing
- ☒ Register addressing



Which instruction is used to move the data to top of the stack? *

- ☐ PUSH AX
- ☐ POP B
- ☐ POP
- ☒ PUSH A

Which of the following registers are used for performing count operation in 8086 *

- ☒ CX
- ☐ AX
- ☐ BX
- ☐ DX

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