

CRITICAL LAYOUT - These grounds must return with very low inductance to the switching node ground.

**NIH labs**

Sheet: /Preregulator/  
File: Preregulator.sch

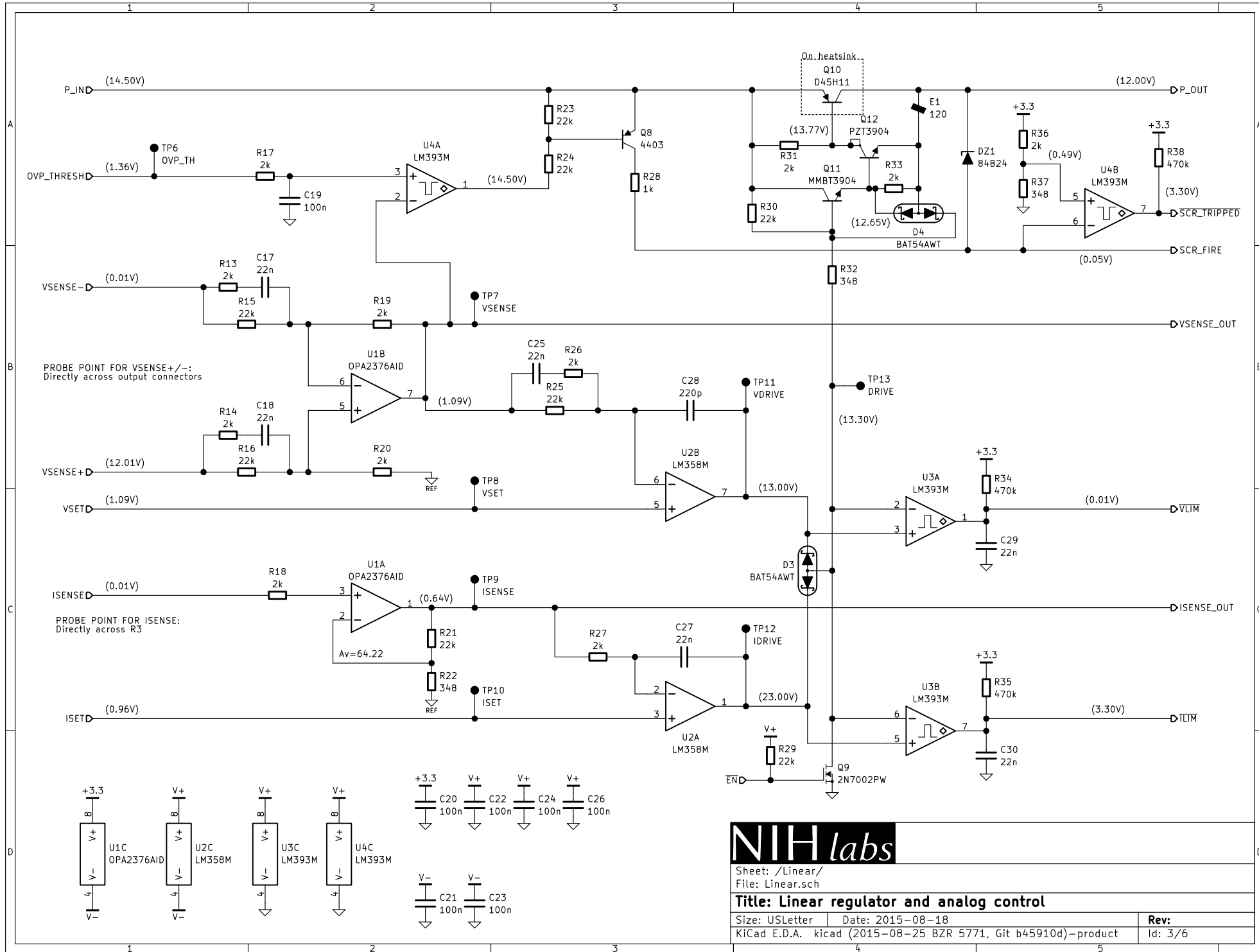
**Title: Buck preregulator**

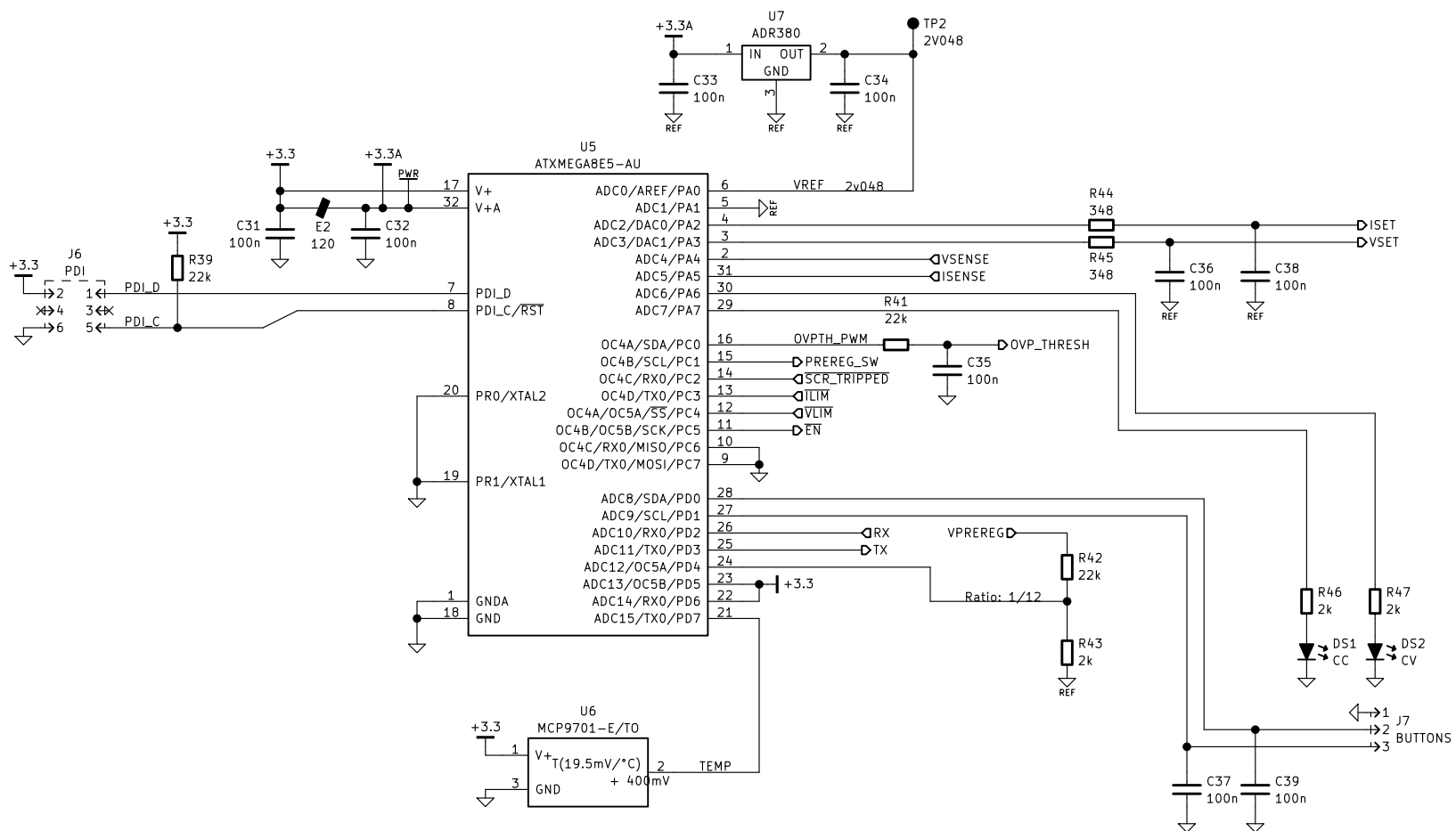
Size: A Date: 2015-08-18

KiCad E.D.A. kicad (2015-08-25 BZR 5771, Git b45910d)-product

Rev:  
Id: 2/6

Test conditions:  
Vout = 12V Vovp = 15V  
Iout = 1A Ilim = 1.5A





**NIH** labs

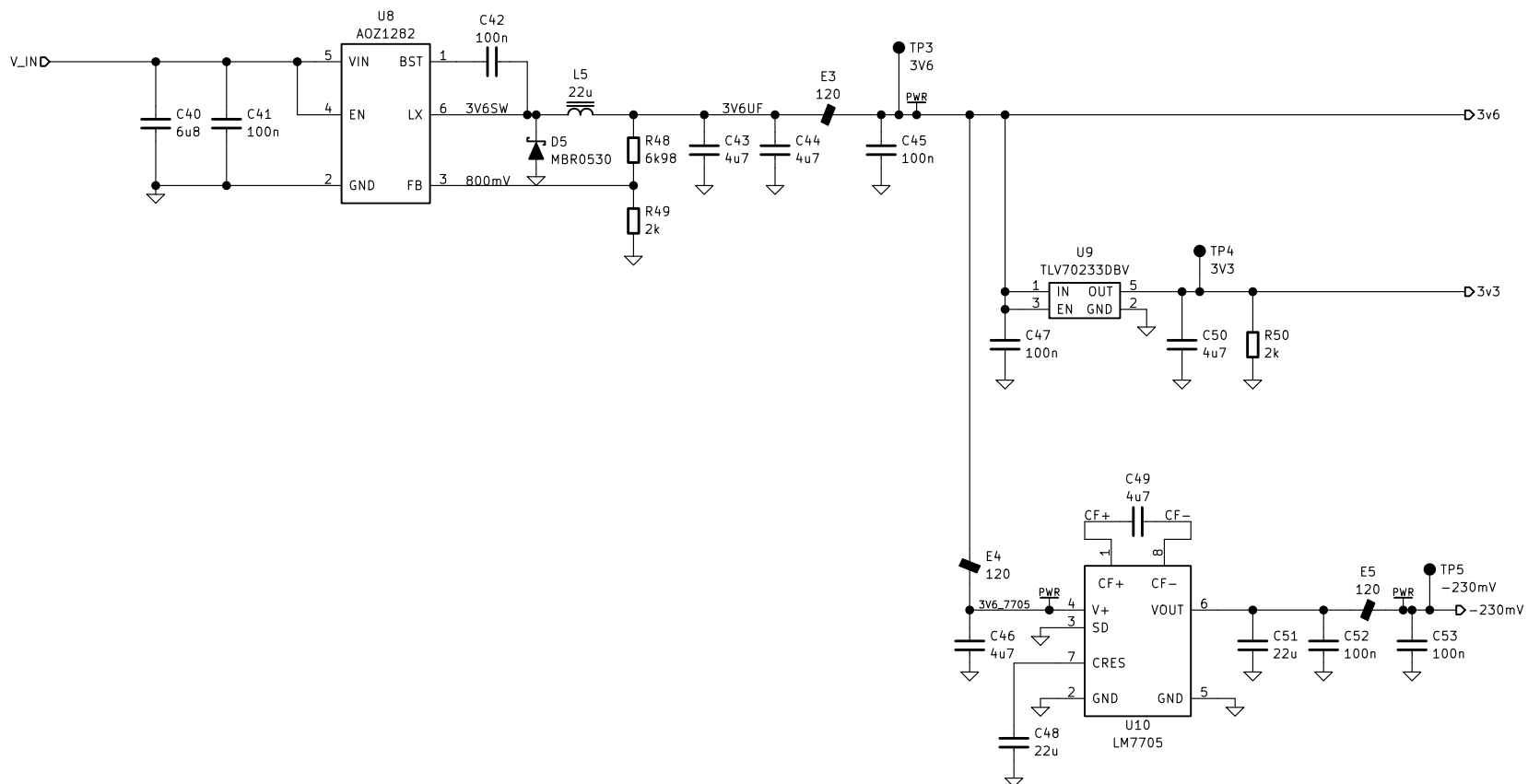
Sheet: /Control/  
File: Control.sch

**Title: Microcontroller, A-D, D-A**

Size: USLetter Date: 2015-08-18

KiCad E.D.A. kicad (2015-08-25 BZR 5771, Git b45910d)-product

Rev:  
Id: 4/6



**NIH** labs

Sheet: /LocalPwr/  
File: LocalPwr.sch

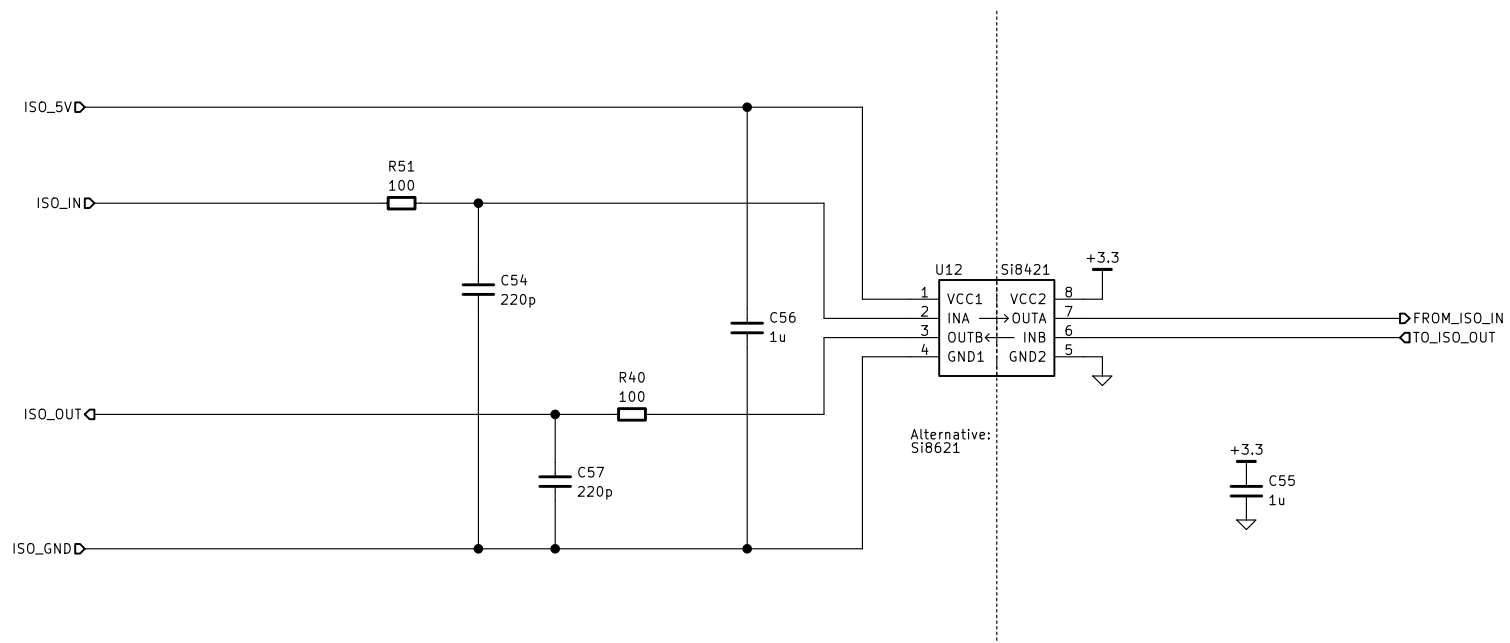
**Title: Local power supplies**

Size: A Date: 2015-08-18

Rev:

KiCad E.D.A. kicad (2015-08-25 BZR 5771, Git b45910d)-product

Id: 5/6



**NIH** labs

Sheet: /Iso/  
File: Iso.sch

**Title: Isolated control interface**

Size: USLetter Date: 2015-08-18

KiCad E.D.A. kicad (2015-08-25 BZR 5771, Git b45910d)-product

**Rev:**  
Id: 6/6