%8:
 %9 = shl i64 %5, 5
 %cmp218.i = icmp sgt i32 %3, 0
 %10 = zext i32 %3 to i64
 br i1 %cmp218.i, label %pregion_for_entry.entry.i.us.preheader, label
 ... %mvt_kernel2.exit

| T | F |

pregion_for_entry.entry.i.us.preheader:
 br label %pregion_for_entry.entry.i.us

pregion_for_entry.entry.i.us:
 %_local_id_x.0.us = phi i64 [%18, %if.end.r_exit.i.us], [0,
 ... %pregion_for_entry.entry.i.us.preheader]
 %11 = add nuw nsw i64 %_local_id_x.0.us, %9
 %conv.i.us = trunc i64 %11 to i32
 %cmp.i.us = icmp slt i32 %conv.i.us, %3
 br i1 %cmp.i.us, label %for.body.lr.ph.i.us, label %if.end.r_exit.i.us

| T | F |

for.body.lr.ph.i.us:
 %sext.i.us = shl i64 %11, 32
 %idxprom7.i.us = ashr exact i64 %sext.i.us, 32
 %arrayidx8.i.us = getelementptr inbounds float, float* %1, i64 %idxprom7.i.us
 %.pre.i1.us4 = load float, float* %arrayidx8.i.us, align 4, !tbaa !12
 br label %for.body.i.us

for.body.i.us:
 %indvars.iv.next.i3.us = phi i64 [%indvars.iv.next.i.us, %for.body.i.us],
 ... [0, %for.body.lr.ph.i.us]
 %12 = phi float [%17, %for.body.i.us], [%.pre.i1.us4,
 ... %for.body.lr.ph.i.us]
 %13 = mul nuw nsw i64 %indvars.iv.next.i3.us, %10
 %14 = add nsw i64 %13, %idxprom7.i.us
 %arrayidx.i.us = getelementptr inbounds float, float* %0, i64 %14
 %15 = load float, float* %arrayidx.i.us, align 4, !tbaa !12
 %arrayidx5.i.us = getelementptr inbounds float, float* %2, i64
 ... %indvars.iv.next.i3.us
 %16 = load float, float* %arrayidx5.i.us, align 4, !tbaa !12
 %17 = tail call float @llvm.fmuladd.f32(float %15, float %16, float %12) #2
 store float %17, float* %arrayidx8.i.us, align 4, !tbaa !12,
 ... !llvm.access.group !16
 %indvars.iv.next.i.us = add nuw nsw i64 %indvars.iv.next.i3.us, 1
 %exitcond.i.us = icmp eq i64 %indvars.iv.next.i.us, %10
 br i1 %exitcond.i.us, label %if.end.r_exit.i.us.loopexit, label
 ... %for.body.i.us

| T | F |

if.end.r_exit.i.us.loopexit:
 br label %if.end.r_exit.i.us

if.end.r_exit.i.us:
 %18 = add nuw nsw i64 %_local_id_x.0.us, 1
 %exitcond = icmp eq i64 %18, 32
 br i1 %exitcond, label %mvt_kernel2.exit.loopexit, label
 ... %pregion_for_entry.entry.i.us, !llvm.loop !18

| T | F |

mvt_kernel2.exit.loopexit:
 br label %mvt_kernel2.exit

mvt_kernel2.exit:
 ret void

CFG for '_pocl_kernel_mvt_kernel2' function