

push %rbp
push %r15
push %r14
push %r13
push %r12
push %rbx
mov 0x8(%rdi),%r11d
mov 0xc(%rdi),%r8d
lea 0x1(%r11),%r14d
mov %r8d,%eax
sub %r14d,%eax
lea -0x1(%rax),%ecx
cmp %eax,%ecx
cmovle %ecx,%eax
cmp %r14d,%r8d
mov \$0xffffffff,%ecx
cmovg %eax,%ecx
movslq %ecx,%rax
lea 0x1(%rax),%r10
cmp %r14d,%r8d
mov 0x48(%rdi),%r15
mov \$0x7fffffff,%ebp
mov \$0xffffffffffffffe,%rbx
cmovle %rbp,%rbx
mov 0x50(%rdi),%rbp
mov \$0x80000000,%ecx
cmovle %r10,%rcx
mov (%rsi),%rdx
imul %r15,%rdx
add 0x18(%rdi),%rdx
mov 0x8(%rsi),%r9
imul %rbp,%r9
add 0x20(%rdi),%r9
test %eax,%eax
cmovns %r10,%rcx
add %r9,%rbp
cmp %rcx,%rbp
mov %rcx,%r13
cmovle %rbp,%r13
cmp %rbx,%r9
cmovl %rbx,%r9
add %rdx,%r15
cmp %rcx,%r15
mov %rcx,%rax
cmovle %r15,%rax
test %rcx,%rcx
cmovs %rbp,%r13
cmovs %r15,%rax
cmp %rbx,%rdx
cmovl %rbx,%rdx
mov %rdx,-0x18(%rsp)
sub %rdx,%rax
test %rax,%rax
jle 0x7fff7fcb4d7 <lu_kernel2+535>

No Jump	Jump
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sub %r9,%r13
test %r13,%r13
jle 0x7fff7fcb4d7 <lu_kernel2+535>

No Jump	Jump
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mov (%rdi),%rdi
mov %rax,%r15
sar \$0x3,%r15
mov %rax,%rcx
and \$0xffffffffffffff8,%rcx
mov %rcx,-0x10(%rsp)
sub %rcx,%rax
mov %rax,-0x8(%rsp)
test %r15,%r15
je 0x7fff7fcb43f <lu_kernel2+383>

No Jump	Jump
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lea 0x1(%r8),%eax
imul %r11d,%eax
lea (%r11,%r9,1),%ecx
add \$0x1,%ecx
imul %r8d,%ecx
mov -0x18(%rsp),%rdx
lea (%rax,%rdx,1),%r12d
add \$0x1,%r12d
add %r11d,%ecx
lea (%rdx,%rcx,1),%edx
add \$0x1,%edx
xor %ebx,%ebx
mov %r9,%rsi
nopl 0x0(%rax)

lea (%r14,%rsi,1),%eax
imul %r8d,%eax
mov %r11,%rbp
add %r11d,%eax
cltq
mov %edx,%r11d
mov %r12d,%r10d
mov %r15,%rcx
nopl 0x0(%rax)

movslq %r11d,%r11
vbroadcastss (%rdi,%rax,4),%ymm0
movslq %r10d,%r10
vmovups (%rdi,%r10,4),%ymm1
vfmadd213ps (%rdi,%r11,4),%ymm0,%ymm1
vmovups %ymm1,(%rdi,%r11,4)
add \$0x8,%r10d
add \$0x8,%r11d
add \$0xffffffffffffff,%rcx
jne 0x7fff7fcb400 <lu_kernel2+320>

No Jump	Jump
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add \$0x1,%rbx
add \$0x1,%rsi
add %r8d,%edx
cmp %r13,%rbx
mov %rbp,%r11
jne 0x7fff7fcb3e0 <lu_kernel2+288>

No Jump	Jump
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mov -0x8(%rsp),%r15
test %r15,%r15
je 0x7fff7fcb4d7 <lu_kernel2+535>

No Jump	Jump
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mov -0x10(%rsp),%rdx
add -0x18(%rsp),%rdx
lea 0x1(%r8),%eax
imul %r11d,%eax
lea (%r11,%r9,1),%ecx
add \$0x1,%ecx
imul %r8d,%ecx
lea (%rax,%rdx,1),%r10d
add \$0x1,%r10d
add %r11d,%ecx
lea (%rdx,%rcx,1),%edx
add \$0x1,%edx
xor %ebx,%ebx
nopl (%rax)

lea (%r14,%r9,1),%eax
imul %r8d,%eax
add %r11d,%eax
cltq
mov %edx,%esi
mov %r10d,%ecx
mov %r15,%rbp
data16 nopw %cs:0x0(%rax,%rax,1)

movslq %esi,%rsi
vmovss (%rdi,%rax,4),%xmm0
movslq %ecx,%rcx
vmovss (%rdi,%rcx,4),%xmm1
vfmadd213ss (%rdi,%rsi,4),%xmm0,%xmm1
vmovss %xmm1,(%rdi,%rsi,4)
add \$0x1,%ecx
add \$0x1,%esi
add \$0xffffffffffffff,%rbp
jne 0x7fff7fcb4a0 <lu_kernel2+480>

No Jump	Jump
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add \$0x1,%rbx
add \$0x1,%r9
add %r8d,%edx
cmp %r13,%rbx
jne 0x7fff7fcb480 <lu_kernel2+448>

No Jump	Jump
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pop %rbx
pop %r12
pop %r13
pop %r14
pop %r15
pop %rbp
vzeroupper
ret