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%8:
%mul.i.i = shl i64 %5, 5
%cmp218.i = icmp sgt i32 %3, 0, !llvm.access.group !12
%9 = zext i32 %3 to i64
br label %preion_for_entry.entry.i

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preion_for_entry.entry.i:
%_local_id_x.0 = phi i64 [ 0, %8 ], [ %16, %if.end.r_exit.i ]
%add1.i.i = add nuw nsw i64 %_local_id_x.0, %mul.i.i, !llvm.access.group !12
%conv.i = trunc i64 %add1.i.i to i32, !llvm.access.group !12
%cmp.i = icmp slt i32 %conv.i, %3, !llvm.access.group !12
%or.cond.i = and i1 %cmp218.i, %cmp.i, !llvm.access.group !12
br i1 %or.cond.i, label %for.body.lr.ph.i, label %if.end.r_exit.i,
... !llvm.access.group !12

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for.body.lr.ph.i:
%sext.i = shl i64 %add1.i.i, 32, !llvm.access.group !12
%idxprom7.i = ashr exact i64 %sext.i, 32, !llvm.access.group !12
%arrayidx8.i = getelementptr inbounds float, float* %1, i64 %idxprom7.i,
... !llvm.access.group !12
%.pre.i = load float, float* %arrayidx8.i, align 4, !tbaa !14,
... !llvm.access.group !12
br label %for.body.i, !llvm.access.group !12

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for.body.i:
%indvars.iv.next.i2 = phi i64 [ %indvars.iv.next.i, %for.body.i ], [ 0,
... %for.body.lr.ph.i ]
%10 = phi float [ %15, %for.body.i ], [ %.pre.i, %for.body.lr.ph.i ]
%11 = mul nuw nsw i64 %indvars.iv.next.i2, %9, !llvm.access.group !12
%12 = add nsw i64 %11, %idxprom7.i, !llvm.access.group !12
%arrayidx.i = getelementptr inbounds float, float* %0, i64 %12,
... !llvm.access.group !12
%13 = load float, float* %arrayidx.i, align 4, !tbaa !14, !llvm.access.group
... !12
%arrayidx5.i = getelementptr inbounds float, float* %2, i64
... %indvars.iv.next.i2, !llvm.access.group !12
%14 = load float, float* %arrayidx5.i, align 4, !tbaa !14,
... !llvm.access.group !12
%15 = tail call float @llvm.fmuladd.f32(float %13, float %14, float %10) #3,
... !llvm.access.group !12
store float %15, float* %arrayidx8.i, align 4, !tbaa !14, !llvm.access.group
... !12
%indvars.iv.next.i = add nuw nsw i64 %indvars.iv.next.i2, 1,
... !llvm.access.group !12
%exitcond.not.i = icmp eq i64 %indvars.iv.next.i, %9, !llvm.access.group !12
br i1 %exitcond.not.i, label %if.end.r_exit.i.loopexit, label %for.body.i,
... !llvm.loop !18, !llvm.access.group !12

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if.end.r_exit.i.loopexit:
br label %if.end.r_exit.i

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if.end.r_exit.i:
%16 = add nuw nsw i64 %_local_id_x.0, 1
%exitcond.not = icmp eq i64 %16, 32
br i1 %exitcond.not, label %mvt_kernel2.exit, label
... %preion_for_entry.entry.i, !llvm.loop !20

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mvt_kernel2.exit:
ret void

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CFG for '_pocl_kernel_mvt_kernel2' function