i2
in2
2

Gain block
Capacitor
1/C

dv31/dt

Integrator1
1/s

v31
out1
1

i2
in2
2

v32
in3
3

Gain block
Inductor
1/L

di1/dt

Integrator2
1/s

i1
out2
2

u
in1
1

i1
in5
5

+
−

i2
out4
4

i1
in4
4

Resistor
R

v21
out3
3

v31
in6
6

in7
7

v21
+
−

v32
out5
5

Elemental, Compatibility and Continuity Equations modelled in Simulink