

CprE 381, Computer Organization and Assembly Level Programming

Team Contract – Project Part 2

Project Teams Group #: 03-06

Team Members: Ella Knott

James Minardi

Discuss the following aspects of teamwork with your team – make sure to get input from each member. Write down your team's consensus for each of the bolded headings. Italicized text contains instructions and examples and should be deleted once you've read it. Please see the example contract for rough length expectations.

Course Goals: *List and acknowledge the goals of your individual team members.*

- *Get a good grade and pass the course*
- *Gain a better understanding of hardware design*
- *know enough to understand security risks posed by hardware primitives*

Team Expectations:

- **Conduct:** *We are expected as team members to be respectful, flexible, communicative, and patient with each other.*
- **Communication:** *Discord would be the easiest and most effective mode of communication for the two of us. For urgent communication, utilizing both Discord and Snapchat may be necessary. We should expect responses from each other within the day permitting sleep and daily activities. We should communicate our progress and struggles frequently.*
- **Group conventions:** *For naming conventions, being descriptive as possible is important, and we will be leaving comments describing variables, ports, etc. We're going to have our code on Github for version control. For compilation, we are going to use the same settings and versions for VHDL and Modelsim. We will be using Do files as necessary for large test benches that require multiple commands.*
- **Meetings:** *Our group will meet throughout the week to update each other on our progress, struggles, and what we are going to continue to work on. We will meet as schedules permit. Preferably these will be in person and we will work on the lab together for tough challenges. We are available to meet Tuesdays and Thursdays between 2:00-3:30pm. We are also available in the afternoons for any online meetings we may need.*
- **Peer Evaluation Criteria:** *We will evaluate each other on our willingness to try to complete tasks, and effective communication efforts as well.*

Role Responsibilities: Complete the following planning table. Each lab part should be the responsibility of one team member. Also make sure that no one team member is the lead on both the design and test aspects of a single lab part. These guidelines aid in all students having a complete view of the lab. Note that the non-lead is encouraged to participate and support the lead wherever possible, increasing both the quality of the lab part and each team member's knowledge.

Lab Part		Estimated Time	Design		Test	
			Lead	Deadline	Lead	Deadline
Software-Pipeline Scheduled	Control Signals	0.5 hr	James			
	Datapath	3 hr	James			
	Testing	3 hr	both			
	Synthesis (human effort)	0.5 hr	both			
Hardware-Pipeline Scheduled	Pipeline Register Update	1 hr	James			
	Data Hazard Avoidance	4 hr	Ella			
	Control Hazard Avoidance	2-6 hr based on group size	Ella			
	Integration (Hardware-Schedule Pipeline)	3 hr	both			
	Testing	3 hr	both			
	Synthesis	0.5 hr	both			

Estimated Time is given as a **very rough** guide for even distribution of tasks assuming you've already read through the lab document and have the prerequisite knowledge. Please note that to be done properly, the test programs will require significant time investment, but will result in a much stronger final design.

Integrity of Work: Do not delete the following. We agree that the work we provide to other team members and ultimately submit for a grade is a direct result of our own work as described in the course syllabus. Specifically, we will generate all VHDL code ourselves and not copy VHDL code from online sources, other groups, book companion material, or past student projects to which anyone outside of my team has contributed.

Student Signature _Ella knott___ **Date** __04/01/2022__

Student Signature __James Minardi_____ **Date** _04/01/2022__