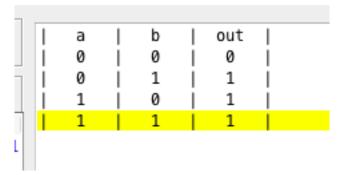
## And

а	b	out	
0	0	0	
0	1	0	
1	0	0	
1	1	1	Т

### Or



# Not

### Dmux

in	sel		a	b	
0	0		0	0	
0	1		0	0	
1	0		1	0	
1	1	-	0	1	

### Mux

Τ	а	Т	b	Т	sel	T	out	Т
1	0		0		0		0	
1	0		0		1		0	
1	0		1		0		0	
1	0		1		1		1	
1	1		0		0		1	
1	1		0		1		0	
1	1		1		0		1	
1	1		1		1	1	1	