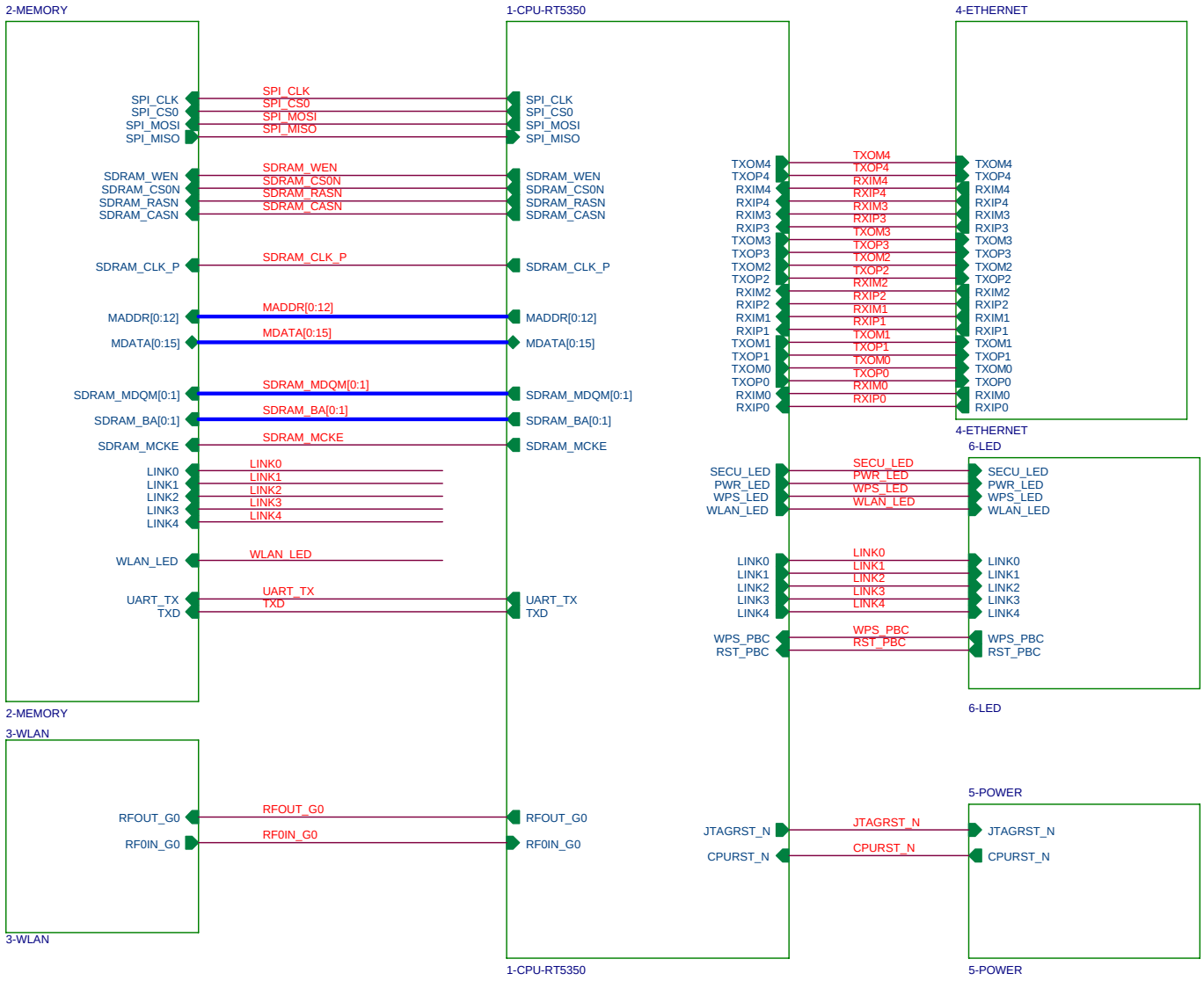
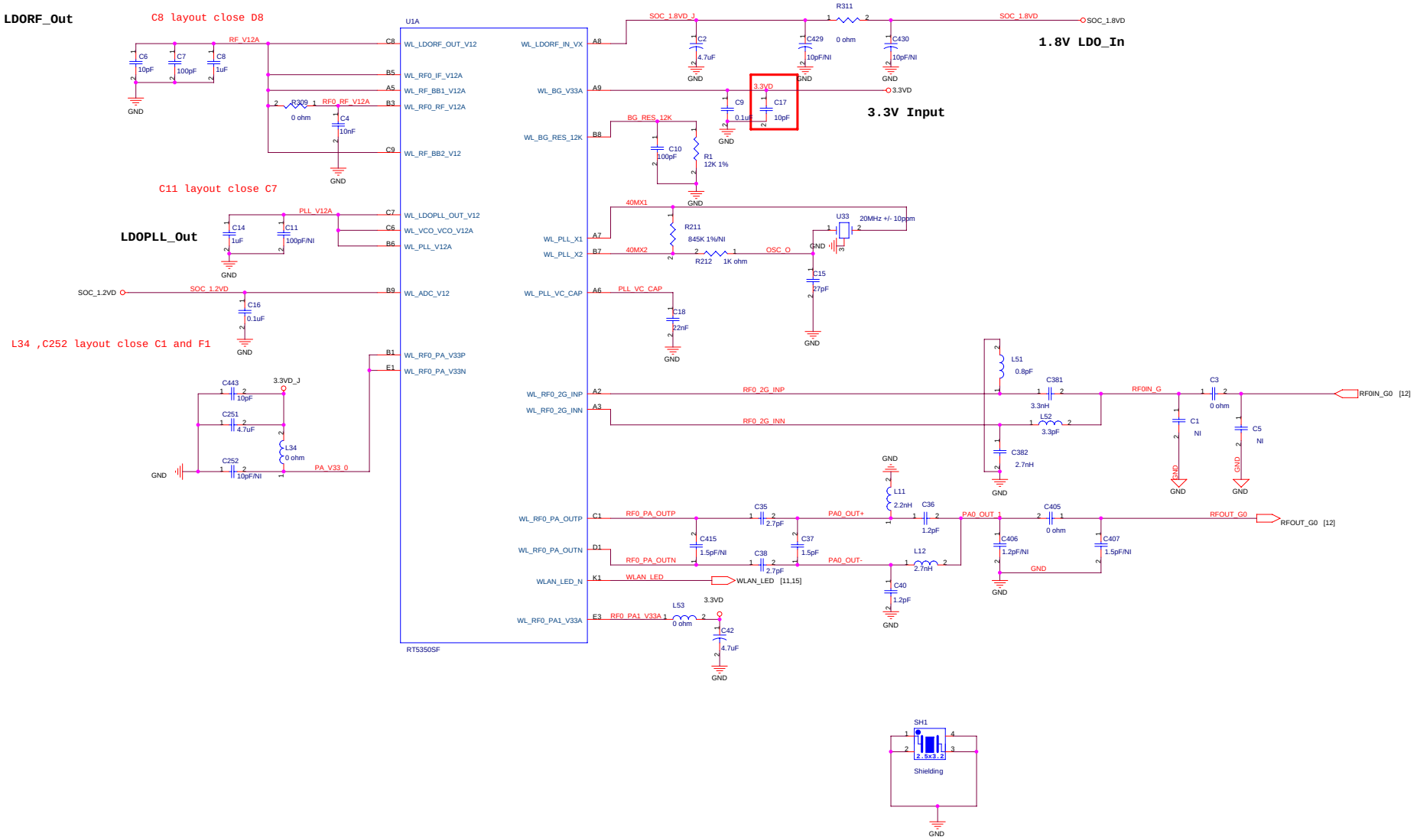
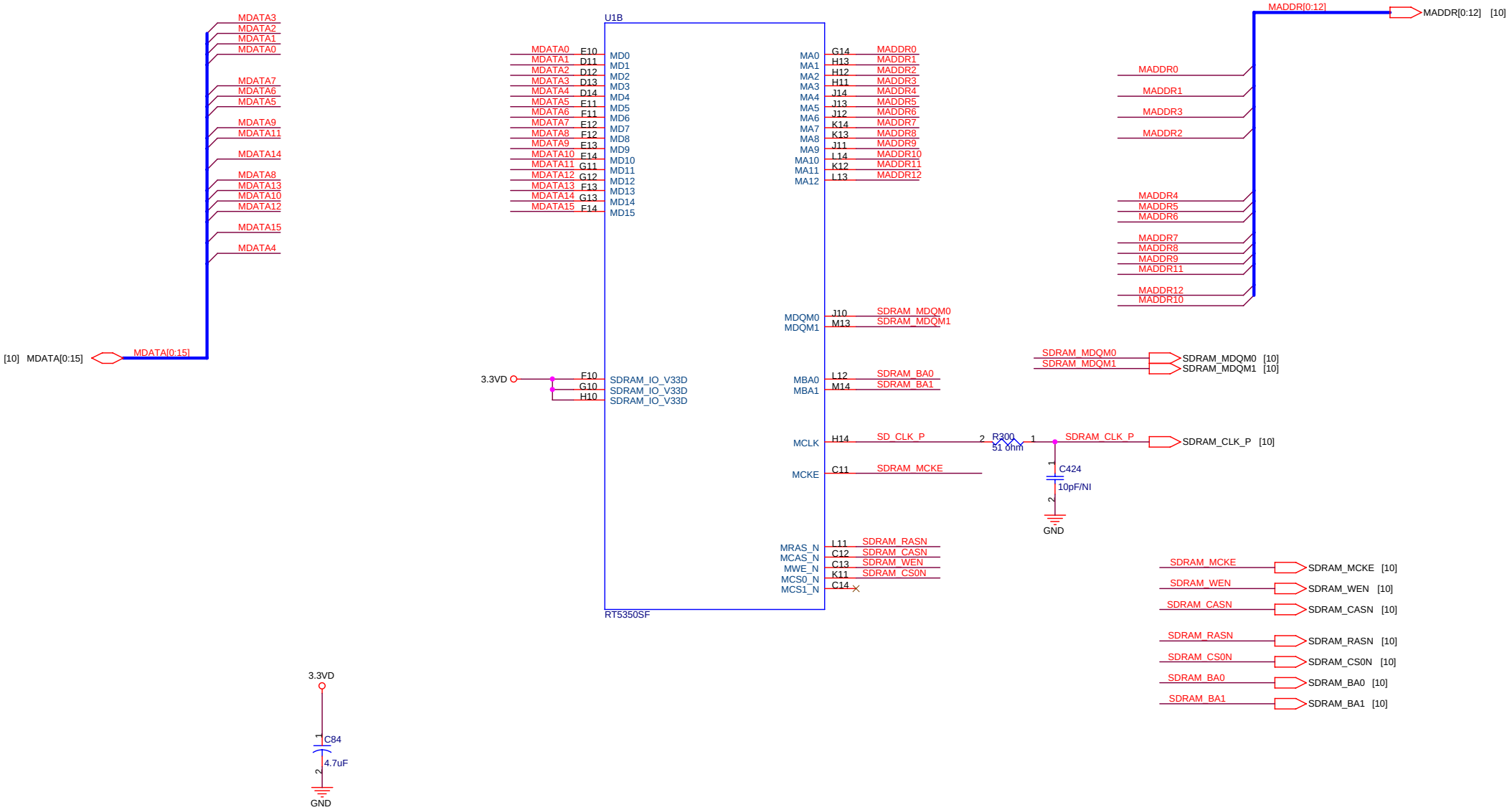


Filename:
AP-RT5350-V22-SPI-SDRAM-1X1-101019.DSN

Ralink	***RALINK TECHNOLOGY, CORP. CONFIDENTIAL***	
	Title: 11n 2.4G AP-RT5350	
Size: B	Document Number: AP-RT5350-V22.SCH	Rev: 2.2
Date: Wednesday, October 20, 2010	Sheet 1 of 16	



Title	RT5350 RF Power-A		
Size C	Document Number 11-RT5350 RF Power-A.SCH		Rev 2.2
Date: Thursday, October 21, 2010	Sheet 2 of 16		



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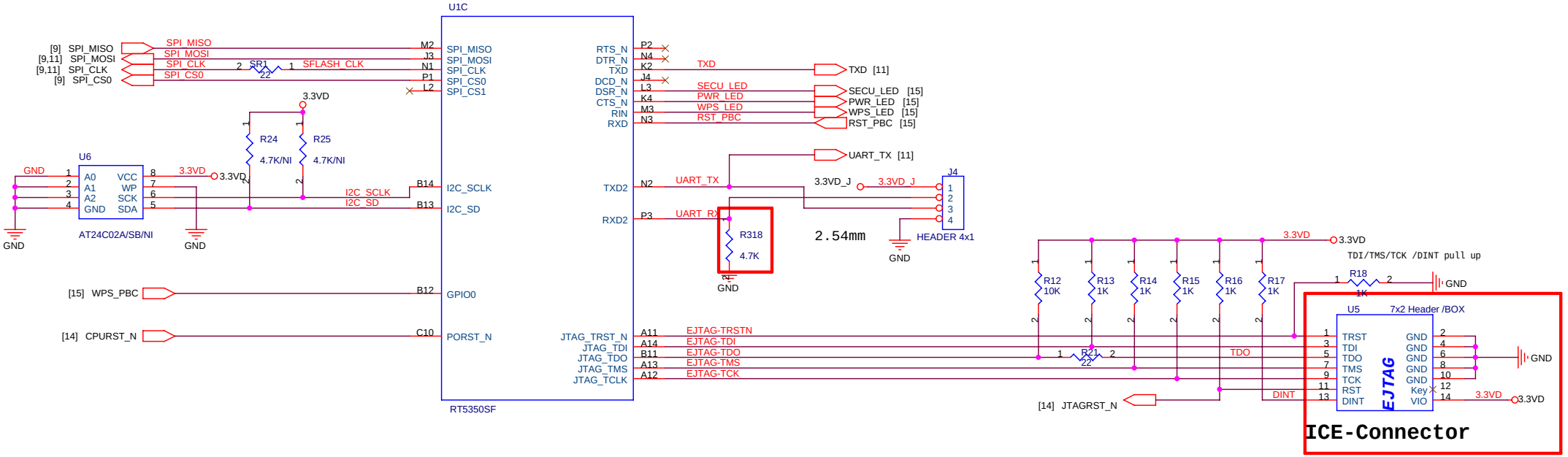
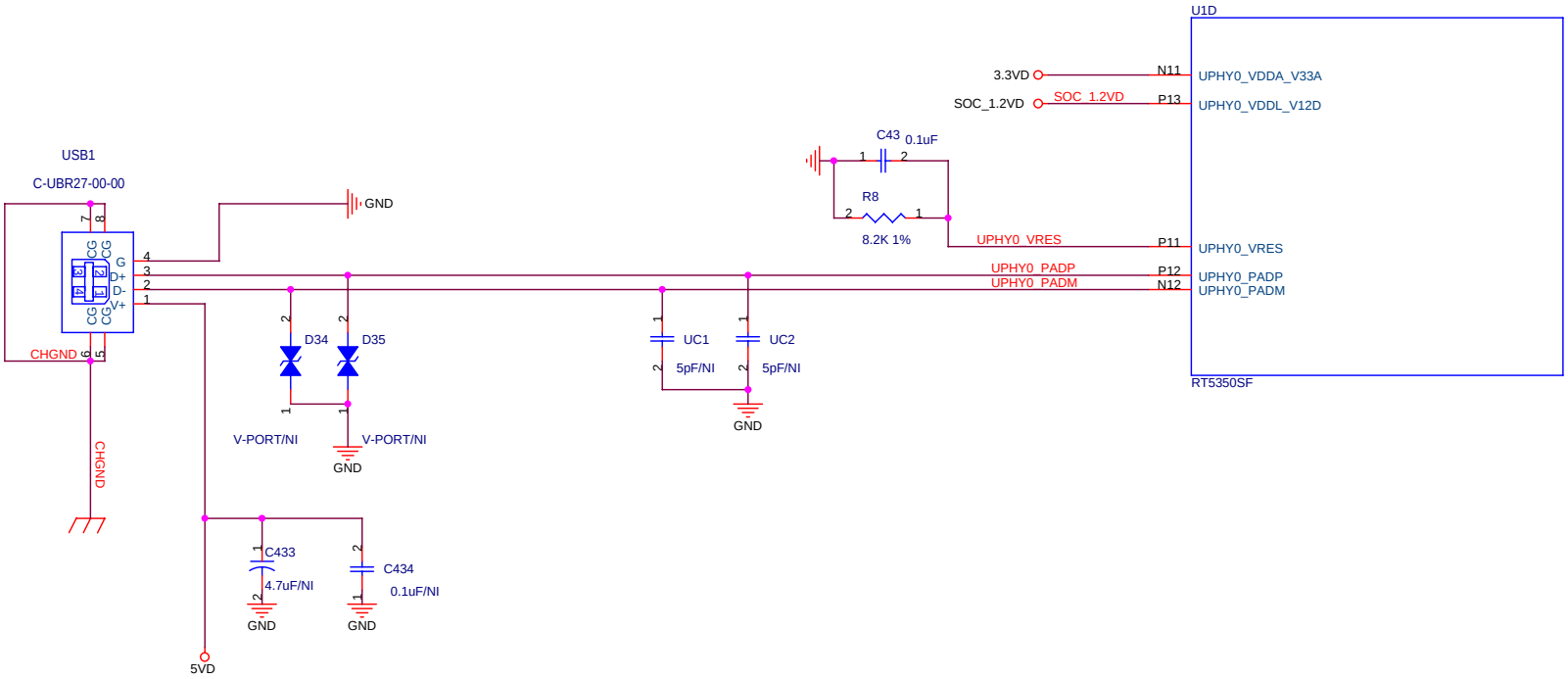
Title	RT5350 SDRAM I/F-B	
Size B	Document Number 13-RT5350 SDRAM I/F-B.SCH	Rev 2.2
Date: Thursday, October 21, 2010	Sheet 3 of 16	



RALINK TECHNOLOGY, CORP. CONFIDENTIAL

Title	RT5350 Pheripheral-C	
Size B	Document Number 13-RT5350 Pheripheral-C.SCH	Rev 2.2
Date: Thursday, October 21, 2010	Sheet 4 of 16	



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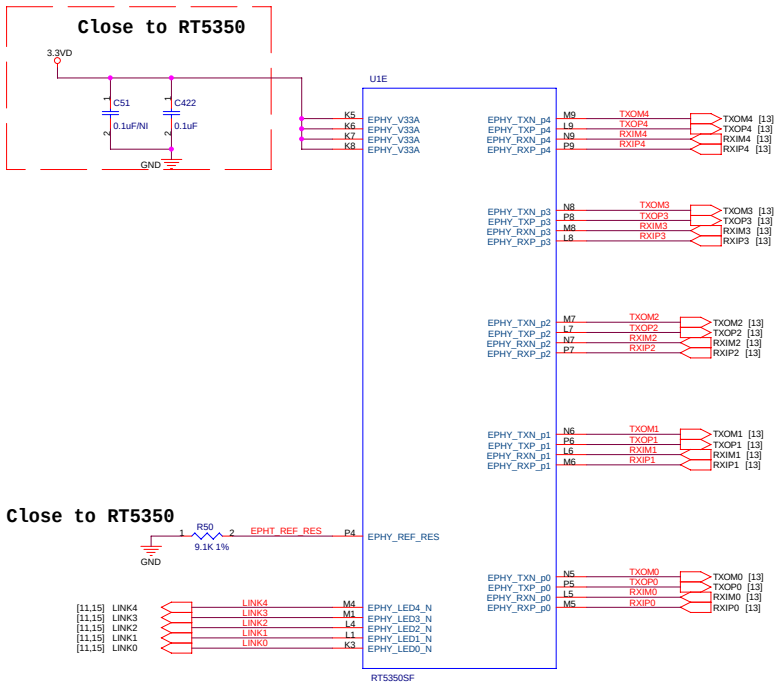
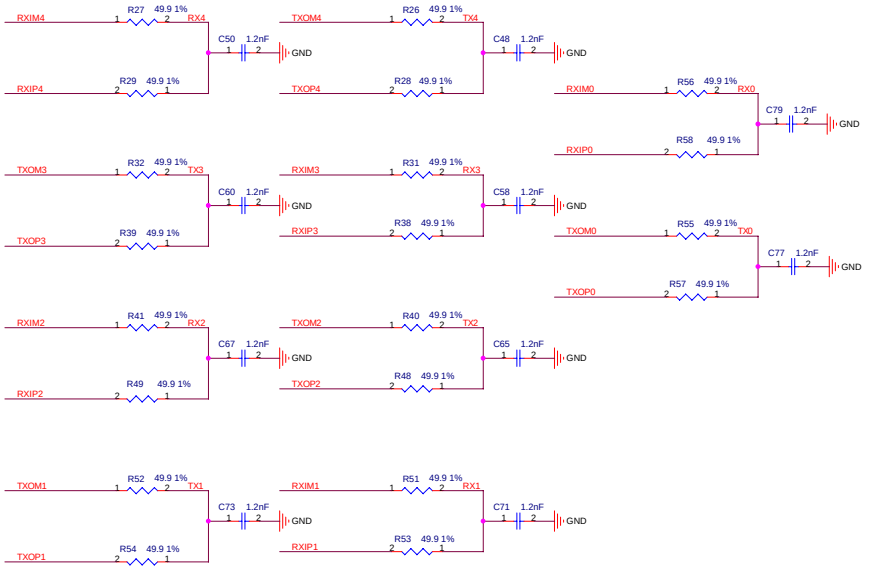
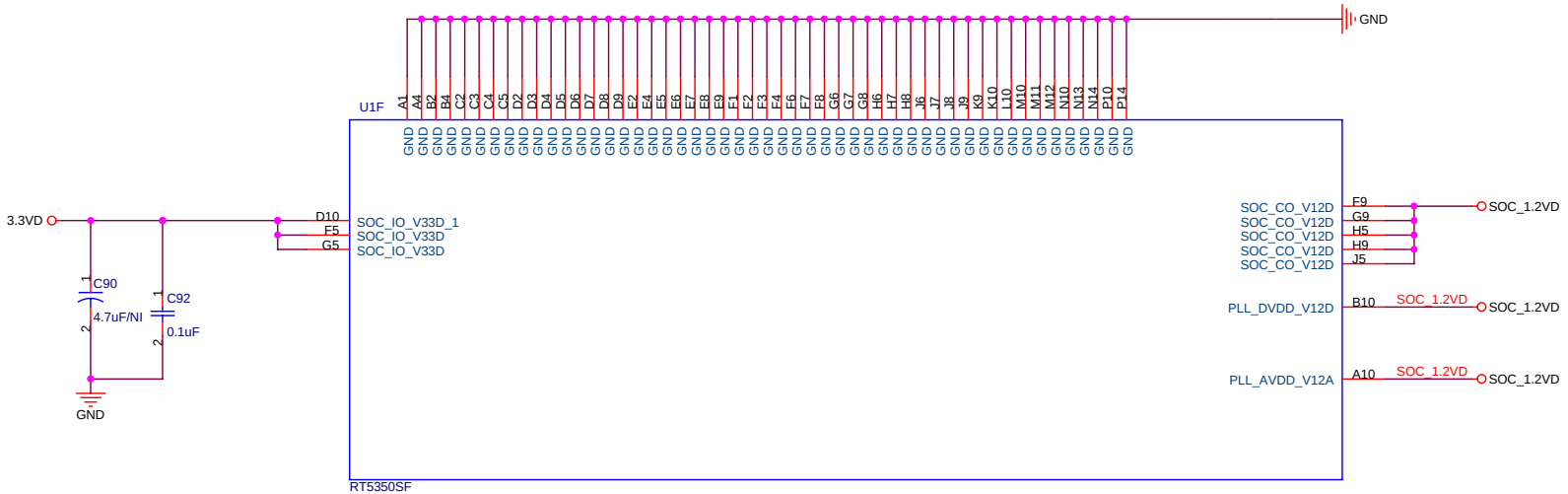
Title	RT5350 USB-D	
Size B	Document Number 14-RT5350 USB-D.SCH	Rev 2.2
Date: Thursday, October 21, 2010	Sheet 5 of 16	

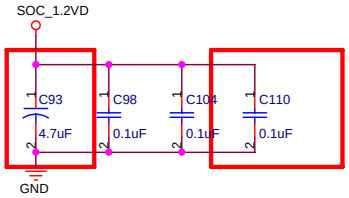




PHY address 5'd0 -> Internal PHY for port 0
PHY address 5'd1 -> Internal PHY for port 1
PHY address 5'd2 -> Internal PHY for port 2
PHY address 5'd3 -> Internal PHY for port 3
PHY address 5'd4 -> Internal PHY for port 4
PHY address 5'd5 -> default for the external Port 5
PHY address 5'd5 ~ 5'd31 are free for the external PHY.

Title	RT5350 Ethernet-E	
Size C	Document Number 15-RT5350 Ethernet-E.SCH	Rev 2.2
Date: Thursday, October 21, 2010	Sheet 6 of 16	





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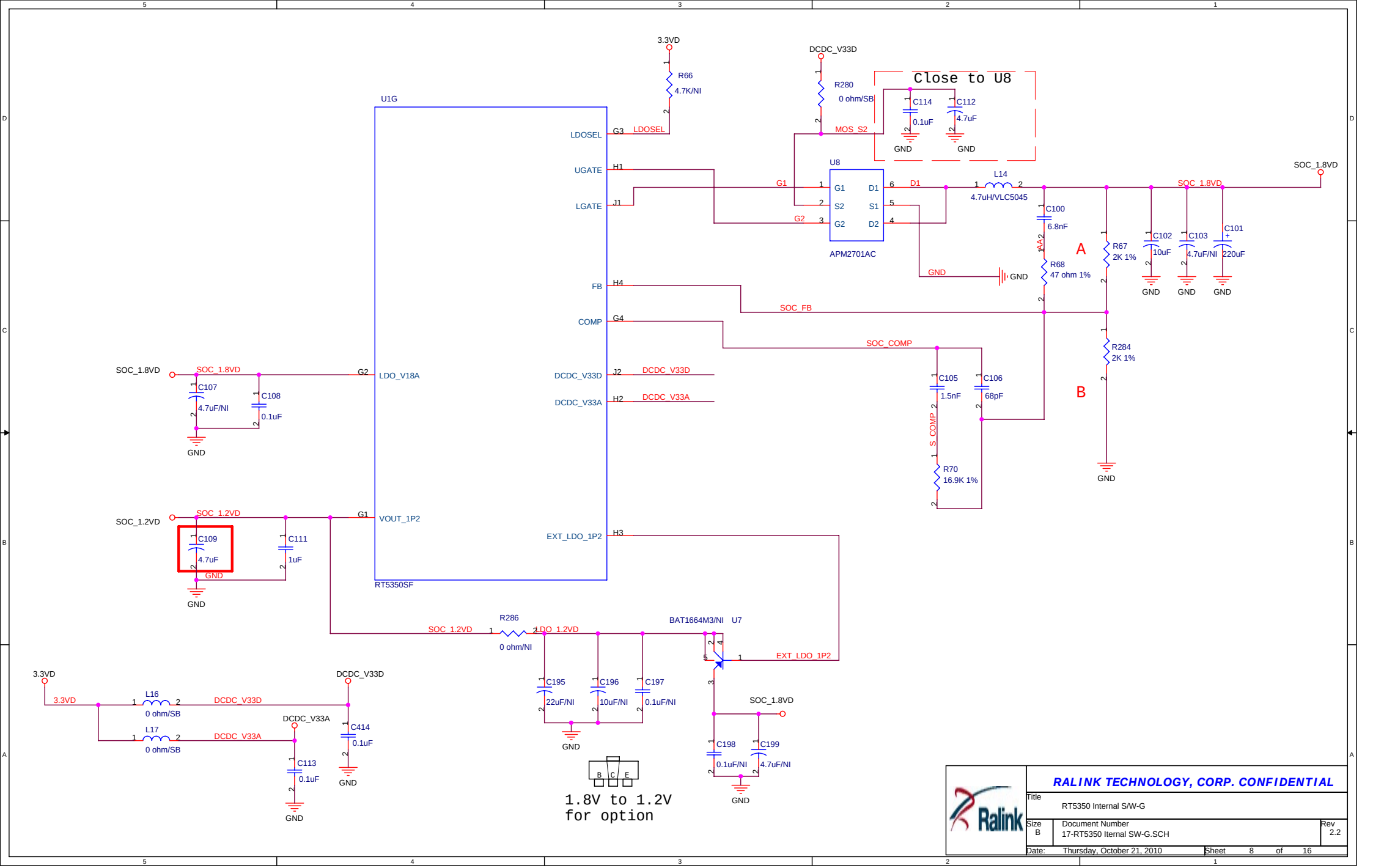
Title	RT5350 Power-F	
Size B	Document Number 16-RT5350 Power-F.SCH	Rev 2.2
Date: Thursday, October 21, 2010	Sheet 7 of 16	


3.3VD
R66
4.7K/NI
U1G
LDOSEL
G3
LDOSEL
UGATE
H1
LGATE
J1
FB
H4
COMP
G4
DCDC_V33D
J2
DCDC_V33D
DCDC_V33A
H2
DCDC_V33A
EXT_LDO_1P2
H3
LDO_V18A
G2
VOUT_1P2
G1
RT5350SF
DCDC_V33D
R280
0 ohm/SB
MOS_S2
C114
0.1uF
C112
4.7uF
GND
Close to U8
U8
G1
S2
G2
D1
S1
D2
APM2701AC
L14
4.7uH/VLC5045
SOC_1.8VD
C100
6.8nF
R67
2K 1%
R68
47 ohm 1%
A
C102
10uF
C103
4.7uF/NI
C101
220uF
SOC_FB
SOC_COMP
R284
2K 1%
B
C105
1.5nF
C106
68pF
R70
16.9K 1%
SOC_1.8VD
C107
4.7uF/NI
C108
0.1uF
SOC_1.2VD
C109
4.7uF
C111
1uF
R286
0 ohm/NI
LDO_1.2VD
BAT1664M3/NI
U7
EXT_LDO_1P2
C195
22uF/NI
C196
10uF/NI
C197
0.1uF/NI
C198
0.1uF/NI
C199
4.7uF/NI
3.3VD
L16
0 ohm/SB
DCDC_V33D
L17
0 ohm/SB
DCDC_V33A
C414
0.1uF
C113
0.1uF
B C E
1.8V to 1.2V
for option
RALINK TECHNOLOGY, CORP. CONFIDENTIAL
Title
RT5350 Internal S/W-G
Size
B
Document Number
17-RT5350 Iternal SW-G.SCH
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2.2
Date:
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8
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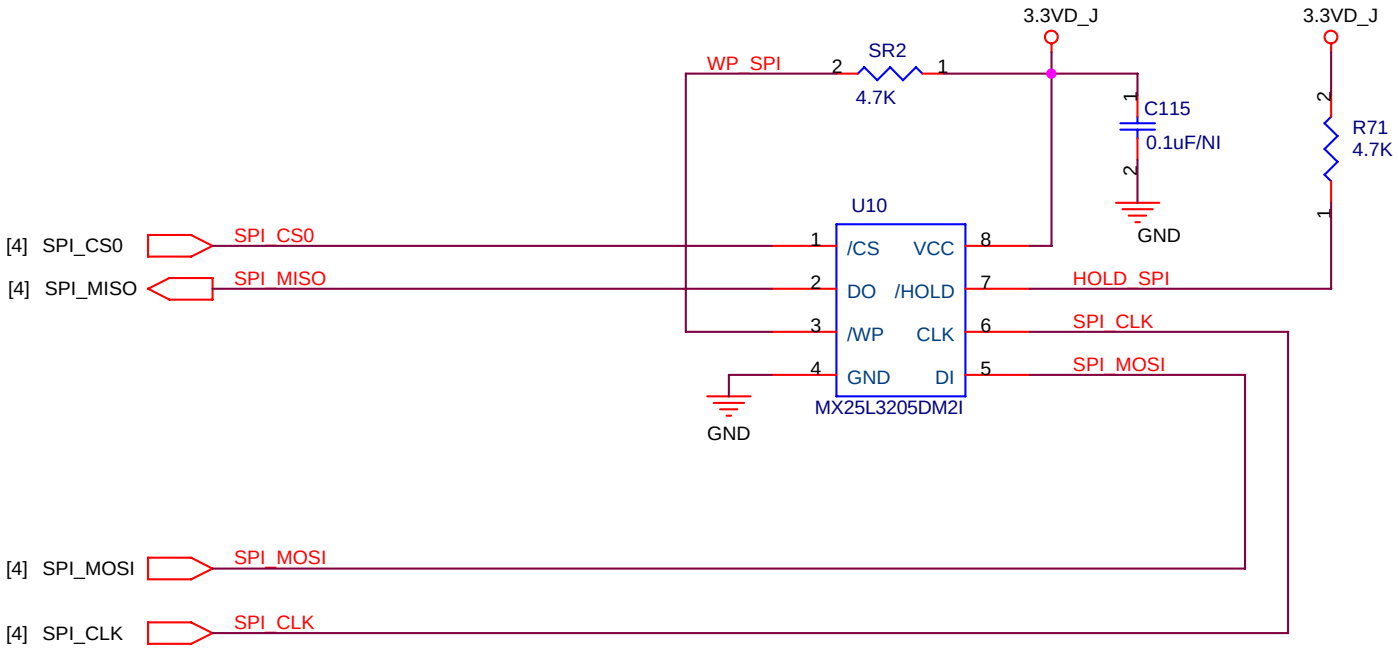
16



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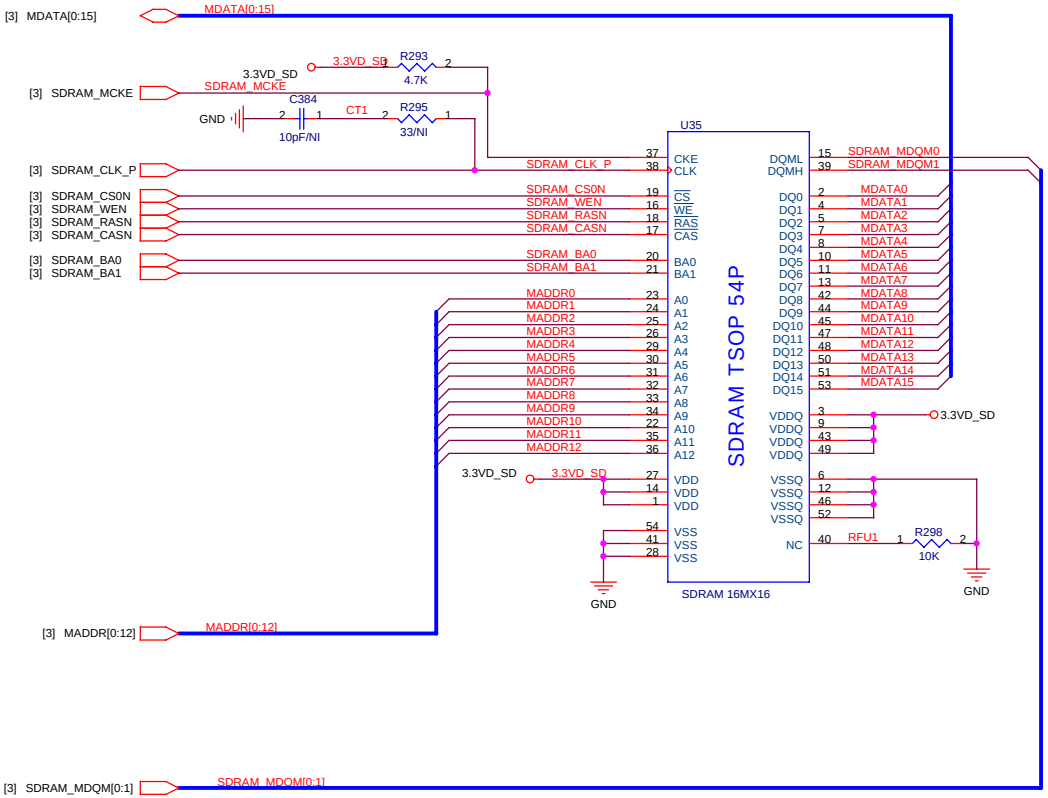
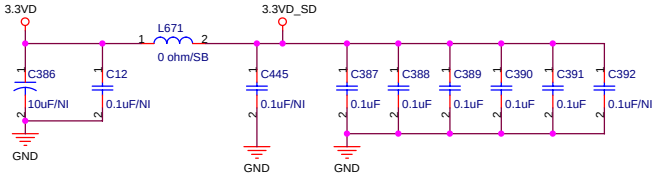
Title	SPI Flash	
Size A	Document Number 21-SPI Flash.SCH	Rev 2.2
Date: Monday, October 18, 2010	Sheet 9 of 16	





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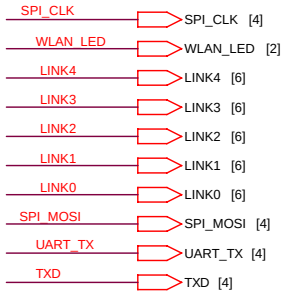
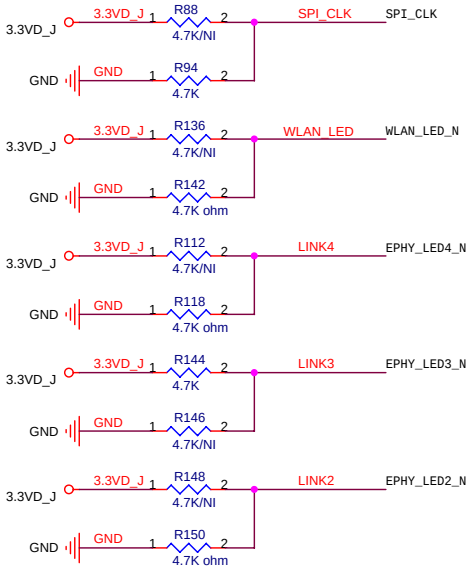
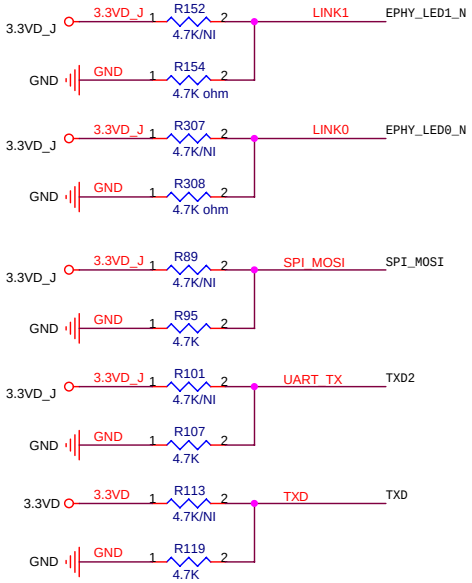
Title	SDRAM	
Size: Custom	Document Number: 22-SDRAM-CFG.SCH	Rev: 2.2
Date: Monday, October 18, 2010	Sheet 10 of 16	



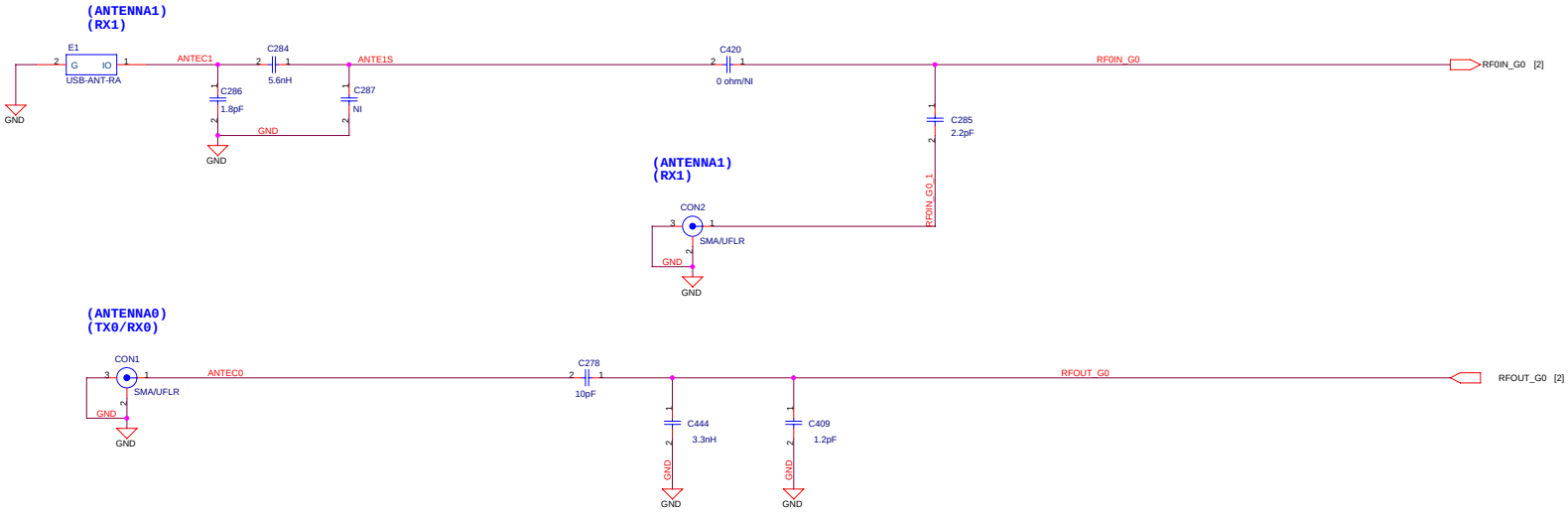




RT5350 Boot Up Strapping

Pin Name	Description	Value=0	Value=1
SPI_CLK	XTAL_FREQ _HI	20MHz	40MHz
WLAN_LED_N	Big Endian	Little Endian	Big Endian
EPHY_LED4_N	DRAM_FROM _EE	from boot strapping	from EEPROM
{EPHY_LED3_N, EPHT_LED2_N}	DRAM_SIZE	INIC/AP(SDR) 0: 2MB/8MB 1: 8MB/16MB 2: 16MB/32MB 3: 32MB/64MB	
{EPHY_LED1_N, EPHT_LED0_N}	CPU_CLK _SEL	CPU clock select 0: 360MHz 1: Reserved 2: 320MHz 3: 300MHz	
{SPI_MOSI, TXD2, TXD}	CHIP_ MODE[2:0]	A vector to set chip function/test/debug modes 0 : Normal mode(boot fromSPI serial flash) 1 : iNIC-USB mode 2 : Reserved 3 : Reserved 4 : Reserved 5 : iNIC-PHY mode 6 : SCAN mode 7 : TEST/DEBUG mode	

Title	CONFIG
Size B	Document Number 23-CONFIG.SCH
Rev	2.2
Date:	Monday, October 18, 2010
Sheet	11 of 16



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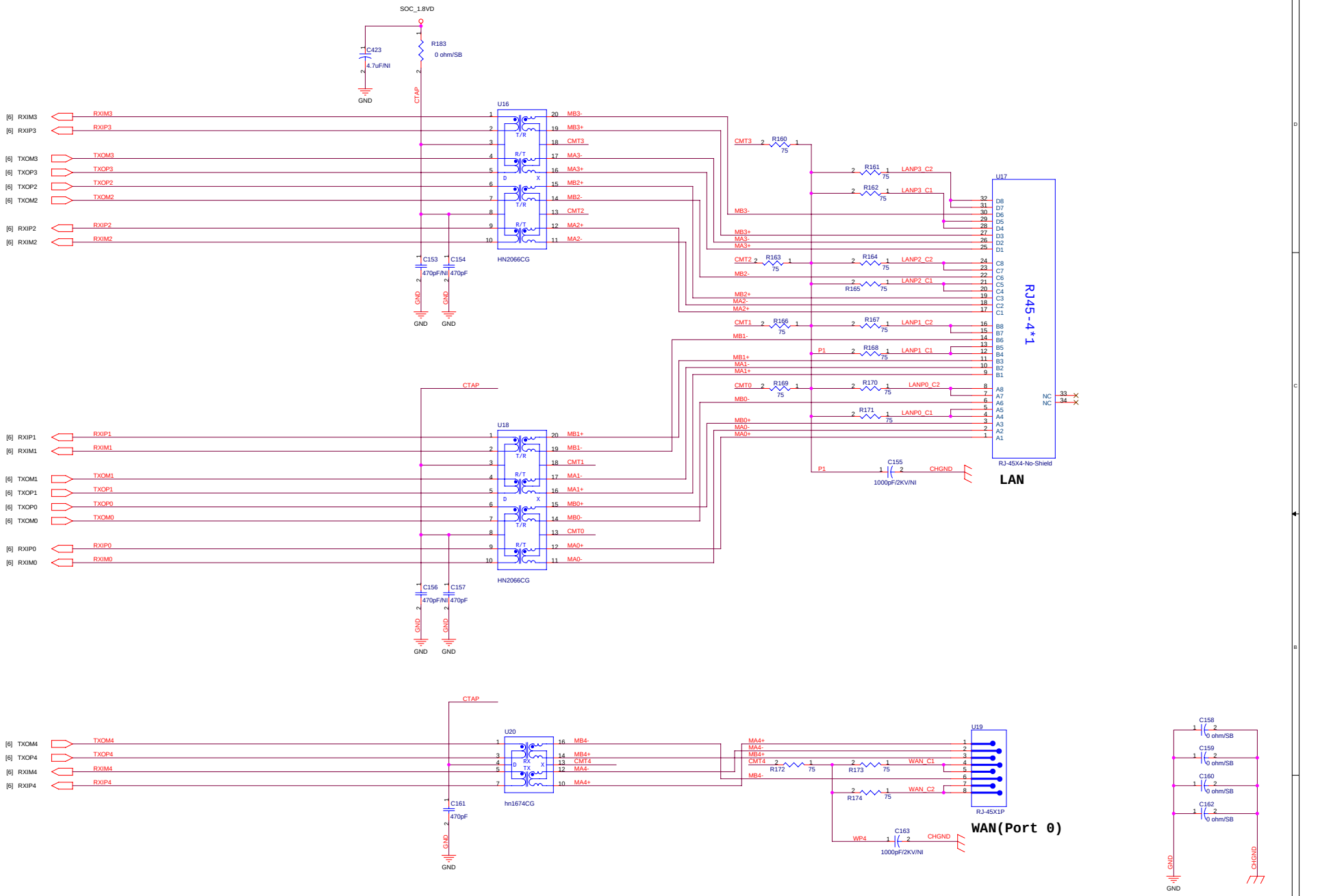
Title	Front End	
Size C	Document Number 31-Front End.SCH	Rev 2.2
Date: Monday, October 18, 2010	Sheet 12 of 16	



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Title	XMER-RJ45	
Size C	Document Number 4-XMER-RJ45.SCH	Rev 2.2
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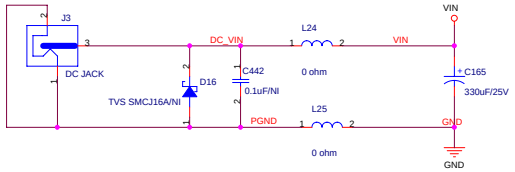
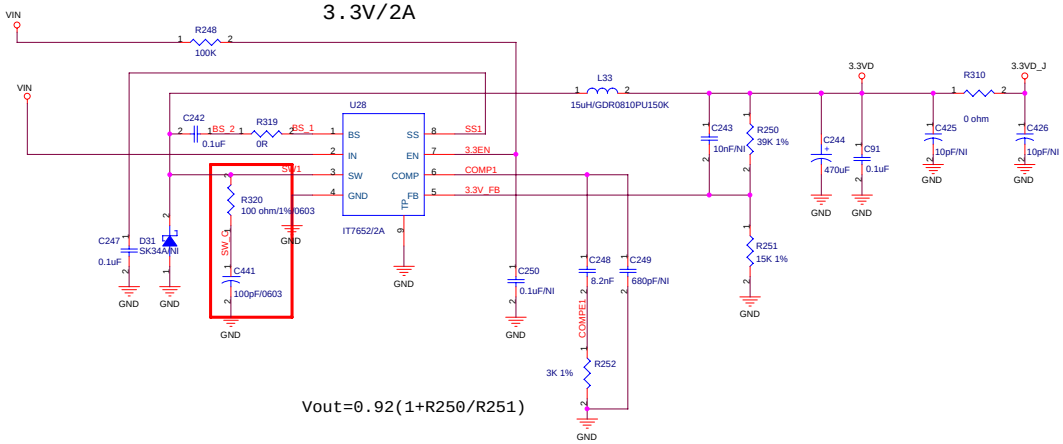
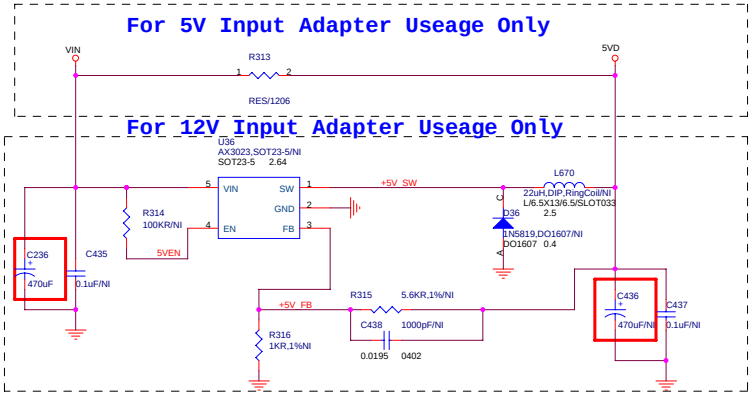
5V DC ADAPTER



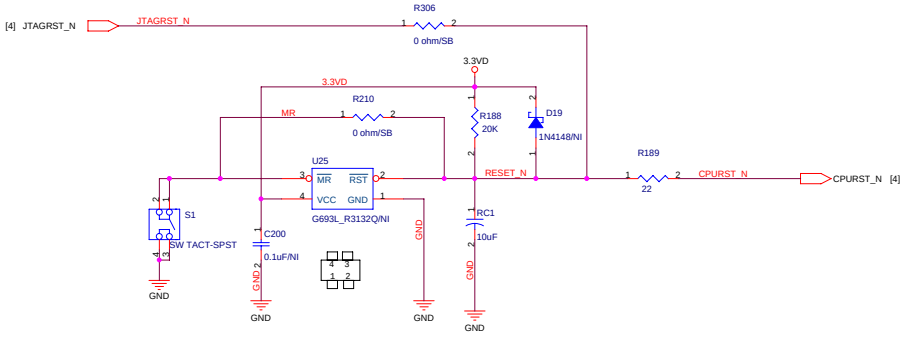
3.3V/2A



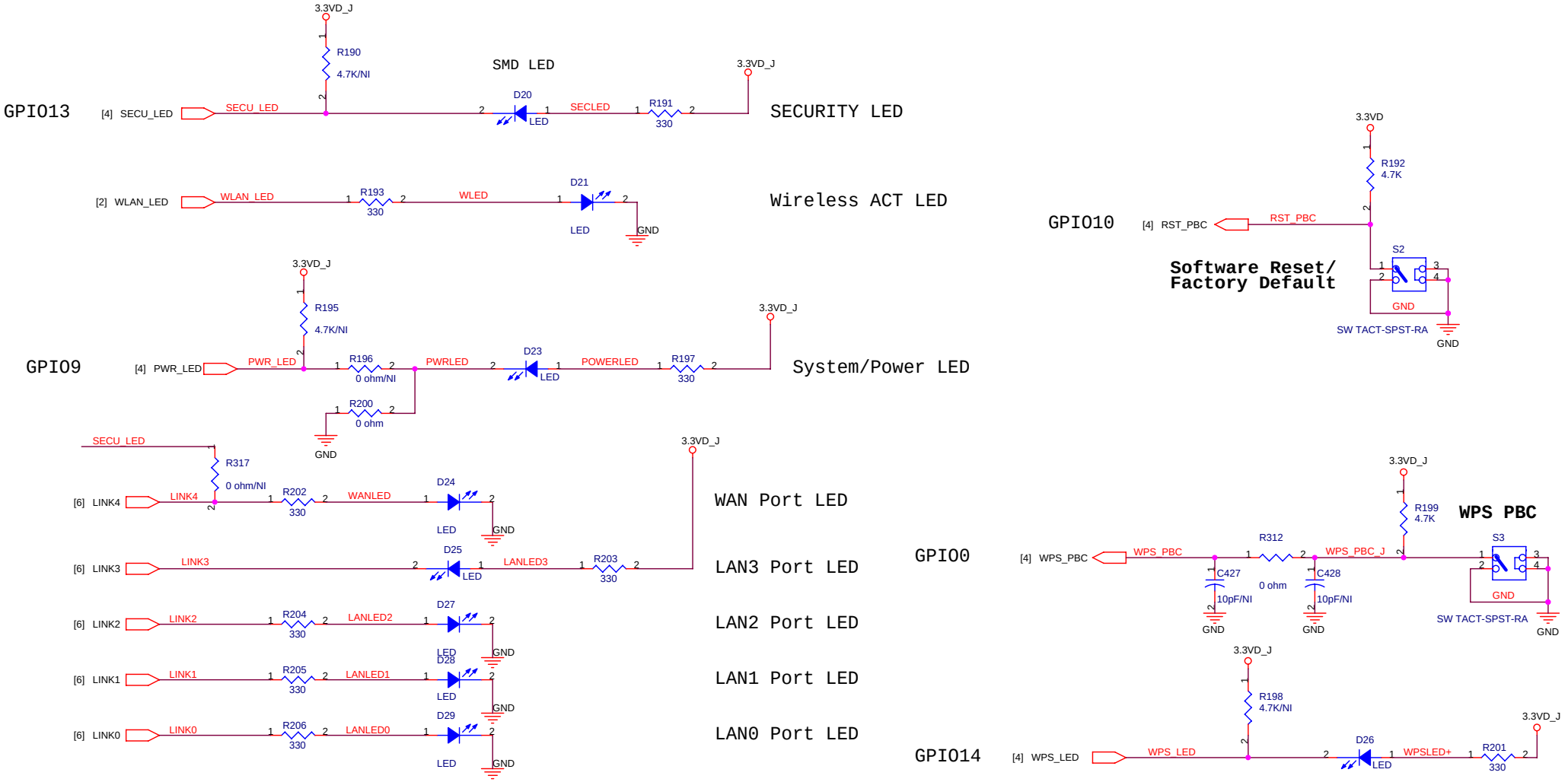
Vout=0.92(1+R250/R251)



Reset Circuit



Title	Power & Reset	
Size C	Document Number S-POWER-RESET.SCH	Rev 2.2
Date: Tuesday, October 19, 2010	Sheet 14 of 16	



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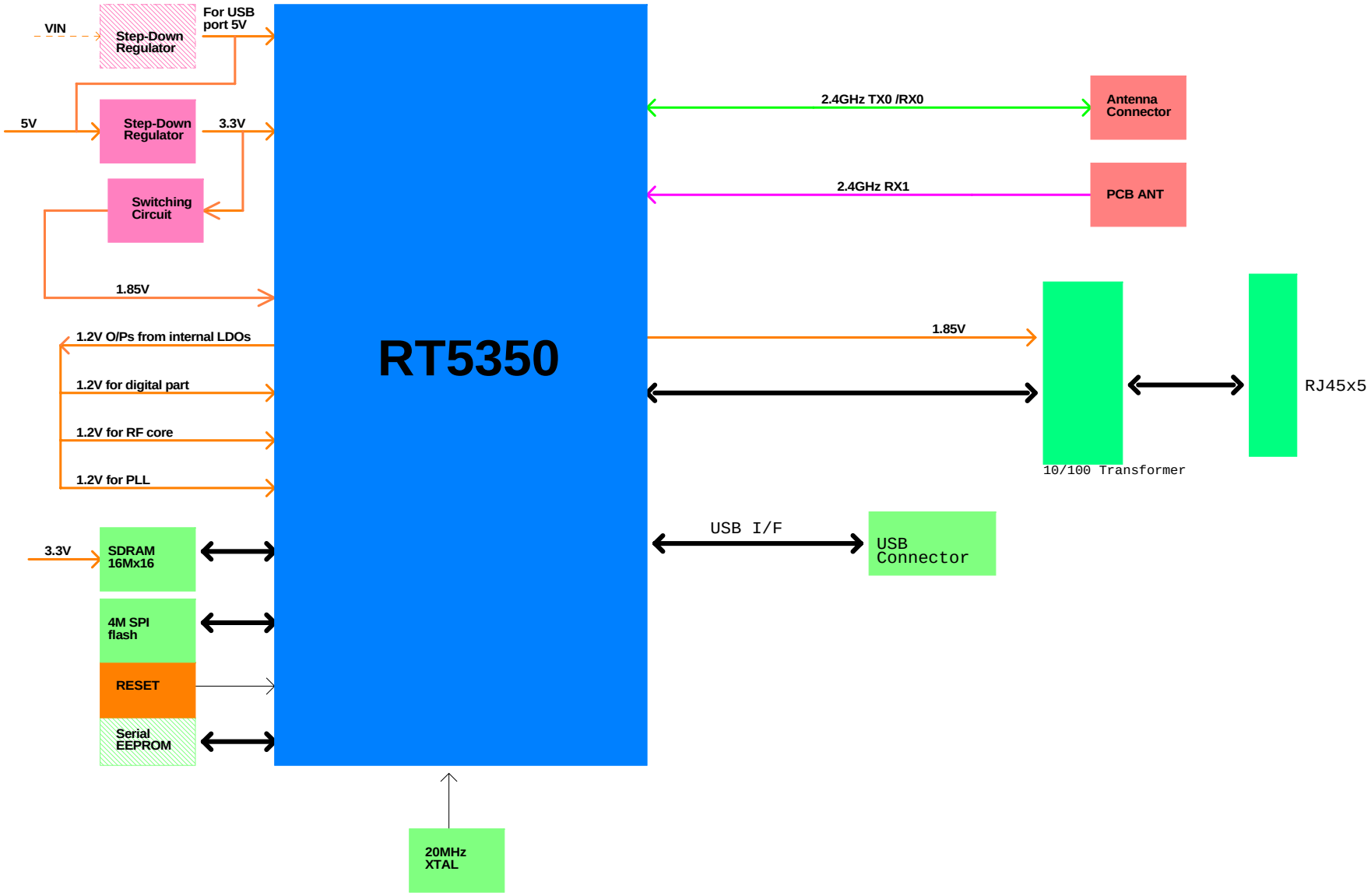
Title	LED	
Size B	Document Number 6-LED.SCH	Rev 2.2
Date: Monday, October 18, 2010	Sheet 15 of 16	



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Title	11n 2.4G AP-RT5350 Block	
Size C	Document Number RT5350 AP Block	Rev 2.2
Date: Tuesday, October 19, 2010	Sheet 16 of 16	