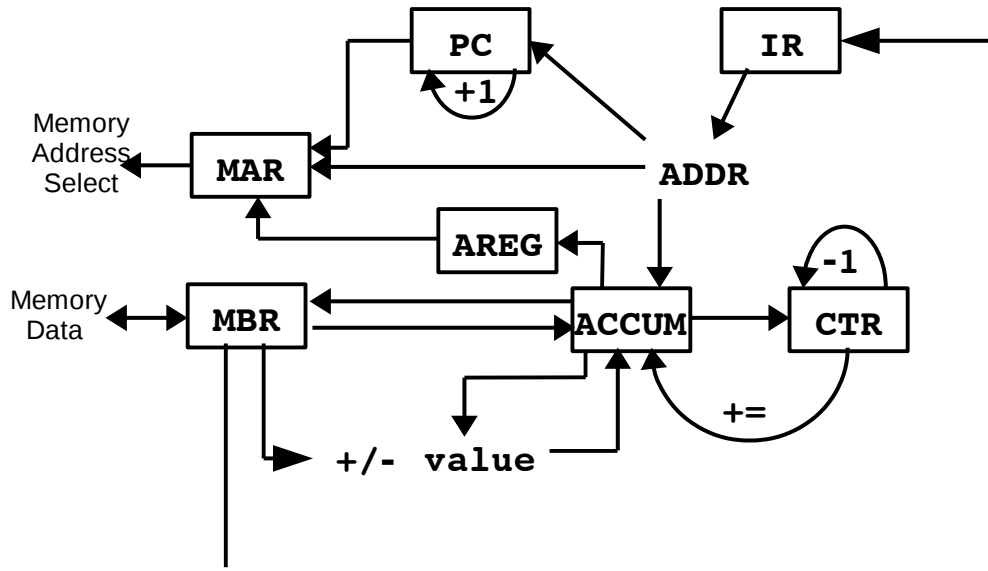


Data Pathways of the Simple Machine

A crude drawing showing the data pathways of the Simple Machine is shown below. These will be useful when describing the Simple Machine's Instructions in Register Transfer Language.



The Simple Machine's Datapaths

(**ADDR** is the decoded address part of the **IR**, or **IR(9:0)**)