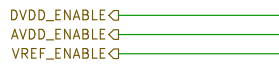


Power Sequencing

=====

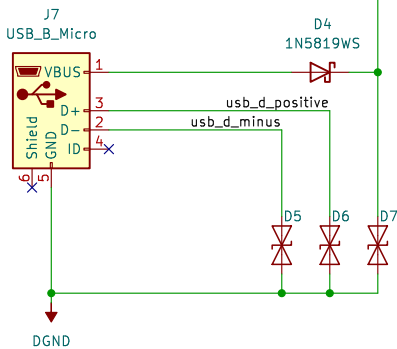
See DAC7118 sheet



JTAG SETUP:

=====

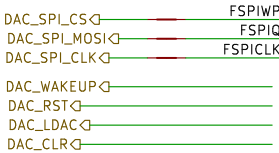
<https://docs.espressif.com/projects/esp-idf/en/stable/esp32s3/api-guides/jtag-debugging/configure-builtin-jtag.html>



ESPS3 SPI

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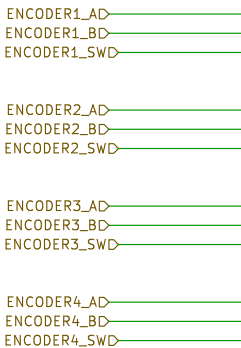
https://github.com/espressif/esp-idf/tree/v5.3.2/examples/peripherals/spi_master/lcd



ESPS3 PCNT

=====

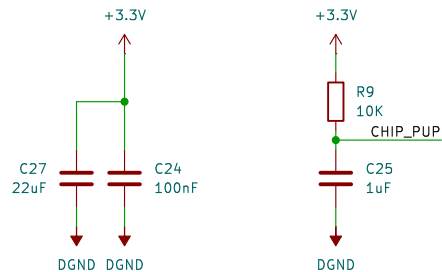
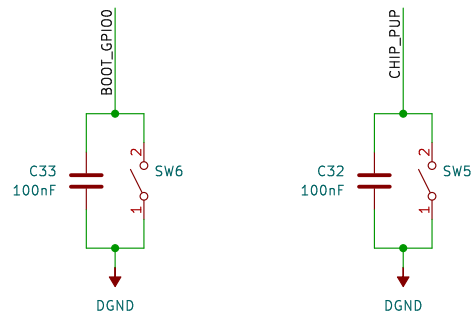
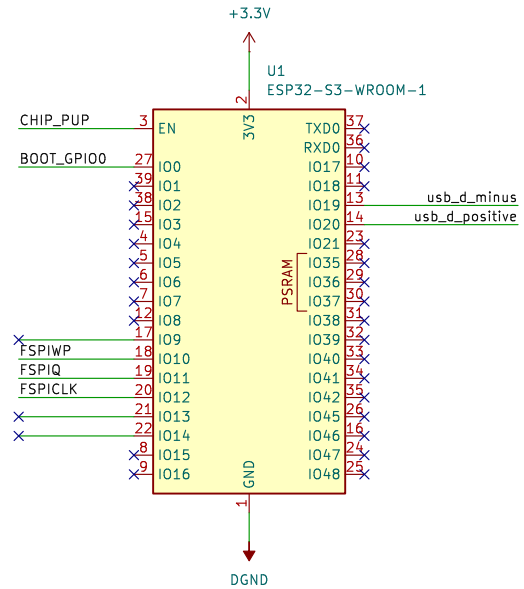
https://github.com/espressif/esp-idf/tree/v5.3.2/examples/peripherals/pcnt/rotary_encoder



ESP32-S3 datasheet

ESP32-S3-WROOM-1 datasheet

ESP32-S3-DevKitC-1_V1.1 Schematic



Created By: C Sutton

Sheet: /ESP32-S3/

File: mcu.kicad_sch

Title: Tiny4xPoweredStereoMixer

Size: A4

Date: 2024-10-22

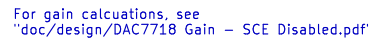
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Rev: D

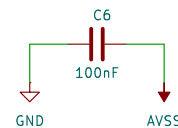
Id: 4/8

DAC7718 Datasheet

- IOVDD
- DVDD
- AVDD/AVSS
- reference voltage



LDAC: Update DAC outputs. Join JP4 for async mode.
CS: SPI bus chip select
WAKEUP: Restore SPI from sleep mode
RSTSEL: $V_{out} = 0v$
 or
 $V_{out} = -\text{Fullscale}$ (join JP1+JP3 and cut JP2)
CLR: All V_{out} pins connected to AGND



Id: 5/8

