

Sheet: PSU

File: PSU.sch

Sheet: InputSignal

ADIN_SDOUT
ADIN_SCLK
ADIN_LRCK
ADIN_MCLK

File: InputSignal.sch

Sheet: SignalProcessing

I2S_IN
I2S_SCLK_IN
I2S_LRCK_IN
I2S_MCLK_IN

File: SignalProcessing.sch

I2S_OUT
I2S_SCLK_OUT
I2S_LRCK_OUT
I2S_MCLK_OUT

Sheet: OutputSignal

ADOUT_SDIN
ADOUT_SCLK
ADOUT_LRCK
ADOUT_MCLK

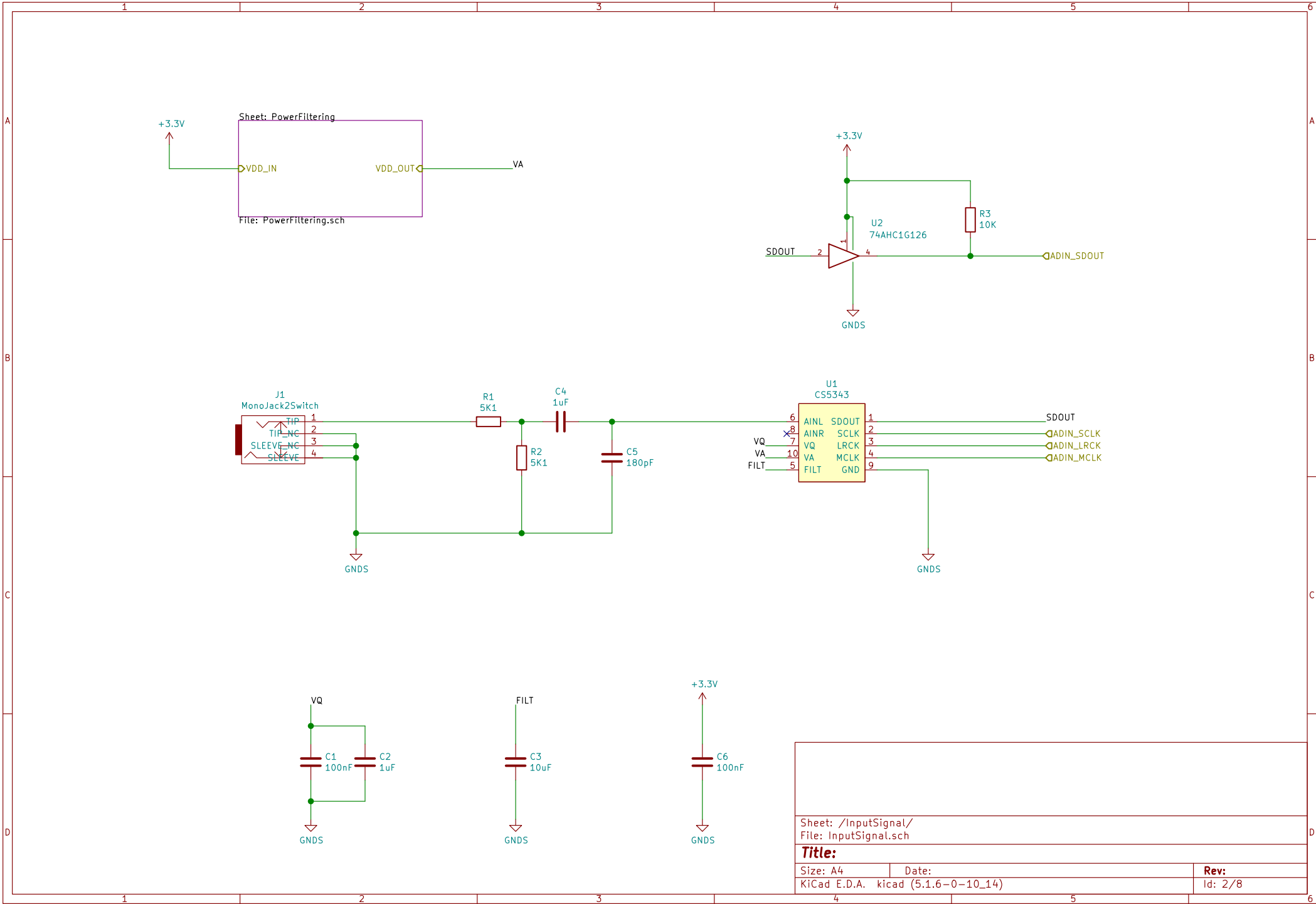
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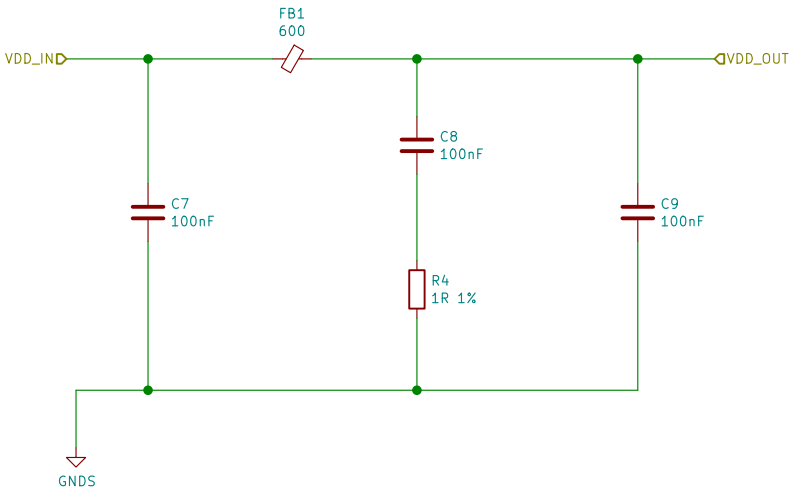
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File: CirrusDspPedal.sch

Title:

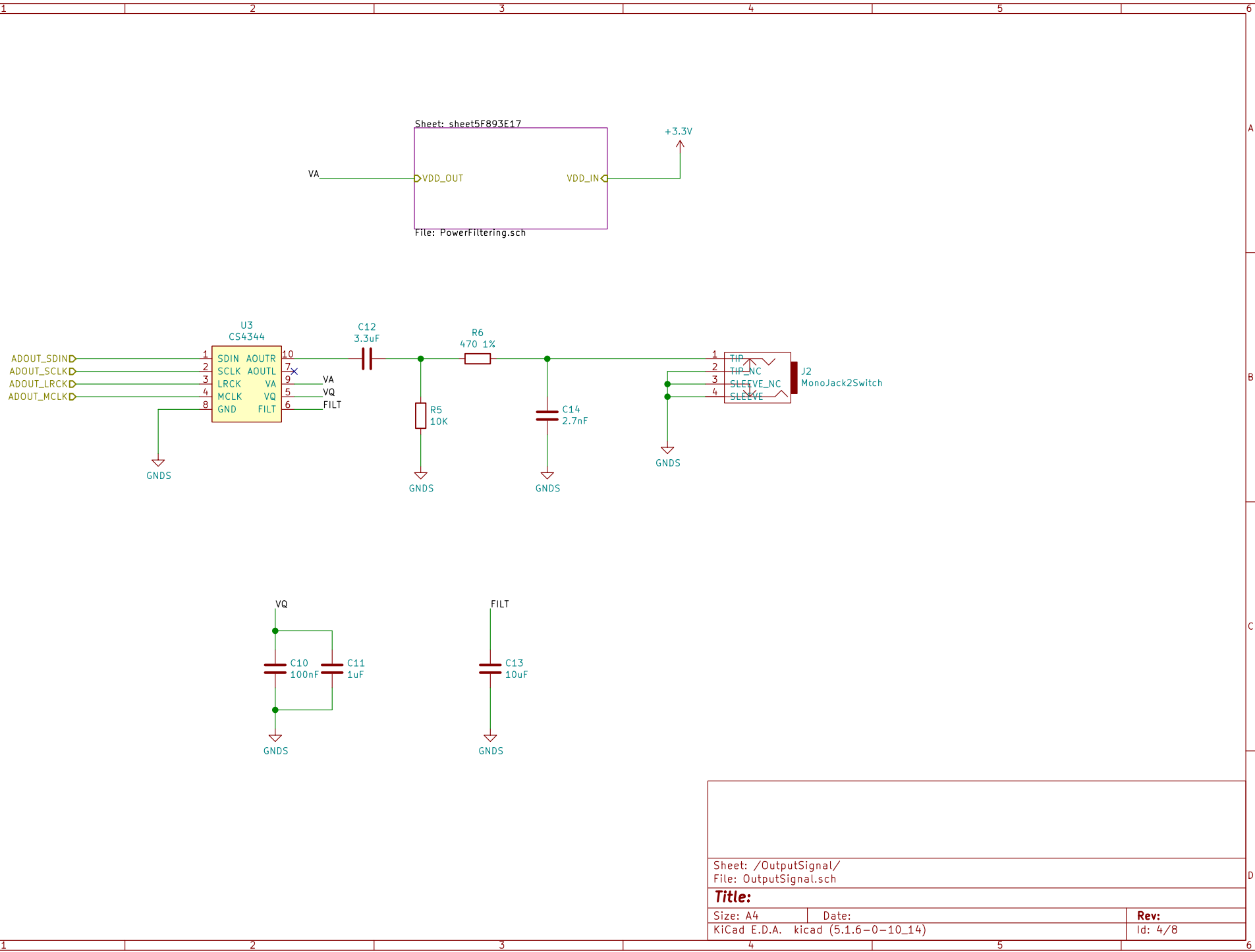
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KiCad E.D.A. kicad (5.1.6-0-10_14)

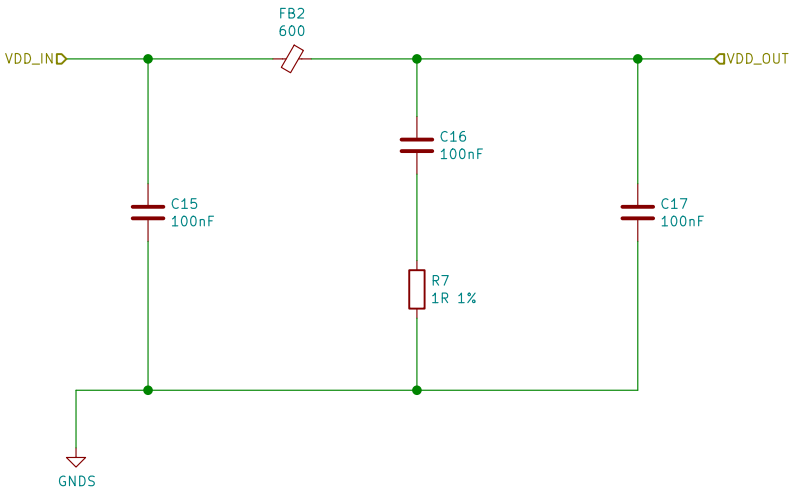
Rev:
Id: 1/8





Sheet: /InputSignal/PowerFiltering/ File: PowerFiltering.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad (5.1.6-0-10_14)		Id: 3/8

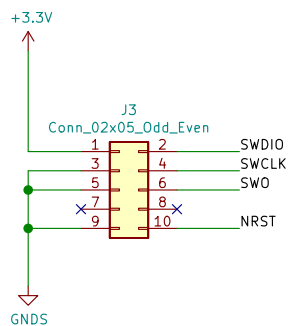
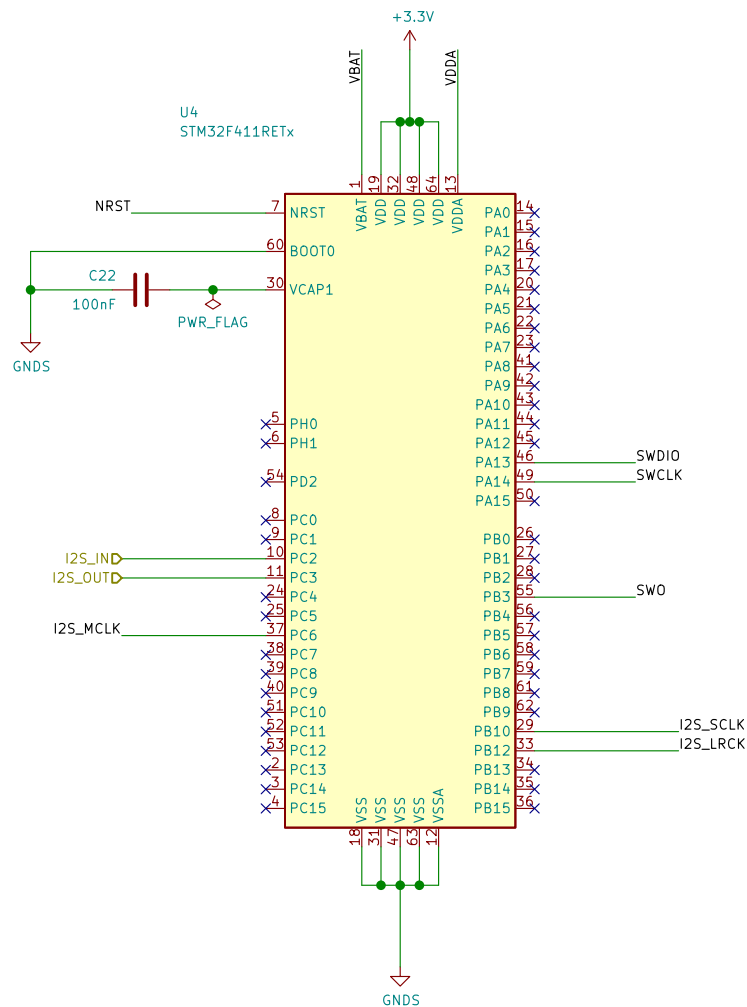
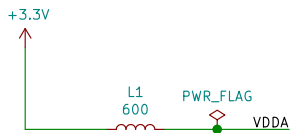
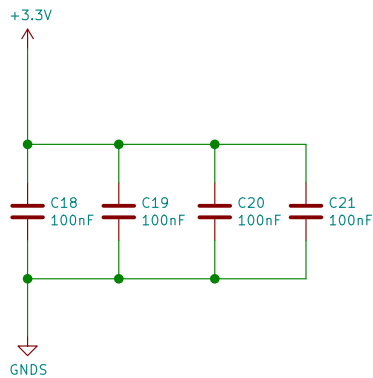
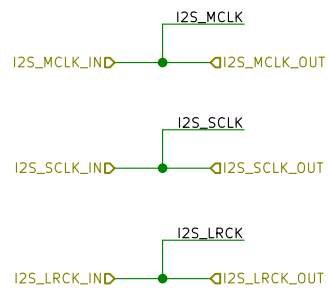
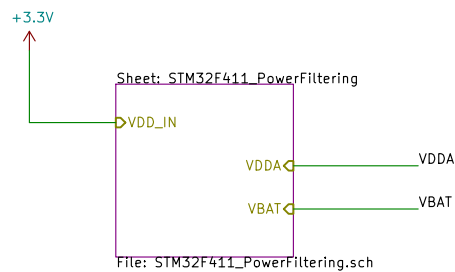


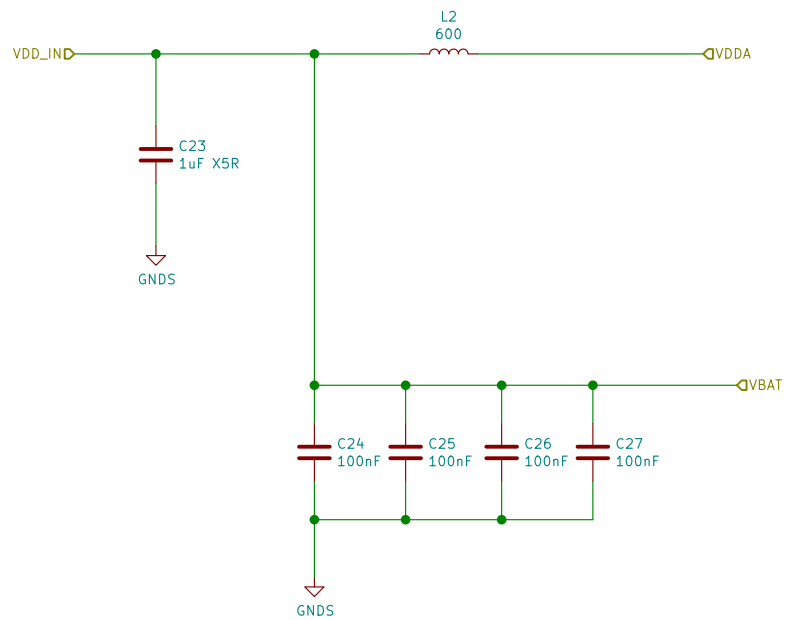


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KiCad E.D.A. kicad (5.1.6-0-10_14)		Id: 5/8





Sheet: /SignalProcessing/STM32F411_PowerFiltering/
File: STM32F411_PowerFiltering.sch

Title:

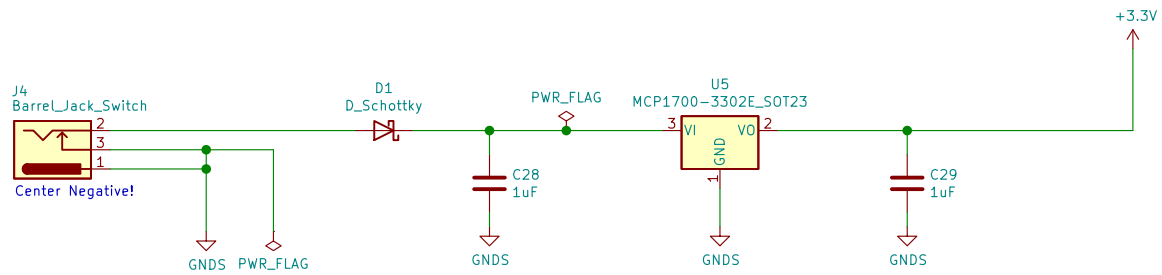
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Rev:

Id: 7/8



Sheet: /PSU/
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Rev:

Id: 8/8