









Using Internal VREF COMP1_INP PB0 24 PB1 25 PB2 26 PB3 56 PB4 57 PB5 58 PB6 59 PB7 60 SG0 ADC_IN3
DCBIAS_INVERT GPIO_OUTPUT T_SW0 DAC1_OUT1
DAC1_OUT2
DAC2_OUT1 PA6
21
PA7
42
PA8
43
PA9
44
PA10
45
PA11
46
PA12
49
PA13 TFT_CS PB9 62 PB10 30 PB11 33 PB12 34 PB13 35 PB14 36 PB14 37 PB15 37 TFT_RST T_SWDIO 50 PA14 T_SWCLK TIM1_CH1 ENC_A
TIM1_CH2 ENC_B
GPI0_EXTI2 ENC_SW PD2 55 CH1_MUX SG2 SPI3 <u>TFT_SCK</u> SPI3 SPI3 TFT_MOSI GPIO_EXTI14 BTN1 PC14-OSC32_IN GPIO_EXTI15 BTN2 PC15-OSVC32_OUT PB8-B00T0

PF0-OSC_IN

PF1-OSC_OUT

PG10-NRST

T_NRST

GPIO_EXTIO BTN3

GPIO_EXTI1 BTN4

U6A STM32G474RET
 SG0
 SG0_OUT

 SG1
 SG1_OUT

 SG2
 SG2_OUT



