



To use ADC IN2(PA1) both FX\_PWM\_CLK outputs must share tim21. Bridge JP3, JP5 and JP7.

Sheet: /PedalCo File: PedalContro		l			
Title:					
Size: A2	Date:			Rev:	
KiCad E.D.A. kicad (6.0.5-0)			ld: 2/2		
10			11		