

	1	2	3	4	5	6	7	8
A								
B								
C								
D								
E								
F								

Sheet: STPMIC1_Board

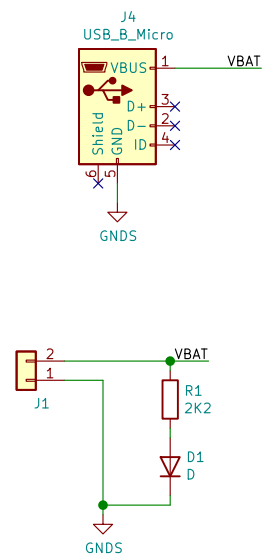
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Sheet: PF3000_Board

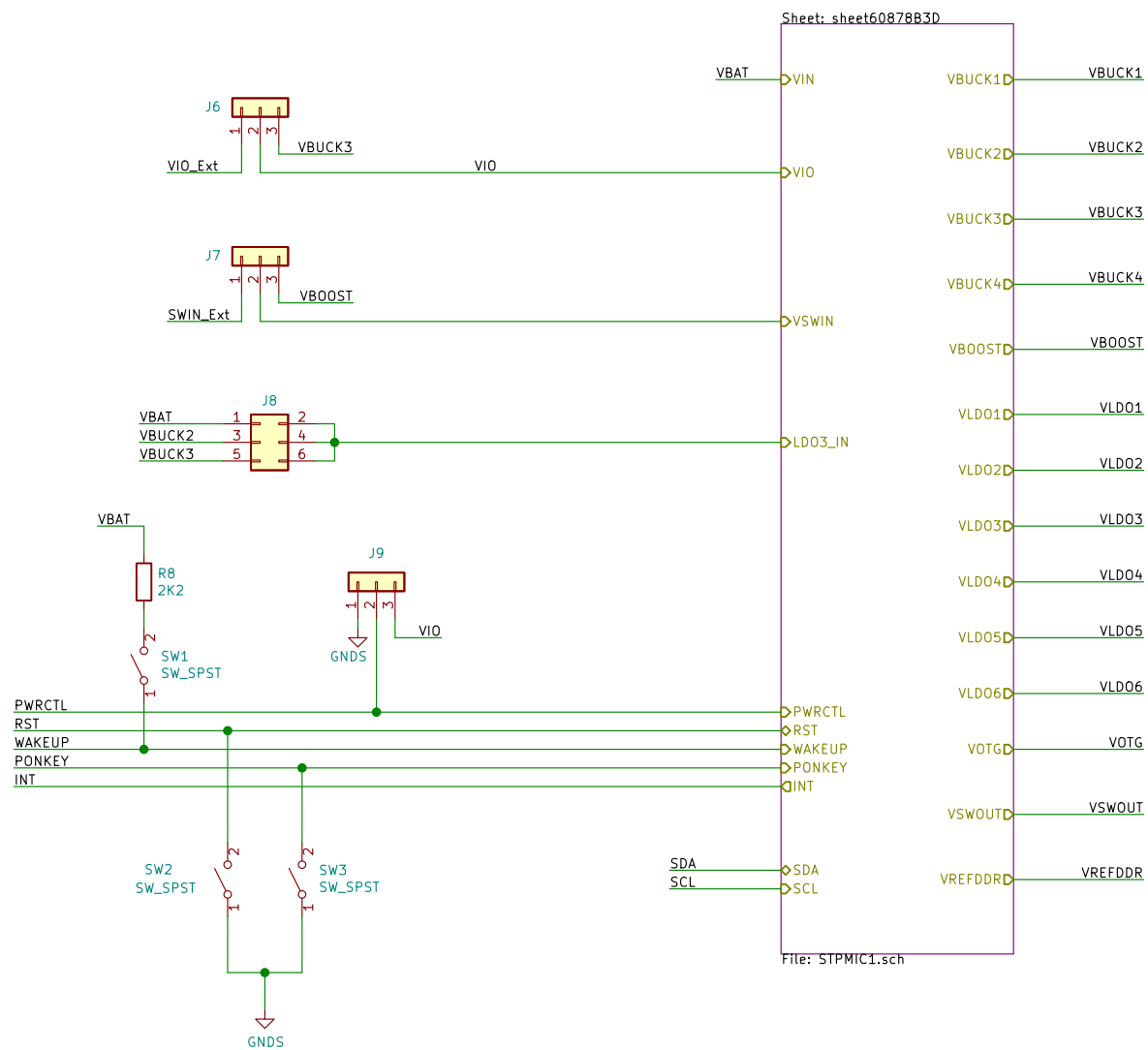
File: PF3000_Board.sch

Chris Sutton		
Sheet: /		
File: STPMIC1_MC32PF3000_Board.sch		
Title: System Overview		
Size: A3	Date: 2021-03-02	Rev: A
KiCad E.D.A. kicad (5.1.8-0-10_14)	Id: 1/5	

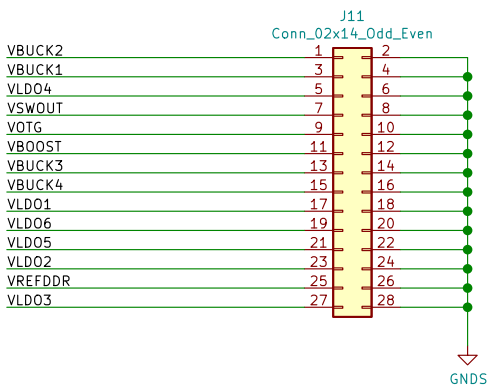
POWER INPUT



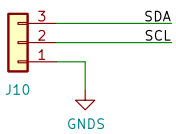
POWER MANAGEMENT IC



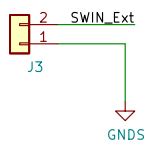
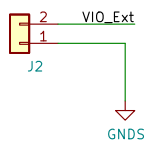
POWER OUTPUT



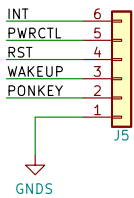
DEBUG OUTPUT



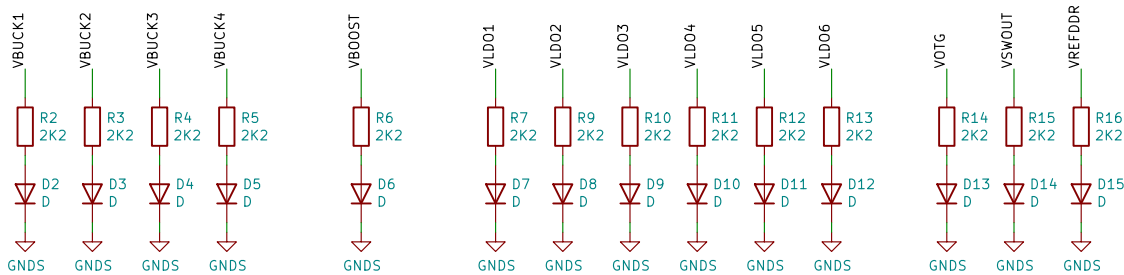
SUBSYSTEM JUMPERS



DIAGNOSTICS OUTPUT



OUTPUT POWER INDICATORS



Development connections for STPMIC1

Chris Sutton

Sheet: /STPMIC1_Board/
File: STPMIC1_Board.sch

Title: STPMIC1 DEV

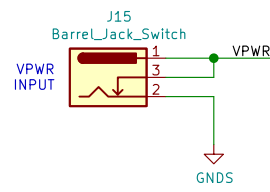
Size: A3 Date: 2021-03-02
KiCad E.D.A. kicad (5.1.8-0-10_14)

Rev: A

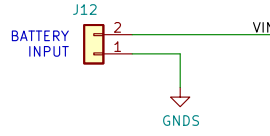
Id: 2/5

POWER INPUT

3.7 V to 5.5 V Supply input (Default).
Shorts VPWR to ground when not used (pin 3-2)

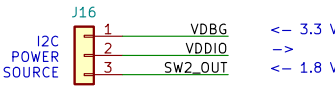


3.1 V to 4.5 V Supply input for batteries

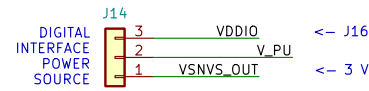
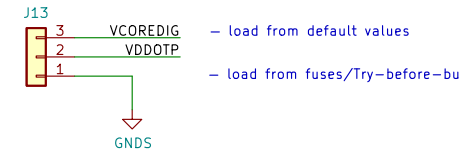


SUBSYSTEM POWER JUMPERS

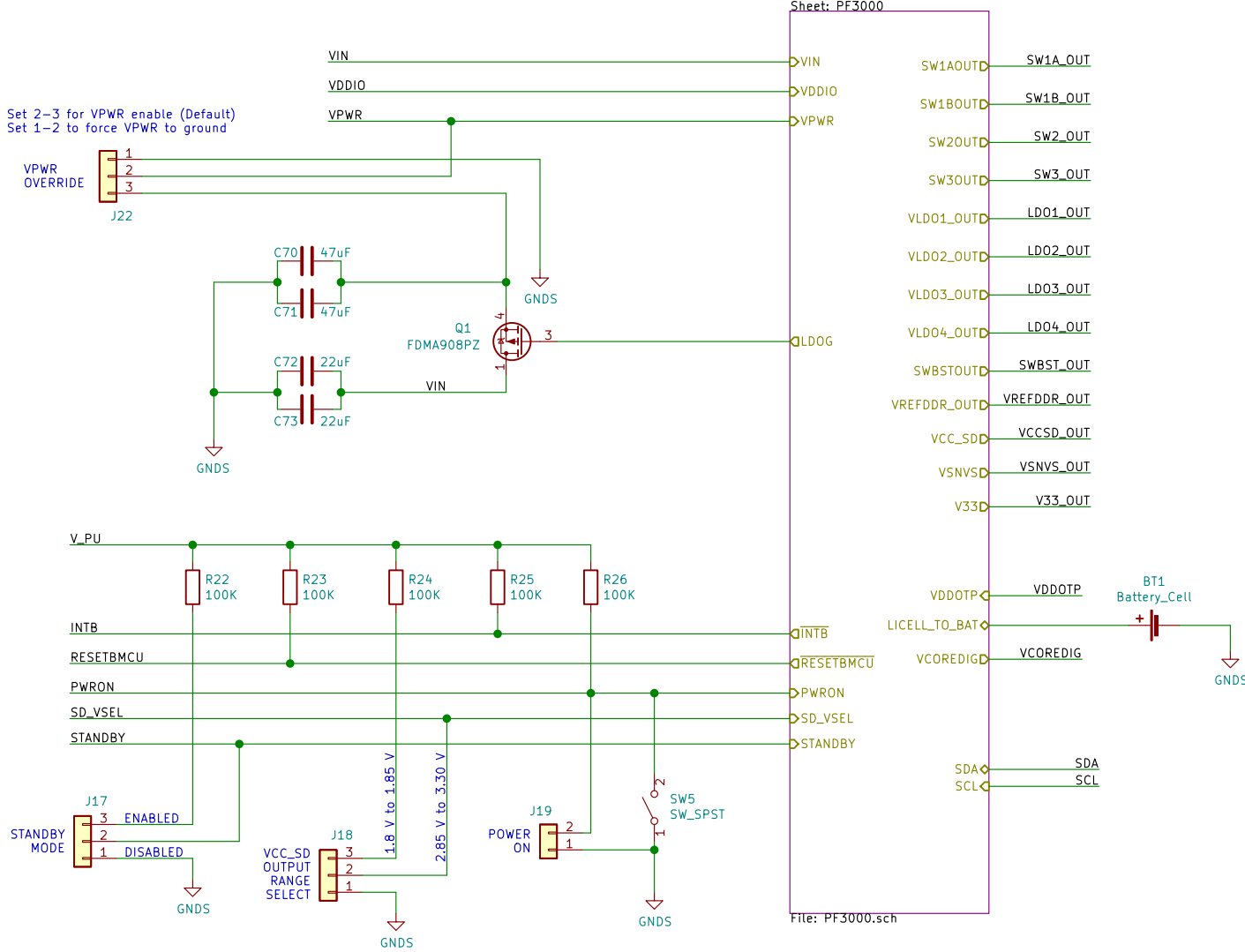
Ensure that VDDIO is always lesser than or equal to VIN



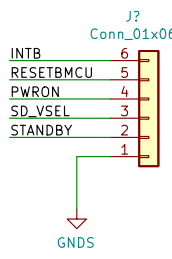
Supply to program OTP fuses. Connect VDDOTP to GND during normal application



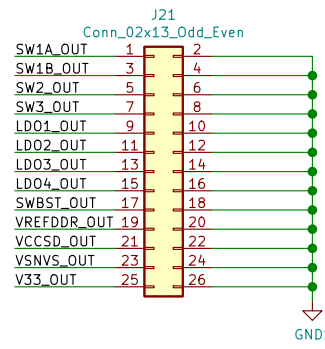
POWER MANAGEMENT IC



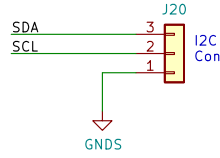
DIAGNOSTICS OUTPUT CONNECTOR



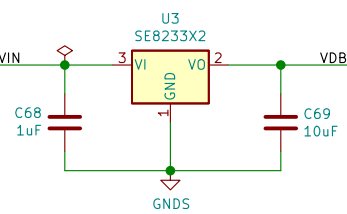
POWER OUTPUT CONNECTOR



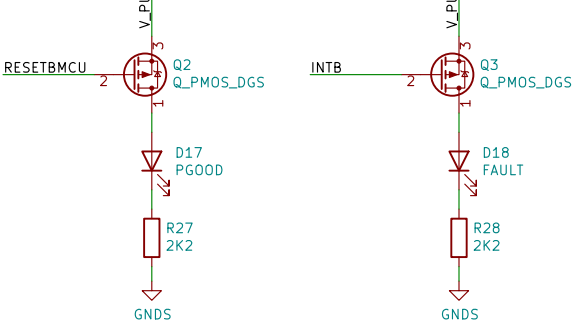
DEBUG OUTPUT CONNECTOR

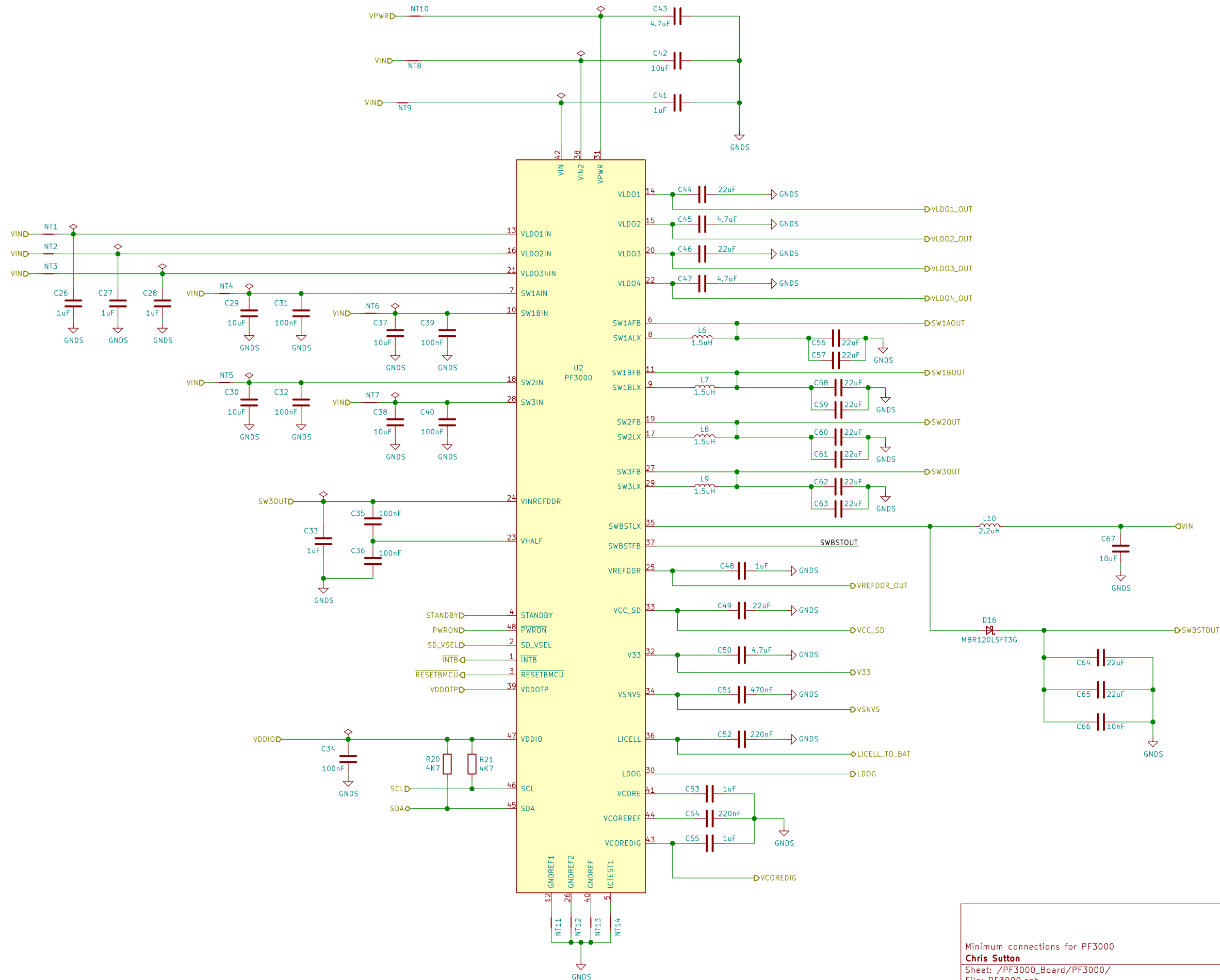


VDBG LDO



SYSTEM STATUS INDICATORS





Minimum connections for PF3000

Chris Sutton

Sheet: /PF3000_Board/PF3000/

File: PF3000.sch

Title: PF3000 MAIN

Size: A3

Date: 2021-03-02

Rev: A

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Id: 5/5