

ZARC I/O Board - Connectors

TITLE

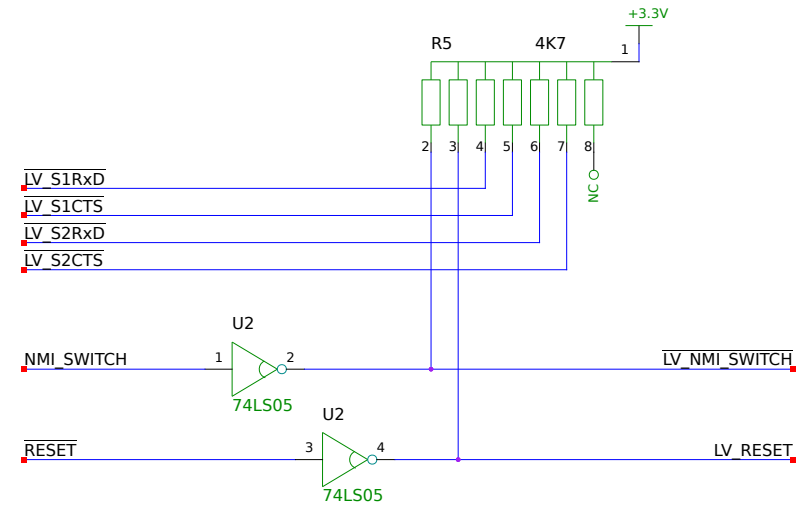
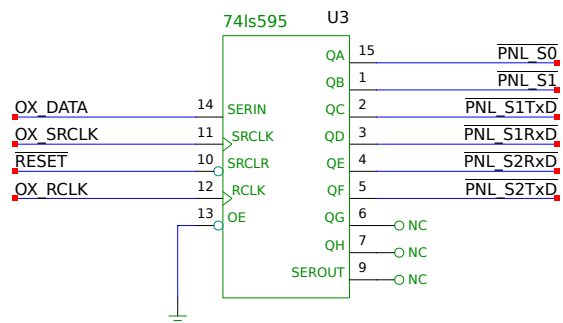
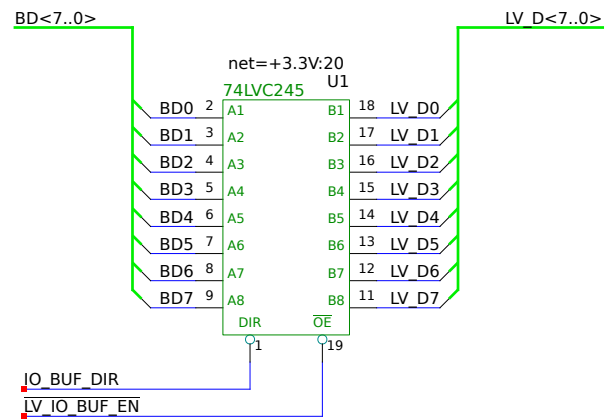
FILE: 1_Conns.sch

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ZARC I/O Board - Level Translation and Output Expander

TITLE

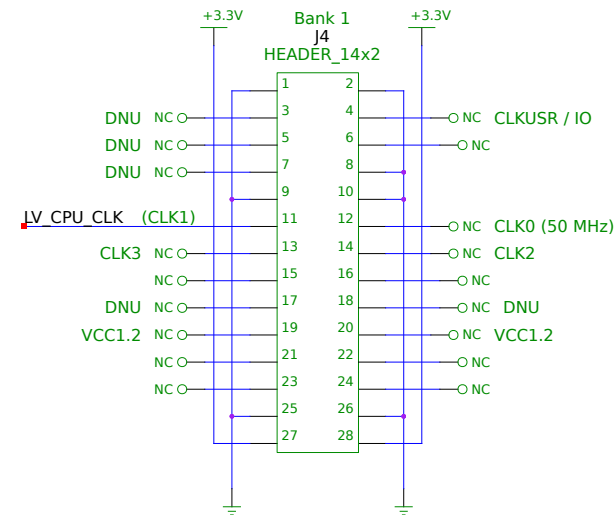
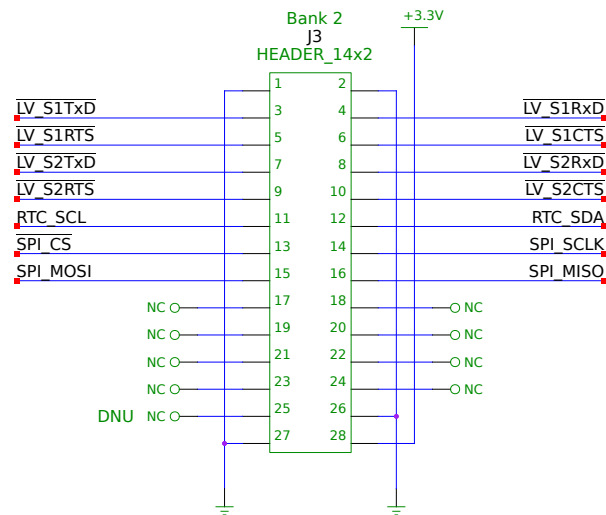
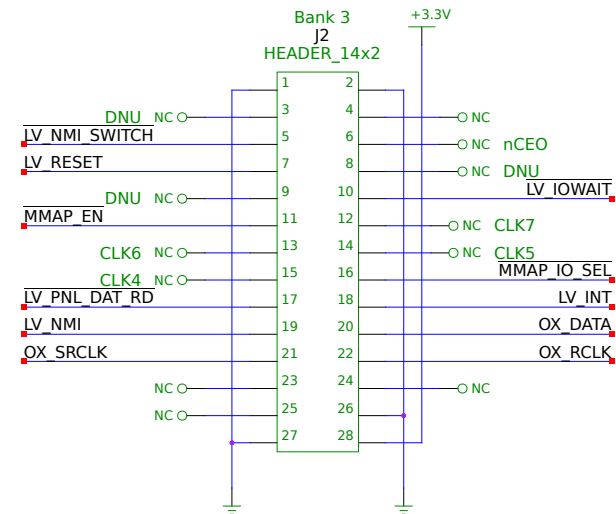
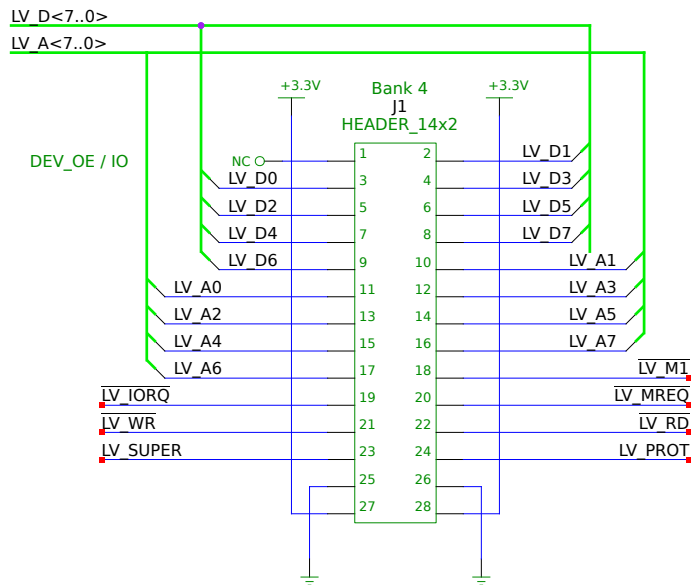
FILE: 2_LevelTrans.sch

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DNU: Do Not Use

ZARC I/O Board - FPGA Interconnect

TITLE

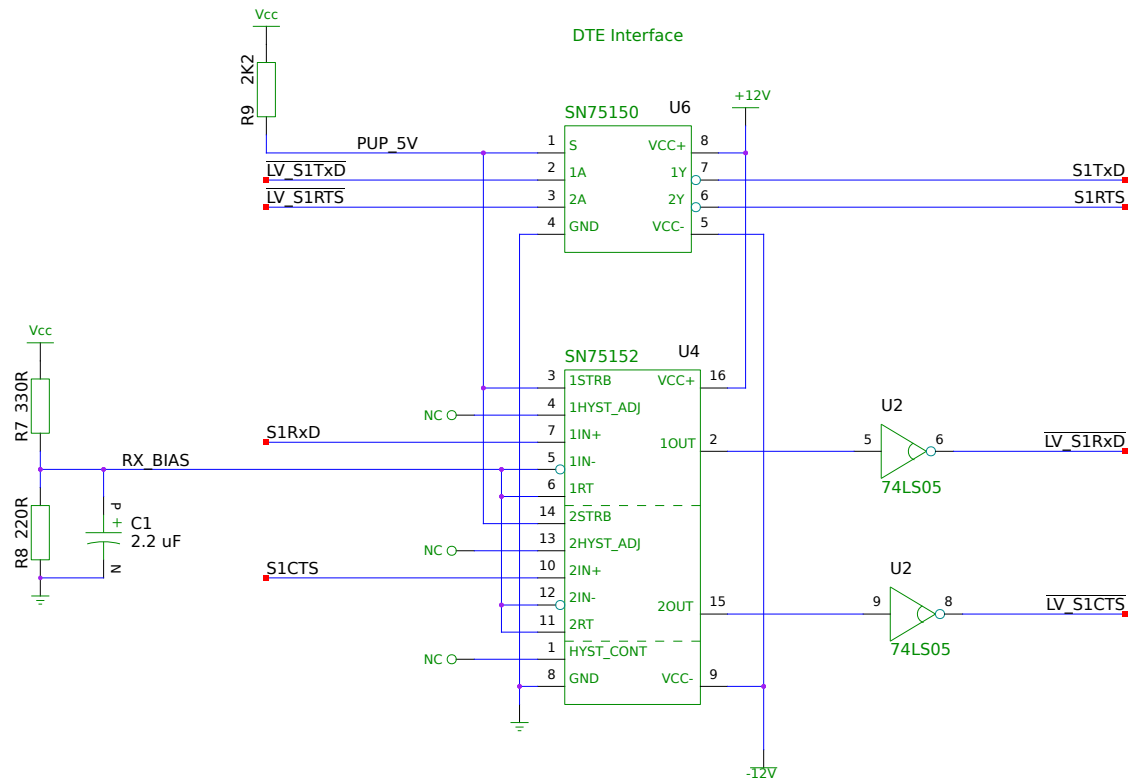
FILE: 3_FPGA.sch

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ZARC I/O Board - Serial Port 1

TITLE

FILE: 4_Serial1.sch

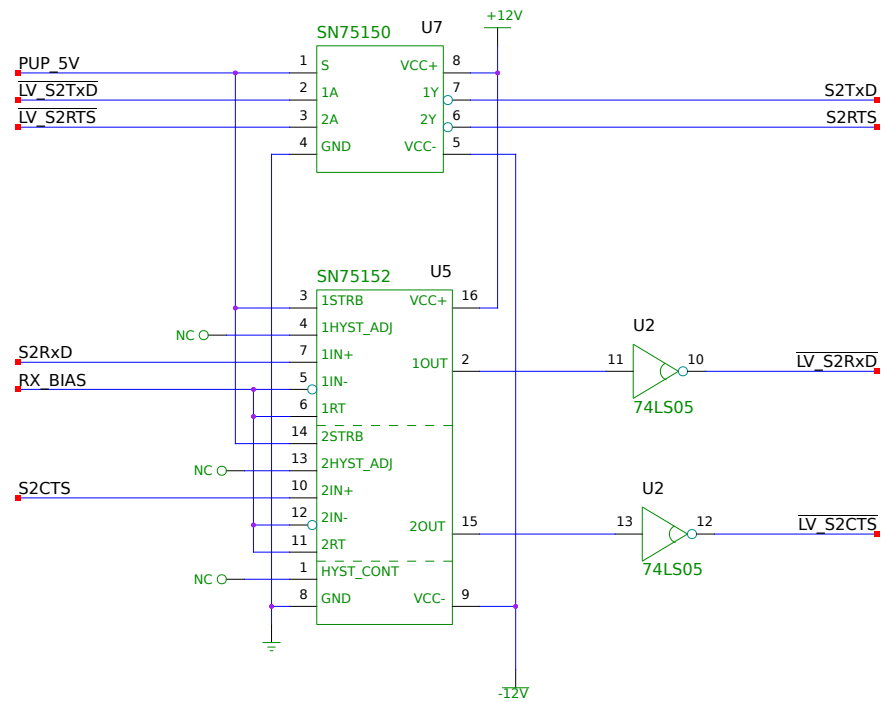
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DTE Interface



ZARC I/O Board - Serial 2

TITLE

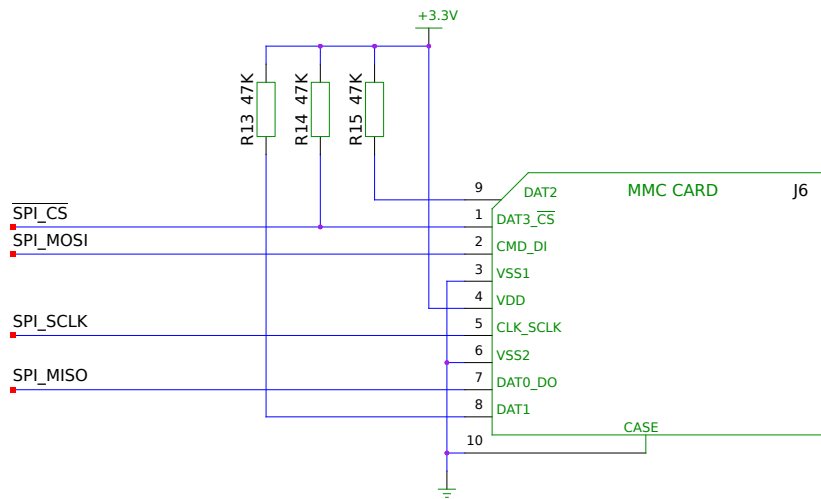
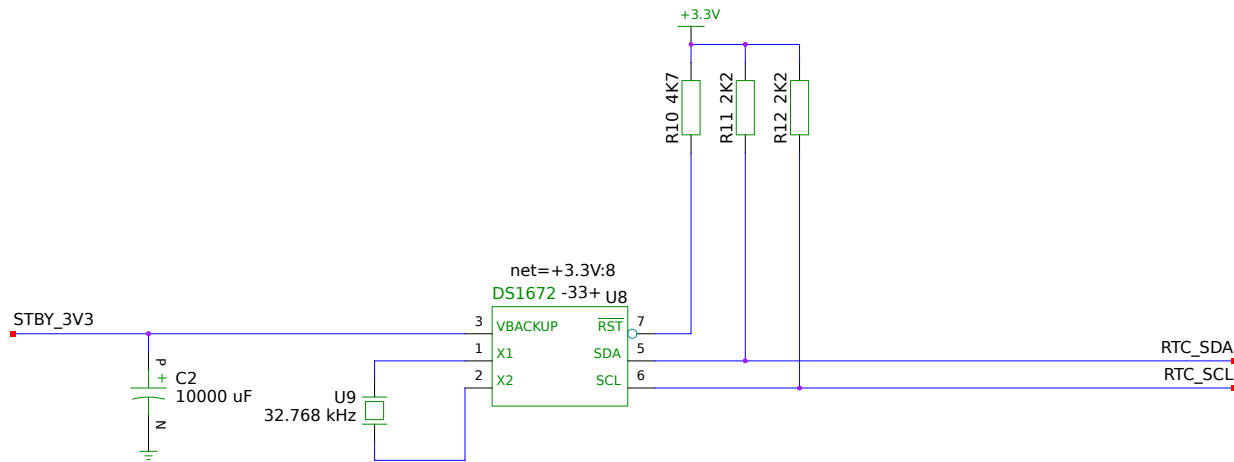
FILE: 5_Serial2.sch

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ZARC I/O Board - Real Time Clock (RTC) & Memory Card
TITLE

FILE: 6_RTC_MMC.sch

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