

Boyang Wang (Albert)

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Goal - To obtain a full time junior front-end web developer position

SUMMARY

- 6 months in web development (6 months in front-end)
- 6 months' internship focusing on setting up websites for company and clients
- strong verbal and written communication and interpersonal skills

TECHNICAL SKILLS

Programming:	JavaScript, HTML, CSS, PHP, Python, Java, C, Verilog
Web Technology:	Responsive Web, AngularJS (MVC), NodeJS, jQuery
Databases:	MySQL, SQL
Operating Systems:	Windows, OS X, Linux,
IDE/Tools:	WebStorm, Eclipse, Sublime Text 2, Notepad++

WORK EXPERIENCE

Smoothies Technology Inc, Santa Clara – **Front End Web Developer** 07/2015-Present

- Designed and developed website for a staffing company to promote their business of partners and members (HTML, CSS, JavaScript, Bootstrap)
- Redesigned and developed a homepage (HTML, CSS, Bootstrap)
- Design and Develop website for analytics training program (HTML, CSS, Bootstrap)
<http://academy.focuskpi.com/>
- Develop an internal platform including to-do list, filing information, and reminder (HTML, CSS, AngularJS, NodeJS, MongoDB)
- Implemented web crawling technology to extract patients' information and reviews from medicine website (Python, SQL)

Technology used: HTML, CSS, JavaScript, AngularJS, NodeJS, MongoDB, Bootstrap

KEY PROJECT

Individual Website 10/2015-Present

- <http://crazyalbertw.github.io/myweb/>
- Designed and developed web pages – keep updating (HTML, CSS, Bootstrap)
- Developed a battle ship game and to-do list (HTML, CSS, JavaScript, AngularJS, Bootstrap)

Technology used: HTML, CSS, JavaScript, AngularJS, Bootstrap

Stereo Vision System with Improved SGM Path Cost Propagation Method 09/2014-05/2015

- Generated real time Disparity video using VMOD-CAM stereo camera and Spartan-6 FPGA
- Implemented improved Semi-Global Matching Algorithm (Branch SGM) with Census Transform. Utilized 10% more hardware resources and reduced 10% to 30% average errors compared to SGM Algorithm

Technology used: Verilog, C/C++, and VHDL

EDUCATION

M.S. Electrical Engineering concentration in Digital Systems and Logic Design

- San Jose State University, San Jose CA (2013-2015, graduated)

B.S. Electrical Engineering & Automation

- Beijing Information Science & Technology University, Beijing, China (2009-2013)