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TTL/CMOS logic chips comparison table for the "Retro Chip Tester Professional"

Tests that have been verified with a real chip are highlighted in green.
Tests that have been reported to work but have not been verified yet are highlighted in yellow.
Tests marked in red have never been tested with a real chip but may work.
"RAM" or "ROM": use the corresponding entry in the main menu; "n/a": not available (in most cases it cannot be implemented); "TBD": to be done
Some of these ICs are marked with "(!)". Please refer the manual before testing this chip. "(*)" means analog device, test result has limited informative value.
If you have some of the chips marked in yellow/red or not listed and do not need them, I would appreciate these to complete the tests.
"Setting" entry means that the IC is not available in the menu, you have to use the IC mentioned in this entry.

Notes:

Fairchild 9xXXX = 74xXXX (e.g. 9LS54 = 74LS65); **93XXX = 74XXX** (e.g. 93170 = 74170) test with 74XXX; CMOS: **14xxx = 40xxx** (e.g. HD14193 = 40193) test with 40xxx
AMD Am25xXXX = 74xXXX (e.g. Am25LS158 = 74LS158, except listed); **RFT D1xxD = 74xx, DLxxxD = 74xxx** (e.g. D172D = 7472, DL132D = 74132)

Identifier	Pins	Status	UdSSR	UdSSR	DDR	Cyrillic	Philips	Siemens	Setting	Remark
7400	14	v15	KR1531LA3	K555LA3	DL000D	ЛА3	FJH131	FLH101		Quad 2-Input NAND Gate
7401	14	v14		K555LA8		ЛА8	FJH231	FLH201		Quad 2-Input NAND Gate with Open Collector Outputs
74H01	14	v19					GJH231			Quad 2-Input NAND Gate with Open Collector Outputs
7402	14	v15	KR531LE1	K555LE1	DL002N	ЛЕ1	FJH221	FLH191		Quad 2-Input NOR Gate
7403	14	v14		K555LA9	DL003D	ЛА9	FJH291	FLH291		Quad 2-Input NAND Gate with Open Collector Outputs (different pInput than 7401)
7404	14	v15	KR1531LN1	K555LN1	DL004D	ЛН1	FJH241	FLH211		Hex Inverter
7405	14	v15	KR531LN2	K555LN2		ЛН2	FJH251	FLH271		Hex Inverter
7406	14	v14		K555LN3		ЛН3		FLH481		Hex Inverter
7407	14	v14		K555LP9		ЛП9		FLH491		Hex Buffer/Driver with 30V Open Collector Outputs
7408	14	v15	KR531LI1	K555LI1	DL008D	ЛИ1		FLH381		Quad 2-Input AND Gate
7409	14	v13		K555LI2		ЛИ2		FLH391		Quad 2-Input AND Gate with Open Collector Outputs
7410	14	v15	KR1531LA4	K555LA4	DL010D	ЛА4	FJH121	FLH111		Triple 3-Input NAND Gate
7411	14	v14	KR531LI3	K555LI3	DL011D	ЛИ3		FLH581		Triple 3-Input AND Gate
7412	14	v13	K155LA10	K555LA10		ЛА10		FLH501		Triple 3-Input NAND Gate with Open Collector Outputs
7413	14	v15	K155TL1			ТЛ1	FJL131	FLH351		Dual Schmitt Trigger 4-Input NAND Gate
7414	14	v14	K155TL2	K555TL2	DL014D	ТЛ2				Hex Schmitt Trigger Inverter
7415	14	v13		K555LI4		ЛИ4				Triple 3-Input AND Gate with Open Collector Outputs
7416	14	v13	K155LN5			ЛН5		FLH481T		Hex Inverter
7417	14	v14	K155LN4			ЛН4		FLH491T		Hex Buffer/Driver with 15V Open Collector Outputs
7418	14	v13								Dual 4-Input NAND Schmidt Trigger
7419	14	v14					FJJ401			Hex Schmitt Trigger Inverter
7420	14	v14	KR1531LA1	K555LA1	DL020D	ЛА1	FJH111	FLH121		Dual 4-Input NAND Gate
7421	14	v15		K555LI6	DL021D	ЛИ6				Dual 4-Input AND Gate
7422	14	v13	K155LA7	K555LA7		ЛА7		FLH611		Dual 4-Input NAND Gate with Open Collector Outputs
7423	16	v15	K155LE2			ЛЕ2		FLH511		Expandable Dual 4-Input NOR Gate with Strobe
7424	14	v16								Quad 2-Input NAND Gate
7425	14	v15	K155LE3			ЛЕ3		FLH521		Dual 4-Input NOR Gate with Strobe
7426	14	v15	K155LA11		D126D	ЛА11	FJH301	FLH291U		Quad 2-Input NAND Gate with 15V Open Collector Outputs
7427	14	v15	K155LE4	K555LE4		ЛЕ4		FLH621		Triple 3-Input NOR Gate
7428	14	v15		K555LE5		ЛЕ5		FLH661		Quad 2-Input NOR Gate Buffer
7430	14	v14	KR531LA2	K555LA2	DL030D	ЛА2	FJH101	FLH131		8-Input NAND Gate
7431	16	v13								Hex Delay Elements
7432	14	v14	K155LL1	K555LL1	DL032D	ЛЛ1		FLH631		Quad 2-Input OR Gate
7433	14	v15		K555LE11		ЛЕ11				Quad 2-Input NOR Gate Buffer with Open Collector Outputs
7434	14	v13		K555LI9		ЛИ9				6x Buffer
7435	14	v13	K555IM7			ИМ7				6x Buffer, Open Collector
7436	14	v13								Quad 2-Input NOR Gate (different pInput than 7402)
7437	14	v13		K555LA12	DL037D	ЛА12		FLH531		Quad 2-Input NAND Gate
7438	14	v14		K555LA13	DL038D	ЛА13		FLH541		Quad 2-Input NAND Gate with Open Collector Outputs
7439	14	v13								Quad 2-Input NAND Gate
7440	14	v14		K555LA6	DL040D	ЛА6	FJH141	FLH141		Dual 4-Input NAND Gate
7441	16	v16								BCD to decimal Decoder / Nixie tube Driver
7442	16	v14		K555ID6		ИД6	FJH261	FLH281		Binary-Coded Decimal
7443	16	v16						FLH361		Excess-3 to Decimal Decoder
7444	16	v16						FLH371		Excess-3-Gray to Decimal Decoder
7445	16	v16		K555ID24		ИД24		FLL111		BCD to decimal Decoder/Driver
7446	16	v15			D146D			FLL121U		BCD to 7-segment Decoder/Driver
7447	16	v15			D147D			FLL121T		BCD to 7-segment Decoder/Driver ("6" and "9" without tail, a few 7447 have a tail, use 74247 instead)
7448	16	v15						FLH551		BCD to 7-segment Decoder/Driver
7449	14	v15								BCD to 7-segment Decoder/Driver
7450	14	v16	K131LR1		D150D	ЛР1	FJH151	FLH151		Dual 2-Wide 2-Input AND-OR-INVERT Gate
7451	14	v15			D151D		FJH161	FLH161		Dual 2-Wide 2-Input AND-OR-INVERT Gate (51, H51, S51 only)
74LS51	14	v15		K555LR11	DL051D	ЛР11				Dual 2-2-3-3-Input AND-OR-INVERT Gate (L51, LS51, ...)
7452	14	v18								Expandable 4-Wide 2-Input AND-OR Gate
7453	14	v15	K131LR3		D153D	ЛР3	FJH171	FLH171		Expandable 2-2-2-2-Input AND-OR-INVERT Gate (53 only)
74H53	14	v13								Expandable 2-2-2-3-Input AND-OR-INVERT Gate (H53 only)
7454	14	v15			D154D		FJH181	FLH181		2-2-2-2-Input AND-OR-INVERT Gate (54 only)
74H54	14	v15					GJH181			2-2-2-3-Input AND-OR-INVERT Gate (H54 only)
74LS54	14	v14		K555LR13		ЛР13				2-2-3-3-Input AND-OR-INVERT Gate (L54, LS54, ...)
74H55	14	v13								2-Wide 4-Input AND-OR-INVERT Gate (H55 only)
74LS55	14	v13								2-Wide 4-Input AND-OR-INVERT Gate (L55, LS55, ...)
7456	8	v23								50:1 frequency divider
7457	8	v23								60:1 frequency divider
7458	14	v13								2-wide 2-Input and 2-wide 3-Input AND-OR Gates
7460	14	v16	K155LD1		D160D	ЛД1	FJY101	FLY101		Dual 4-Input expander
7461	14	v13								Triple 3-Input Expander
7462	14	v17								3-2-2-3-Input Expander
7463	14	n/a								Hex current sensing interface Gates
7464	14	v16		K555LR9		ЛР9				4-3-2-2 Input AND-OR-Invert Gate
7465	14	v16	KR531LR10			ЛР10				4-2-3-2 Input AND-OR-INVERT Gate with Open Collector Output
7468	16	v16								Dual 4-Bit Decade Counters (LS68, not L68)
7469	16	v16								Dual 4-Bit Binary Counters (LS69, nott L69)
7470	14	v15					FJJ101	FLJ101		AND Gated J-K master-slave Flip-Flop, asynchronous Preset and Clear
74H71	14	v17								AND-OR Gated J-K master-slave Flip-Flop, Preset (H71 only)
74L71	14	v13								AND-OR-gated R-S master-slave Flip-Flop, Preset and Clear (L71 only)
7472	14	v15	K155TV1		D172D	TB1	FJJ111	FLJ111		AND Gated J-K master-slave Flip-Flop, asynchronous Preset and Clear
7473	14	v14					FJJ121	FLJ121		Dual J-K Flip-Flop Flip-Flop with Clear
7474	14	v14	K131TM2	K555TM2	DL074D	TM2	FJJ131	FLJ141		Dual D Positive Edge triggered Flip-Flop with Preset and Clear
7475	16	v14		K555TM7	D175D	TM7	FJJ181	FLJ151		4-bit Bistable Latch, complementary Outputs
7476	16	v18					FJJ191	FLJ131		Dual J-K Flip-Flop with Preset and Clear
74LS76	16	v22								Dual J-K Flip-Flop with Preset and Clear, negative triggered
7477	16	v14	SN74LS77			TM5				4-Bit Bistable Latch
74H78	14	v13								Dual J-K Flip-Flop, Preset, Common Clock and Common Clear, positive Clock (H78 only)
74L78	14	v13								Dual J-K Flip-Flop, Preset, Common Clock and Common Clear, positive Clock (L78 only)
74LS78	14	v15								Dual J-K Flip-Flop, Preset, Common Clock and Common Clear, negative Clock (LS78, ...)
7479	14	v16								Dual D positive edge triggered Flip-Flop, asynchronous Preset and Clear
7480	14	v16	K155IM1			ИМ1	FJH191	FLH221		Gated Full Adder

Identifier	Pins	Status	UdSSR	UdSSR	DDR	Cyrillic	Philips	Siemens	Setting	Remark
7481	14	RAM	K155RU1		D181D	РУ1		FLQ111		16-bit RAM (4 x 4)
7482	14	v16	K155IM2			ИМ2	FJH201	FLH231		2-bit binary Full Adder
7483	16	v15	K155IM3	K555IM3		ИМ3	FJH211	FLH241		4-Bit Binary Full Adder
7484	16	RAM	K155RU3			РУ3		FLQ121		16-bit RAM (4 x 4)
7485	16	v14	KR531SP1	K555SP1		СП1		FLH431		4-Bit Magnitude Comparator
74C85	16	v17								4-Bit Magnitude Comparator
7486	14	v14	KR531LP5	K555LP5	DL086D	ЛП5	FJH271	FLH341		Quad 2-Input Exclusive-OR Gate
74L86	14	v18								Quad 2-Input Exclusive-OR Gate
7487	14	v15						FLH441		4-bit true/complement/zero/one Element
7488	16	ROM					FJQ101	FLR101		256-bit ROM (32x8)
7489	16	RAM					FJQ111	FLQ101		64-bit RAM (16x4), inverted Outputs
7490	14	v14 (!)		K555IE2	DL090D	ИЕ2	FJJ141	FLJ161		4-Bit Decade Counter
74C90	14	v14 (!)							7490	4-Bit Decade Counter
7491	14	v17	K134IR2		D191D	ИР2	FJJ151	FLJ221		8-Bit Shift Register, serial in, serial out, Gated Input
7492	14	v15 (!)	K155IE4			ИЕ4	FJJ251	FLJ171		Divide-by-12 Counter
7493	14	v14 (!)		K555IE5	DL093D	ИЕ5	FJJ211	FLJ181		4-Bit Binary Counter
74C93	14	v17								4-Bit Binary Counter
74L93	14	v22								4-Bit Binary Counter
7494	16	v17						FLJ231		4-Bit Shift Register, Dual asynchronous Presets
74C95	14	v18								4-Bit Shift Register
74L95	14	v15								4-Bit Shift Register
7495	14	v14		K555IR1	D195D	ИР1	FJJ231	FLJ191		4-Bit Shift Register (7495 and 74LS95)
7496	16	v18					FJJ241	FLJ261		5-bit parallel-in/parallel-out Shift Register, asynchronous Preset
7497	16	v17	K155IE8			ИЕ8		FLJ331		synchronous 6-bit binary rate Multiplier
7498	16	v15	K134IR5			ИР5				4-bit Data Selector/storage Register
7499	16	v24								4-bit bidirectional universal Shift Register
74100	24	v18						FLJ301		Dual 4-bit bistable Latch
74101	14	v13								AND-OR-gated J-K negative-edge-triggered Flip-Flop, Preset
74102	14	v13								AND-gated J-K negative-edge-triggered Flip-Flop, Preset and Clear
74103	14	v15								Dual J-K negative-edge-triggered Flip-Flop, Clear
74104	14	v15						FLJ281		J-K master-slave Flip-Flop
74105	14	v15						FLJ291		J-K master-slave Flip-Flop, J2 and K2 inverted
74106	16	v13								Dual J-K negative-edge-triggered Flip-Flop, Preset and Clear
74107	14	v24								Dual J-K Flip-Flop with Clear
74LS107	14	v14		K555TV6		ТВ6	FJJ261	FLJ271		Dual J-K negative-edge-triggered Flip-Flop with Clear (LS107, HC107)
74108	14	v13								Dual J-K negative-edge-triggered Flip-Flop, Preset, common Clear and common Clock
74109	16	v15	K155TV15			ТВ15				Dual J-Not-K Positive-Edge-triggered Flip-Flop with Clear and Preset
74110	14	v15						FLJ341		AND-gated J-K master-slave Flip-Flop, Data lockout
74111	16	v16						FLJ351		Dual J-K Flip-Flop with Preset and Clear
74112	16	v15		K555TV9	DL112D	ТВ9				Dual J-K Negative-Edge-triggered Flip-Flop with Clear and Preset
74113	14	v15	KR531TV10			ТВ10				Dual J-K Negative-Edge-triggered Flip-Flop with Preset
74114	14	v16	KR531TV11			ТВ11				Dual J-K Negative-Edge-triggered Flip-Flop with Preset
74115	14	v17						FLJ521		Dual J-K Flip-Flop Flip-Flop with Clear
74116	24	v15								Dual 4-bit Latch, Clear
74118	16	v15 (!)					FJJ291	FLJ361		Hex Set/Reset Latch, common reset
74119	24	v21					FJJ301	FLJ371		hex Set/Reset Latch
74120	14	v17						FLY181		Dual pulse synchronizer/Drivers
74121	14	v22	K155AG1		D121D	АГ1	FJK101	FLK101		monostable Multivibrator (adapter required)
74122	14	v22						FLK111		retriggerable monostable Multivibrator, Clear (adapter required)
74123	16	v22		K533AG3	DL123D	АГ3		FLK121		Dual retriggerable monostable Multivibrator, Clear (adapter required)
74124	16	n/a	KR531GG1			ГГ1				Dual voltage-controlled Oscillator
74125	14	v14	K155LP8	K555LP8		ЛП8				Quad Bus Buffer with Three-State Outputs
74126	14	v14		K555LP14		ЛП14				Quad Bus Buffer with Three-State Outputs
74128	14	v15	K155LE6			ЛЕ6				Quad 2-Input NOR Gate
74130	16	v22								Retriggerable monostable Multivibrator (adapter required)
74131	16	v16								Quad 2-Input AND Gate
74132	14	v14	KR531TL3		DL132D	ТЛ3		FLH601		Quad 2-Input NAND Gate
74133	16	v13								13-Input NAND Gate
74134	16	v13	KR531LA19			ЛА19				12-Input NAND
74135	16	v13								XOR/NOR Gate
74136	14	v15		K555LP12		ЛП12				Quad 2-Input Exclusive OR with Open Collector Outputs
74137	16	v15								3 to 8-line Decoder/Demultiplexer with Address Latch
74138	16	v14	KR531ID7	K555ID7		ИД7				3 to 8-line Decoder/Demultiplexer
74139	16	v14	KR531ID14			ИД14				Dual 2 to 4-line Decoder/Demultiplexer
74140	14	v13	KR531LA16			ЛА16				Dual 4-Input NAND Gate
74141	16	v14	K155ID1			ИД1	FJL151	FLL101		BCD to decimal Decoder/Driver for cold-cathode indicator / Nixie tube
74142	16	v22						FLL151		Decade Counter/Latch/decoder/Driver for Nixie tubes
74143	24	v18						FLL171		Decade Counter/Latch/decoder/7-segment Driver
74144	24	v18						FLL171T		Decade Counter/Latch/decoder/7-segment Driver
74145	16	v14		K555ID10		ИД10		FLL111T		BCD to decimal Decoder/Driver
74147	16	v14		K555IV3		ИВ3				10-Line to 4-Line Priority Encoder
74148	16	v14		K555IV1		ИВ1				8-Line to 3-Line Priority Encoder
74149	20	v13								8-line to 8-line priority encoder
74150	24	v14		K555KP1		КП1		FLY111		16-line to 1-line Data Selector/Multiplexer
74151	16	v14	KR531KP7	K555KP7		КП7		FLY121		8-line to 1-Line Data Selector/Multiplexer
74152	14	v13	K155KP5			КП5				8-line to 1-line Data Selector/Multiplexer, inverting Output
74153	16	v14	KR531KP2	K555KP2		КП2		FLY131		Dual 4-Line to 1-Line Data Selector/Multiplexer
74154	24	v14		K555ID3		ИД3	FJH341	FLY141		4-Line to 16-Line Decoder/Demultiplexer
74155	16	v14		K555ID4	DL155D	ИД4	FJH491	FLY151		Dual 2-Line to 4-Line Decoder/Demultiplexer
74156	16	v15		K555ID5		ИД5		FLY161		Dual 2-Line to 4-Line Decoder/Demultiplexer with Open Collector Outputs
74157	16	v14		K555KP16		КП16		FLY171		Quad 2-Line to 1-Line Data Selector/Multiplexer
74158	16	v15		K555KP18		КП18				Quad 2-Line to 1-Line Data Selector/Multiplexer
74159	24	v15								4-Line to 16-Line Decoder/Demultiplexer, Open Collector
74160	16	v19	K155IE9			ИЕ9		FLJ401		Synchronous 4-Bit Decade Counter with asynchronous Clear
74161	16	v19	KR531IE10	K555IE10		ИЕ10		FLJ411		Synchronous 4-Bit Binary Counter with asynchronous Clear
74162	16	v19		K555IE11		ИЕ11		FLJ421		Synchronous 4-Bit Decade Counter with synchronous Clear
74163	16	v19		K555IE18		ИЕ18		FLJ431		Synchronous 4-Bit Binary Counter with synchronous Clear
74164	14	v14		K555IR8	DL164D	ИР8		FLJ441		8-Bit Parallel-Out Serial Shift Register with asynchronous Clear
74165	16	v15		K555IR9		ИР9		FLJ451		8-Bit Serial Shift Register
74166	16	v14		K555IR10		ИР10		FLJ461		8-Bit Shift Register Register
74167	16	v22						FLJ471		Synchronous decade rate Multiplier
74168	16	v16	KR531IE16			ИЕ16				Synchronous Presettable 4-bit up/down decade Counter
74169	16	v16	KR531IE17			ИЕ17				Synchronous Presettable 4-bit up/down binary Counter
74170	16	RAM	K155RP1	K555IR32		ИР32	FJQ101	FLQ131		4 by 4 Register File with Open Collector Outputs
74171	16	v18								Quad D Flip-Flops with Clear
74172	24	v18								16-bit multiple Port Register file (8x2), comprehesive test using Port 2 by SRAM testing
74173	16	v14	K155IR15	K555IR15		ИР15				Quad D Flip-Flop with Three-State Outputs
74174	16	v14	KR531TM9	K555TM9		ТМ9		FLJ531		Hex D Flip-Flop with Common Clear
74175	16	v14	KR531TM8	K555TM8	DL175D	ТМ8		FLJ541		Quad D Edge-triggered Flip-Flop with Complementary Outputs and asynchronous Clear
74176	14	v16								Presettable decade (bi-quinary) Counter/Latch
74177	14	v16								Presettable binary Counter/Latch
74178	14	v17								4-bit parallel-access Shift Register
74179	16	v17								4-bit parallel-access Shift Register, asynchronous Clear Input, complementary Qd Output
74180	14	v14		K555IP2		ИП2	FJH281	FLH421		9-bit odd/even parity bit generator and checker

Identifier	Pins	Status	UdSSR	UdSSR	DDR	Cyrillic	Philips	Siemens	Setting	Remark
74181	24	v13	KR531IP3	K555IP3		ИП3	FJH451	FLH401		4-Bit Arithmetic Logic Unit and Function Generator
74182	16	v13	K155IP4			ИП4		FLH411		Lookahead Carry Generator
74183	14	v15		K555IM5		ИМ5		FLH451		Dual carry-save Full Adder
74184	16	v16	K155PR6			ПР6		FLH561		BCD to binary converter
74185	16	v15	K155PR7			ПР7		FLH571		6-bit binary to BCD converter (->check also with 7488 ROM)
74186	24	ROM								512-bit ROM (64x8)
74187	16	ROM						FLR111		1024-bit ROM (256x4)
74188	16	ROM						FLR121		256-bit PROM (32x8)
74189	16	RAM	KR531RU8			РУ8				64-bit RAM (16x4), inverting Outputs
74190	16	v14		K555IE12		ИЕ12		FLJ201		Synchronous Up/Down Decade Counter
74191	16	v15		K555IE13		ИЕ13		FLJ211		Synchronous Up/Down Binary Counter
74192	16	v14		K555IE6	DL192D	ИЕ6		FLJ241		Synchronous Up/Down Decade Counter with Clear
74193	16	v15		K555IE7	DL193D	ИЕ7	FJJ411	FLJ251		Synchronous Up/Down Binary Counter with Clear
74194	16	v14	KR531IR11	K555IR11A	DL194D	ИР11		FLJ551		4-Bit Bidirectional Universal Shift Register
74195	16	v14	KR531IR12			ИР12		FLJ561		4-Bit Parallel-Access Shift Register
74196	14	v15	KR531IE14	K555IE14		ИЕ14		FLJ381		4-bit decade Counter
74197	14	v16	KR531IE15	K555IE15		ИЕ15		FLJ391		4-bit binary Counter
74198	24	v18	K155IR13			ИР13		FLJ311		8-bit bidirectional universal Shift Register
74199	24	v18						FLJ321		8-bit universal Shift Register, J-Not-K serial Inputs
74200	16	RAM						FLQ141		256-bit RAM (256x1)
74201	16	RAM								256-bit RAM (256x1)
74207	16	RAM								1024-bit RAM (256x4)
74208	20	RAM								1024-bit RAM (256x4), separate Data In- and Outputs
74209	16	RAM								1024-bit RAM (1024x1)
74211	20	RAM								144-bit RAM (16x9) with Output Latch
74212	20	RAM								144-bit RAM (16x9)
74213	20	RAM								192-bit RAM (16x12)
74214	16	RAM								1024-bit RAM (1024x1)
74215	16	RAM								1024-bit RAM (1024x1) with power-down mode
74216	16	RAM								1024-bit RAM (1024x1) with power-down mode
74217	20	RAM								1024-bit RAM (1024x1) with power-down mode
74218	20	RAM								1024-bit RAM (1024x1) with power-down mode
74219	20	FIFO								64-bit RAM (16x4), non-inverting Outputs
74221	16	v22	K555AG4	K555AG4		АГ4				Dual monostable Multivibrator (adapter required)
74222	20	FIFO								64-bit FIFO memory (16x4), synchronous, Input/Output ready enable
74224	16	FIFO								64-bit FIFO memory (16x4), synchronous
74225	16	FIFO	KR531RU10			РУ10				80-bit FIFO memory (16x5), asynchronous
74226	16	TBD								4-bit parallel latched Bus Transceiver
74227	20	FIFO								64-bit FIFO memory (16x4), synchronous, Input/Output ready enable
74228	16	FIFO								64-bit FIFO memory (16x4), synchronous
74229	20	FIFO								80-bit FIFO memory (16x5), asynchronous
74230	20	v19								Dual 4-bit Buffer/Driver, one inverted, one non-inverted; negative enable
74231	20	v19								Dual 4-bit Buffer/Driver, both inverted; one positive and one negative enable
74232	16	FIFO								64-bit FIFO memory (16x4), asynchronous
74233	20	FIFO								80-bit FIFO memory (16x5), asynchronous
74234	16	FIFO								256-bit FIFO memory (64x4), asynchronous
74235	20	FIFO								320-bit FIFO memory (64x5), asynchronous
74236	16	FIFO								256-bit FIFO memory (64x4), asynchronous
74237	16	v13								3-of-8 Decoder/Demultiplexer with Address Latch
74238	16	v15								3-of-8 Decoder/Demultiplexer
74240	20	v15	KR531AP3	K555AP3		АП3				Octal Buffer, inverting Outputs
74241	20	v15	KR531AP4	K555AP4		АП4				Octal Buffer, non-inverting Outputs
74242	14	v13		K555IP6		ИП6				Quad Bus Transceiver with inverted Three-State Outputs
74243	14	v15		K555IP7		ИП7				Quad Bus Transceiver with non-inverted Three-State Outputs
74244	20	v14		K555AP5		АП5				Octal Buffer with non-inverted Three-State Outputs
74245	20	v14		K555AP6		АП6				Octal Bus Transceiver with non-inverted Three-State Outputs
74246	16	v15								BCD to 7-segment Decoder/Driver
74247	16	v15		K555ID18	D347D	ИД18				BCD to 7-segment Decoder/Driver
74248	16	v15			D348D					BCD to 7-segment Decoder/Driver
74249	16	v15								BCD to 7-segment Decoder/Driver
74251	16	v14	KR531KP15	K555KP15	DL251D	КП15				8-line to 1-line Data Selector/Multiplexer with Three-State Outputs
74253	16	v15	KR531KP12	K555KP12	DL253D	КП12				Dual 4-line to 1-line Data Selector/Multiplexer with Three-State Outputs
74256	16	v18								Dual 4-bit addressable Latch
74257	16	v14	KR531KP11	K555KP11	DL257D	КП11				Quad 2-line to 1-line Data Selector/Multiplexer with non-inverted Three-Outputs (= MOS 7708)
74258	16	v15	KR531KP14	K555KP14		КП14				Quad 2-line to 1-line Data Selector/Multiplexer with inverted Three-State Outputs (= MOS 7709)
74259	16	v14		K555IR30	DL259D	ИР30				8-Bit addressable Latch
74260	14	v15	KR531LE7			ЛЕ7				Dual 5-Input NOR Gate
74261	16	v17		K555IP8		ИП8				2-bit by 4-bit parallel binary Multiplier
74262	20	ROM								5760-bit ROM (Teletext character set, 128 characters 5x9)
74265	16	v16								Quad complementary Output Elements
74266	14	v13		K555LP13		ЛП13				Quad 2-Input Exclusive NOR Gate with Open Collector Outputs
74269	24	v18								8-bit bidirectional binary Counter
74270	16	ROM								2048-bit ROM (512x4)
74271	20	ROM								2048-bit ROM (256x8)
74273	20	v15		K555IR35		ИР35				8-bit Register, asynchronous Clear
74274	20	TBD								4-bit by 4-bit binary Multiplier
74275	16	v22								7-bit slice Wallace tree
74276	20	v18								Quad J-Not-K edge-triggered Flip-Flops, separate Clocks, common Preset and Clear
74278	14	v13								4-bit cascadeable priority Registers, latched Data Inputs
74279	16	v16		K555TR2		ТР2				Quad set-reset Latch
74280	14	v14	KR531IP5	K555IP5		ИП5				9-Bit Odd/Even Parity Generator/Checker
74281	24	TBD								4-bit parallel binary Accumulator
74283	16	v15		K555IM6		ИМ6				4-Bit Binary Full Adder
74284	16	v16								4-bit by 4-bit parallel binary Multiplier (low order 4 bits of product)
74285	16	v16								4-bit by 4-bit parallel binary Multiplier (high order 4 bits of product)
74286	14	v17								9-bit parity generator/checker, bus Driver parity I/O Port
74287	16	ROM								1024-bit PROM (256x4)
74288	16	ROM								256-bit PROM (32x8)
74289	16	RAM	KR531RU9			РУ9				64-bit RAM (16x4), inverted Outputs
74290	14	v16	K555IE20			ИЕ20				decade Counter (separate divide-by-2 and divide-by-5 sections)
74292	16	v16								Programmable Frequency Divider/Digital Timer
74293	14	v13								4-Bit Binary Counter
74294	16	v13								Programmable Frequency Divider/Digital Timer
74295	14	v16		K555IR16	DL295D	ИР16				4-bit bidirectional Shift Register
74297	16	n/a								digital phase-locked loop filter
74298	16	v13		K555KP13		КП13				Quad 2-Input Multiplexer with Storage
74299	20	v14	KR531IR24		DI299D	ИР24				8-Bit Bidirectional Universal Shift/Storage Register with Three-State Outputs
74300	16	RAM								256-bit RAM (256x1)
74301	16	RAM								256-bit RAM (256x1)
74309	16	RAM								1024-bit RAM (1024x1)
74311	20	RAM								144-bit RAM (16x9) with Output Latch
74312	20	RAM								144-bit RAM (16x9)
74313	20	RAM								192-bit RAM (16x12)
74314	16	RAM								1024-bit RAM (1024x1)

Identifier	Pins	Status	UdSSR	UdSSR	DDR	Cyrillic	Philips	Siemens	Setting	Remark
74315	16	RAM								1024-bit RAM (1024x1) with power-down mode
74316	16	RAM								1024-bit RAM (1024x1) with power-down mode
74317	20	RAM								1024-bit RAM (1024x1) with power-down mode
74318	20	RAM								1024-bit RAM (1024x1) with power-down mode
74320	16	n/a								crystal-controlled Oscillator
74321	16	n/a								crystal-controlled Oscillators, F/2 and F/4 count-down Outputs
74322	20	v17		K555IR28		ИР28				8-Bit Shift Register, Sign Extend
74323	20	v16	KR531IR29			ИР29				8-bit Bidirectional Universal Shift/Storage Register, synchronous Clear
74324	14	n/a								voltage-controlled Oscillator (or crystal controlled), enable Input, complementary Outputs
74325	16	n/a								Dual voltage-controlled Oscillator (or crystal controlled), complementary Outputs
74326	16	n/a								Dual voltage-controlled Oscillator (or crystal controlled), enable Input, complementary Outputs
74327	14	n/a								Dual voltage-controlled Oscillator (or crystal controlled)
74333	24	n/a								PLA (12 Inputs, 32 terms, 6 Outputs), 4-bit state Registers, Three-State Outputs, sequential logic
74334	24	n/a								PLA (12 Inputs, 32 terms, 6 Outputs), Three-State Outputs, combinatorial logic
74335	24	n/a								PLA (12 Inputs, 32 terms, 6 Outputs), 4-bit state Registers, Open-Collectors Outputs, sequential logic
74336	24	n/a								PLA (12 Inputs, 32 terms, 6 Outputs) Open-Collectors Outputs, combinatorial logic
74347	16	v16								BCD-to-7 segment Decoders/Drivers, low voltage version of 7447
74348	16	v13		K555IV2		ИВ2				8 to 3-line priority encoder
74350	16	v18		KP1531IR42		ИР42				4-bit Shifter
74351	20	v18								Dual 8-line to 1-line Data Selectors/Multiplexers, 4 common Data Inputs
74352	16	v16		K555KP19		КП19				Dual 4-line to 1-line Data Selectors/Multiplexers, inverting Outputs
74353	16	v21		K555KP17		КП17				Dual 4-line to 1-line Data Selectors/Multiplexers, inverting Outputs
74354	20	v13								8-line to 1-line Data Selector/Multiplexer, transparent Registers
74355	20	v13								8-line to 1-line Data Selector/Multiplexer, transparent Registers
74356	20	v13								8-line to 1-line Data Selector/Multiplexer, edge-triggered Registers
74357	20	v19								8-line to 1-line Data Selector/Multiplexer, edge-triggered Registers
74361	22	n/a								Bubble memory function timing generator
74363	20	v13								Octal transparent Latch
74364	20	v13								Octal edge-triggered D-Type Register
74365	16	v15	K155LP10			ЛП10				Hex Buffer with non-inverted Three-State Outputs
74366	16	v15	K155LN6			ЛН6				Hex Buffer with inverted Three-State Outputs
74367	16	v14	K155LP11			ЛП11				Hex Buffer with non-inverted Three-State Outputs
74368	16	v15		K555LN7		ЛН7				Hex Buffer with inverted Three-State Outputs
74370	16	ROM								2048-bit ROM (512x4)
74371	20	ROM								2048-bit ROM (256x8)
74373	20	v14	KR531IR22	K555IR22		ИР22				Octal Register with Three-State Outputs
74374	20	v14	KR531IR23	K555IR23	DL374D	ИР23				Octal Register with Three-State Outputs
74375	16	v15		K555TM10		ТМ10				Quad Bistable Latch
74376	16	v16								Quad J-Not-K Flip-Flop, common Clock and common Clear
74377	20	v14		K555IR27		ИР27				8-Bit Register with Clock Enable
74378	16	v16								6-Bit Register with Clock Enable
74379	16	v16								4-bit Register, Clock enable and complementary Outputs
74381	20	v18	KR531IK2			ИК2				4-bit arithmetic Logic Unit/Function Generator, generate and propagate Outputs
74382	20	v18								4-bit arithmetic Logic Unit/Function Generator, ripple carry and overflow Outputs
74384	16	v22		K555IP9		ИП9				8-bit by 1-bit two's complement Multipliers
74385	20	v18								Quad serial Adder/Subtractor
74386	14	v13								Quad 2-Input Exclusive-OR Gate
74387	16	ROM								1024-bit PROM (256x4)
74388	16	v24								4-bit D-Type Register
74390	16	v15								Dual 4-Bit Decade Counter
74393	14	v15		K555IE19		ИЕ19				Dual 4-Bit Binary Counter
74395	16	v16		K555IR25		ИР25				4-bit cascadable Shift Register
74396	16	v16		K555IR43		ИР43				Octal storage Registers, parallel access
74398	20	v16								Quad 2-Input Multiplexers, storage and complementary Outputs
74399	16	v15		K555KP20		КП20				Quad 2-Input Multiplexer, storage
74S400	18	RAM								4096-bit SRAM (4k x 1), Open Collector Outputs
74F401	14	TBD								Cyclic Redundancy Check Generator/Checker
74S401	18	RAM								4096-bit SRAM (4k x 1), Three-State Outputs
74F402	16	TBD								Expandable Cyclic Redundancy Check Generator/Checker
74S405	16	v18								1 out of 8 binary Decoder (equivalent to i8205)
74S408	48	n/a								64K Dynamic RAM Controller
74S409	48	n/a								256K Dynamic RAM Controller
74412	24	v17								Multi-mode Buffered 8-bit Latches (equivalent to Intel 3212/8212)
74413	16	FIFO								256-bit FIFO memory (64x4)
74416	16	TBD								Programmable Modulo-N Decade Counter
74F416	28	TBD								16-Bit Memory Error Detection and Correction Circuit (EDAC)
74S416	16	v19								4-bit Data bus sender/receiver
74422	14	v22								retriggerable monostable Multivibrators, two Inputs (adapter required)
74423	16	v22								Dual retriggerable monostable Multivibrator (adapter required)
74425	14	v16								Quad Bus Buffer with Three-State Outputs
74426	14	v16								Quad Bus Buffer with Three-State Outputs
74428	28	n/a								System Controller for Intel 8080A (equivalent to Intel 8228)
74429	28	n/a								FIFO RAM Controller
74F430	28	TBD								Cyclic Redundancy Checker/Corrector
74F432	24	v18								Multi-mode Buffered 8-bit Latches, inverted Outputs
74436	16	v18								Line Driver/memory Driver circuits - MOS memory interface, damping Output resistor
74437	16	v18								Line Driver/memory Driver circuits - MOS memory interface
74438	28	n/a								System Controller for Intel 8080A (equivalent to Intel 8238)
74440	20	v17								Quad tridirectional Bus Transceiver, non-inverting Outputs
74441	20	v17								Quad tridirectional Bus Transceiver, inverting Outputs
74442	20	v17								Quad tridirectional Bus Transceiver, non-inverting Outputs
74443	20	v17								Quad tridirectional Bus Transceiver, inverting Outputs
74444	20	v17								Quad tridirectional Bus Transceiver, inverting and non-inverting Outputs
74445	16	v13								BCD-to-decimal Decoders/Drivers
74446	16	v18								Quad Bus Transceivers, direction controls, inverting Outputs
74447	16	v16								BCD to 7-segment Decoder/Driver
74448	20	v17								Quad tridirectional Bus Transceiver, inverting and non-inverting Outputs
74449	16	v18								Quad Bus Transceivers, direction controls, non-inverting Outputs
74S450	16	ROM								8192-bit PROM (1024x8) with power-down
74S451	16	ROM								8192-bit PROM (1024x8) with power-down
74452	16	TBD								Dual Decade Counter (=MCC4052, MCC4352)
74453	16	TBD								Dual Hexadecimal Counter (=MCC4053, MCC4353)
74454	24	TBD								Dual Decade Up/Down Counter
74456	16	v18								4-bit NBCD Full Adder
74461	24	v17								8-Bit Presettable Binary Counter
74462	20	n/a								fiber-optic Data-link transmitter
74463	20	n/a								fiber-optic Data-link receiver
74465	20	v17	KR1533AP14	K555AP14		АП14				Octal Buffer, non-inverting Outputs
74466	20	v13	KR1533AP15	K555AP15		АП15				Octal Buffers, inverting Outputs
74467	20	v13								Octal Buffers, non-inverting Outputs
74468	20	v13								Octal Buffers, inverting Outputs
74470	16	ROM								2048-bit PROM (256x8)
74471	20	ROM								2048-bit PROM (256x8)
74472	20	ROM								4096-bit PROM (512x8)

Identifier	Pins	Status	UdSSR	UdSSR	DDR	Cyrillic	Philips	Siemens	Setting	Remark
74473	20	ROM								4096-bit PROM (512x8)
74474	24	ROM								4096-bit PROM (512x8)
74475	24	ROM								4096-bit PROM (512x8)
74476	18	ROM								4096-bit PROM (1024x4)
74477	18	ROM								4096-bit PROM (1024x4)
74478	16	ROM								8192-bit PROM (1024x8) with power-down
74479	16	ROM								8192-bit PROM (1024x8) with power-down
74481	48	n/a								4-bit slice cascadable processor Elements
74482	48	n/a								4-bit slice expandable control Elements
74484	20	TBD								BCD-to-binary converter (mask programmed SN74S371 ROM)
74485	20	TBD								Binary-to-BCD converter (mask programmed SN74S371 ROM)
74490	16	v13								Dual decade Counter
74491	24	TBD								10-bit binary up/down Counter, limited Preset
74498	24	TBD								8-bit bidirectional Shift Register, parallel Inputs
74518	20	v13								8-bit comparator, Open Collector
74519	20	v13								8-bit comparator, Open Collector
74520	20	v13								8-bit comparator, inverting Output
74521	20	v15		KR1531SP2		СП2				8-bit comparator, inverting Output
74522	20	v13								8-bit comparator, inverting Output, Open Collector
74524	20	v22								8-bit registered comparator
74526	20	v18								Fuse programmable Identity Comparator, 16-bit (assumes that all fuses are unset)
74527	20	v18								Fuse programmable Identity Comparator, 8-bit + 4-bit conventional Identity comparator (assumes that all fus
74528	16	v18								Fuse programmable Identity comparator, 12-bit (assumes that all fuses are unset)
74533	20	v15		K555IR40		ИР40				Octal transparent Latch, inverting Outputs
74534	20	v15		KR1531IR41		ИР41				Octal Register, inverting Outputs
74537	20	v18		K555ID22		ИД22				1 of 10 Decoder / BCD to decimal Decoder
74538	20	v18								1 of 8 Decoder with 3-state Outputs
74539	20	v18								Dual 2-line to 4-line Decoder/Demultiplexer
74540	20	v15			DL540D					Octal Buffers and Line Drivers
74541	20	v15			DL541D					Octal Buffers and Line Drivers
74543	24	v17								Octal registered Transceiver, non-inverting
74544	24	v17								Octal registered Transceiver, inverting
74545	20	v18								Octal bidirectional transceiver, non-inverting
74546	24	v22								8-bit bidirectional registered transceiver, non-inverting
74547	24	v22								8-bit bidirectional latched transceiver, non-inverting
74F547	20	v22								3 to 8 Decoder/Multiplexer
74F548	20	v22								3 to 8 Decoder/Multiplexer
74550	28	TBD								Octal registered Transceiver with Status Flags, non-inverting
74551	28	TBD								Octal registered Transceiver with Status Flags, inverting
74557	40	n/a								8-bit by 8-bit Multiplier
74558	40	n/a								8-bit by 8-bit Multiplier
74560	20	v18								4-bit decade Counter
74561	20	v18								4-bit binary Counter
74563	20	v15								8-bit D-Type transparent Latch, inverting Outputs
74564	20	v15								8-bit D-Type edge-triggered Register, inverting Outputs
74566	24	v22								8-bit bidirectional registered transceiver, inverting
74567	24	v22								8-bit bidirectional latched transceiver, inverting
74568	20	v17								4-bit Decade Up/Down Counter
74569	20	v17								4-bit Binary Up/Down Counter
74570	16	ROM								2048-bit PROM (512x4)
74571	20	ROM								2048-bit PROM (512x4)
74572	18	ROM								4096-bit PROM (1024x4)
74S573	18	ROM								4096-bit PROM (1024x4)
74LS573	20	v15		K555IR33		ИР33				Octal D Latch with Tri-State Outputs (all, except 74S573)
74574	20	v15		K555IR37		ИР37				Octal D-Type edge-triggered Flip-Flop
74575	20	v17								Octal D-Type edge-triggered Flip-Flop, synchronous Clear
74576	20	v16								Octal D-Type edge-triggered Flip-Flop, inverting Outputs
74577	24	v18								Octal D-Type edge-triggered Flip-Flop, synchronous Clear, inverting Outputs
74579	20	v18								8-bit bidirectional binary Counter
74580	20	v16								Octal D-Type transparent Latch, inverting Outputs
74582	24	TBD								4-bit BCD arithmetic Logic Unit
74583	16	v18								4-bit BCD Adder
74588	20	v18								Octal bidirectional transceiver, non-inverting
74589	16	v13								8-Bit Shift Register with Input Latch with Three-State Outputs
74590	16	v16								8-bit binary Counter, Output Registers
74591	16	v18								8-bit binary Counter, Output Registers
74592	16	v18								8-bit binary Counter, Input Registers
74593	16	v18								8-bit binary Counter, Input Registers
74594	16	v18								8-bit Shift Registers, Serial-In, Parallel-Out, Output Latches
74595	16	v19		K555IR52		ИР52				8-bit Shift Registers, Serial-In, Parallel-Out, Output Latches, Output enable
74596	16	v19								8-bit Shift Registers, Serial-In, Parallel-Out, Output Latches, Output enable
74597	16	v13								Serial-out Shift Register with Input Latches
74598	20	v22								8-bit Shift Register, Selectable Parallel-In/Out Input Latches
74599	16	v18								8-bit Shift Registers, Serial-In, Parallel-Out, Output Latches
74600	20	n/a								Dynamic memory refresh Controller, transparent and burst Modes, for 4K or 16K DRAM
74601	20	n/a								Dynamic memory refresh Controller, transparent and burst Modes, for 64K DRAM
74602	20	n/a								Dynamic memory refresh Controller, cycle steal and burst Modes, for 4K or 16K DRAM
74603	20	n/a								Dynamic memory refresh Controller, cycle steal and burst Modes, for 64K DRAM
74604	28	v18								Octal 2-Input Multiplexer, Latch, high-speed, Three-State
74605	28	v18								Octal 2-Input Multiplexer, Latch, high-speed, Open Collector
74606	28	v18								Octal 2-Input Multiplexer, Latch, glitch-free, Three-State
74607	28	v18								Octal 2-Input Multiplexer, Latch, glitch-free, Open Collector
74608	16	n/a								Memory cycle Controller
74610	40	n/a								PC/AT Memory Mapper, Three-State, latched
74611	40	n/a								PC/AT Memory Mapper, Open-Collector, latched
74612	40	n/a								PC/AT Memory Mapper, Three-State
74613	40	n/a								PC/AT Memory Mapper, Open-Collector
74620	20	v13		K555AP26		АП26				Octal Bus Transceiver, inverting, Three-State Outputs
74621	20	v13								Octal Bus Transceiver, non-inverting, Open Collector
74622	20	v13								Octal Bus Transceiver, inverting
74623	20	v13								Octal Bus Transceiver, non-inverting, Three-State Outputs
74624	14	n/a								voltage-controlled Oscillator, enable control, range control, two-phase Outputs
74625	16	n/a								Dual voltage-controlled Oscillator, two-phase Outputs
74626	16	n/a								Dual voltage-controlled Oscillator, enable control, two-phase Outputs
74627	14	n/a								Dual voltage-controlled Oscillator
74628	14	n/a								voltage-controlled Oscillator, enable control, range control,
74629	16	n/a								Dual voltage-controlled Oscillator, enable control, range control
74638	20	v15								Octal Bus Transceiver, inverting Outputs
74639	20	v15								Octal Bus Transceiver, non-inverting Outputs
74640	20	v15		K555AP9		АП9				Octal Bus Transceiver, inverting Outputs
74641	20	v15								Octal Bus Transceiver, non-inverting Outputs
74642	20	v15								Octal Bus Transceiver, inverting Outputs
74643	20	v15		K555AP16		АП16				Octal Bus Transceiver, mix of inverting and non-inverting Outputs
74644	20	v15								Octal Bus Transceiver, mix of inverting and non-inverting Outputs

Identifier	Pins	Status	UdSSR	UdSSR	DDR	Cyrillic	Philips	Siemens	Setting	Remark
74645	20	v13								Octal Bus Transceiver, non-inverting Outputs
74646	24	v19		K555AP10		АП10				Octal Bus Transceiver/Latch/Multiplexer, non-inverting Outputs, Three-State
74647	24	v19								Octal Bus Transceiver/Latch/Multiplexer, non-inverting Outputs, Open-Collector
74648	24	v19								Octal Bus Transceiver/Latch/Multiplexer, inverting Outputs, Three-State
74649	24	v19								Octal Bus Transceiver/Latch/Multiplexer, inverting Outputs, Open-Collector
74651	24	v17		K555AP17		АП17				Octal Bus Transceiver/register, inverting Outputs
74652	24	v17		K555AP24		АП24				Octal Bus Transceiver/register, non-inverting Outputs
74653	24	v17								Octal Bus Transceiver/register, inverting Outputs
74654	24	v17								Octal Bus Transceiver/register, non-inverting Outputs
74657	24	v22								Octal bidirectional transceiver with 8-bit parity generator/checker
74666	24	v17								8-bit D-Type transparent read-back Latch, non-inverting
74667	24	v17								8-bit D-Type transparent read-back Latch, inverting
74668	16	v17								Synchronous 4-bit decade up/down Counter
74669	16	v17								Synchronous 4-bit binary up/down Counter
74670	16	RAM		K555IR26		ИР26				4 by 4 Register File with Three-State Outputs
74671	20	v22								4-bit bidirectional Shift Register/Latch/Multiplexer, direct Clear
74672	20	v22								4-bit bidirectional Shift Register/Latch/Multiplexer, synchronous Clear
74673	24	v17								16-bit serial-in, serial/parallel-out Shift Register, Output storage Registers
74674	24	v21								16-bit serial-in, serial/parallel-out Shift Register, Output storage Registers
74675	24	TBD								16-bit serial-in, serial/parallel-out Shift Register
74677	24	v18								16-bit Address Comparator, Enable
74678	24	v19								16-bit Address Comparator, Latch
74679	20	v18								12-bit Address Comparator, Enable
74680	20	v18								12-bit Address Comparator, Latch
74681	20	v22								4-bit parallel binary Accumulator
74682	20	v16								8-bit Magnitude Comparator, P>Q Output
74683	20	v16								8-bit Magnitude Comparator, P>Q Output, Open Collector
74684	20	v16								8-bit Magnitude Comparator, P>Q Output
74685	20	v16								8-bit Magnitude Comparator, P>Q Output, Open Collector
74686	24	v18								8-bit Magnitude Comparator, P>Q Output, Enable
74687	24	v18								8-bit Magnitude Comparator, P>Q Output, Enable
74688	20	v15								8-bit Magnitude Comparator, Enable
74689	20	v13								8-bit Magnitude Comparator, Enable, Open Collector
74690	20	v18								4-bit decimal Counter/Latch/Multiplexer, asynchronous Clear
74691	20	v18								4-bit binary Counter/Latch/Multiplexer, asynchronous Clear
74692	20	v18								4-bit decimal Counter/Latch/Multiplexer, synchronous Clear
74693	20	v18								4-bit binary Counter/Latch/Multiplexer, synchronous Clear
74696	20	v21								4-bit decimal up/down Counter/register/Multiplexer, asynchronous Clear
74697	20	v21								4-bit binary up/down Counter/register/Multiplexer, asynchronous Clear
74698	20	v21								4-bit decimal up/down Counter/register/Multiplexer, synchronous Clear
74699	20	v21								4-bit binary up/down Counter/register/Multiplexer, synchronous Clear
74740	20	v15								Octal Buffer, inverting Outputs
74741	20	v13								Octal Buffer, non-inverting Outputs
74744	20	v15								Octal Buffer with non-inverted Three-State Outputs
74748	16	v14								8 to 3-line priority encoder (glitch-less)
74756	20	v15								Octal Buffer, inverting Outputs, Open Collector and Schmidt Trigger
74757	20	v13								Octal Buffer, non-inverting Outputs, Open Collector and Schmidt Trigger
74758	20	v18								Quad Bus Transceivers, inverting Outputs, Open-Collector
74759	20	v18								Quad Bus Transceivers, non-inverting Outputs, Open-Collector
74760	20	v15								Octal Buffer/line Driver, non-inverting Outputs
74762	20	v18								Octal Buffer/line Driver, inverting and non-inverting Outputs
74763	20	v18								Octal Buffer/line Driver, inverting Outputs, complementary enable Inputs
74779	16	TBD								8-bit bidirectional binary Counter
74783	40	n/a								synchronous address Multiplexer for display systems (= MC6883)
74793	20	v19								8-bit Latch, read-back
74794	20	v19								8-bit Register, read-back
74795	20	v16								Octal Buffer, non-inverting, common enable
74796	20	v16								Octal Buffer, inverting, common enable
74797	20	v16								Octal Buffer, non-inverting, enable for 4 Buffers each
74798	20	v16								Octal Buffer, inverting, enable for 4 Buffers each
74800	20	v19								Triple 4-Input AND/NAND Drivers
74802	20	v19								Triple 4-Input OR/NOR Drivers
74804	20	v15								Hex 2-Input NAND Drivers
74805	20	v15								Hex 2-Input NOR Drivers
74808	20	v17								Hex 2-Input AND Drivers
74810	14	v17								Quad 2-Input XNOR Gates
74821	24	v13								10-bit bus interface Flip-Flop
74822	24	v19								10-bit bus interface Flip-Flop, inverting Inputs
74823	24	v13								9-bit D-Type Flip-Flops, Clear and Clock enable Inputs
74824	24	v13								9-bit D-Type Flip-Flops, Clear and Clock enable Inputs, inverting Inputs
74825	24	v13								8-bit D-Type Flip-Flop, Clear and Clock enable Inputs
74826	24	v19								8-bit D-Type Flip-Flop, Clear and Clock enable Inputs, inverting Inputs
74827	24	v24								10-bit Buffer, non-inverting
74828	24	v24								10-bit Buffer, inverting
74832	20	v17								Hex 2-Input OR Drivers
74839	24	n/a								Field-programmable logic array 14x32x6
74840	24	n/a								Field-programmable logic array 14x32x6
74841	24	v19								10-bit D-Type Flip-Flop
74842	24	v19								10-bit D-Type Flip-Flop, inverting Inputs
74843	24	v18								9-bit D Flip-Flops, Clear and set Inputs
74844	24	v18								9-bit D Flip-Flops, Clear and set Inputs, inverting Inputs
74845	24	v19								8-bit D Flip-Flops, Clear and set Inputs
74846	24	v19								8-bit D Flip-Flops, Clear and set Inputs, inverting Inputs
74848	16	v18								8 to 3-line priority encoder (glitch-less)
74850	28	v19								1 of 16 Data Selector/Multiplexer, Clocked select
74851	28	v19								1 of 16 Data Selector/Multiplexer
74852	24	TBD								8-bit universal transceiver Port Controller
74856	28	TBD								8-bit universal transceiver Port Controller
74857	24	v13								Hex 2-line to 1-line Multiplexer
74861	24	v24								10-bit Bus Transceiver, non-inverting
74862	24	v24								10-bit Bus Transceiver, inverting
74863	24	v24								9-bit Bus Transceiver, non-inverting
74864	24	v24								9-bit Bus Transceiver, inverting
74866	28	v24								8-bit Magnitude Comparator with Latches
74867	24	v22								synchronous 8-bit up/down Counter, asynchronous Clear
74869	24	v18								synchronous 8-bit up/down Counter, synchronous Clear
74870	24	RAM								Dual 16x4 Register files (16 x 4)
74871	24	RAM								Dual 16x4 Register files (16 x 4)
74873	24	v18	KR531IR34			ИР34				Dual 4-bit transparent Latch with Clear
74874	24	v18	KR531IR38			ИР38				Dual 4-bit edge-triggered D Flip-Flops with Clear
74876	24	v18								Dual 4-bit edge-triggered D Flip-Flops with Clear, inverting Outputs
74877	24	TBD								8-bit universal transceiver Port Controller
74878	24	v18								Dual 4-bit D-Type Flip-Flop, synchronous Clear, non-inverting Outputs
74879	24	v18								Dual 4-bit D-Type Flip-Flop, synchronous Clear, inverting Outputs

Identifier	Pins	Status	UdSSR	UdSSR	DDR	Cyrillic	Philips	Siemens	Setting	Remark
74880	24	v18								Dual 4-bit transparent Latch with Clear, inverting Outputs
74881	24	v24								4-bit arithmetic Logic Unit
74882	24	TBD								32-bit lookahead carry generator
74885	24	v18								8-bit Magnitude Comparator
74ALS900	14	v17								Quad 2-Input NAND Gate
74C901	14	v13								Hex inverting CMOS to TTL Buffer
74ALS902	14	v17								Quad 2-Input NOR Gate
74C902	14	v13								Hex non-inverting CMOS to TTL Buffer
74ALS903	14	v17								Quad 2-Input NAND Gate with Open Collector Outputs
74C903	14	v18								Hex inverting TTL to CMOS Buffer
74C904	14	v18								Hex non-inverting TTL to CMOS Buffer
74C906	14	v13								Hex inverting NMOS Buffer
74C907	14	n/a								Hex inverting PMOS Buffer
74C910	18	RAM								RAM 64 x 4 bit
74C914	14	v18								Hex Schmitt Trigger Inverter
74C915	18	v18								7-segment to BCD
74C920	22	RAM								RAM 256 x 4 bit
74C921	18	RAM								RAM 256 x 4 bit
74C929	16	RAM								RAM 1024 x 1 bit
74C930	16	RAM								RAM 1024 x 1 bit
74S940	20	v18								Octal Buffer, inverting Outputs
74S941	20	v18								Octal Buffer, non-inverting Outputs
74942	20	n/a								300 Baud Bell 103 modem (+/- 5 V supply)
74943	20	n/a								300 Baud Bell 103 modem (single 5 V supply)
74C989	16	RAM								RAM 16 x 4 bit
74990	20	v18								8-bit D-Type transparent read-back Latch, non-inverting
74991	20	v19								8-bit D-Type transparent read-back Latch, inverting
74992	24	v19								9-bit D-Type transparent read-back Latch, non-inverting
74993	24	v19								9-bit D-Type transparent read-back Latch, inverting
74994	24	v18								10-bit D-Type transparent read-back Latch, non-inverting
74995	24	v23								10-bit D-Type transparent read-back Latch, inverting
74996	24	v22								8-bit D-Type edge-triggered read-back Latch
741000	14	v18	KR1553LA21			ЛА21			7400	Quad 2-Input NAND Gate
741002	14	v18	KR1553LE1			ЛЕ1			7402	Quad 2-Input NOR Gate
741003	14	v18	KR1553LA23			ЛА23			7403	Quad 2-Input NAND Gate
741004	14	v18	KR1553LH8			ЛН8			7404	Hex inverting Buffer
741005	14	v18	KR1553LH10			ЛН10			7405	Hex inverting Buffer
741008	14	v18	KR1553LI8			ЛИ8			7408	Quad 2-Input AND Gate
741010	14	v18	KR1553LA24			ЛА24			7410	Triple 3-Input NAND Gate
741011	14	v18	KR1553LI10			ЛИ10			7411	Triple 3-Input AND Gate
741020	14	v18	KR1553LA22			ЛА22			7420	Dual 4-Input NAND Gate
741032	14	v18	KR1553LL4			ЛЛ4			7432	Quad 2-Input OR Gate
741034	14	v18	KR1553LP16			ЛП16			7434	Hex non-inverting Buffer
741035	14	v18	KR1553LP17			ЛП17			7435	Hex non-inverting Buffer
741240	20	v18							74240	Octal Buffer / line Driver, inverting (lower-power version of 74x240)
741241	20	v18							74241	Octal Buffer / line Driver, non-inverting (lower-power version of 74x241)
741242	14	v18							74242	Quad Bus Transceiver, inverting (lower-power version of 74x242)
741243	14	v18							74243	Quad Bus Transceiver, non-inverting (lower-power version of 74x243)
741244	20	v18							74244	Octal Buffer / Driver, non-inverting (lower-power version of 74x244)
741245	20	v18							74245	Octal Bus Transceiver (lower-power version of 74x245)
741620	20	v18							74620	Octal Bus Transceiver, inverting
741621	20	v18							74621	Octal Bus Transceiver, non-inverting
741622	20	v18							74622	Octal Bus Transceiver, inverting
741623	20	v18							74623	Octal Bus Transceiver, non-inverting
741638	20	v18							74638	Octal Bus Transceiver, inverting (lower-power version of 74x638)
741639	20	v18							74639	Octal Bus Transceiver, non-inverting (lower-power version of 74x639)
741640	20	v18							74640	Octal Bus Transceiver, inverting (lower-power version of 74x640)
741641	20	v18							74641	Octal Bus Transceiver, non-inverting (lower-power version of 74x641)
741642	20	v18							74642	Octal Bus Transceiver, inverting (lower-power version of 74x642)
741643	20	v18							74643	Octal Bus Transceiver, inverting and non-inverting (lower-power version of 74x643)
741644	20	v18							74644	Octal Bus Transceiver, inverting and non-inverting (lower-power version of 74x644)
741645	20	v18							74645	Octal Bus Transceiver, non-inverting (lower-power version of 74x645)
742708	14	ROM								8192-bit PROM (1024x8)
743037	16	v18								Quad 2-Input NAND
743708	14	ROM								8192-bit PROM (1024x8)
747001	14	v16								Quad 2-Input AND Gate
747002	14	v16								Quad 2-Input NOR Gate
747014	14	v16								Hex non-inverting Buffer
747032	14	v16								Quad 2-Input OR Gates
747080	14	TBD								16-bit parity generator / checker
747266	14	v17								Quad 2-Input XNOR Gate
747403	16	FIFO								256-bit FIFO memory (64x4)
747404	16	FIFO								320-bit FIFO memory (64x5)
748541	20	v18								8-bit Buffer, selectable inverting/non-inverting
749034	20	v17								Nine-wide Buffer, inverting
749035	20	v17								Nine-wide Buffer
749114	20	v17								Nine-wide Buffer, inverting
749115	20	v17								Nine-wide Buffer
749134	20	v17								Nine-wide Buffer, inverting
749135	20	v17								Nine-wide Buffer
749240	24	v17								9-bit Buffer / line Driver, inverting
749244	24	v17								9-bit Buffer / line Driver, non-inverting
749245	24	v17								9-bit bidirectional transceiver, non-inverting
75121	16	v18								Dual line Driver
75122	16	v18								Triple line Driver (N8T14)
75123	16	v18								Dual line Driver
75124	16	v18								Triple line Driver
75125	16	v18								7x line receiver, inverting Outputs
75127	16	v18								7x line receiver, inverting Outputs
75138	16	v18								4x Bus Transceiver
75140	8	v22								2x line receiver
75154	16	v20								4x line receiver
75160	20	v16								8x Bus Transceiver
75172	16	v18								4x line Driver (SN65173)
75173	16	v18								4x line Driver
75189	14	v15								4x Line Drivers (MC1489)
75450	14	v17								2x AND high power
75451	8	v14	K155LA5			ЛА5				2x AND high power
75452	8	v14	K155LA18			ЛА18				2x NAND high power
75453	8	v14	K155LL2			ЛЛ18				2x OR high power
75454	8	v14								2x NOR high power
75460	14	v17	K1102AP10			АП10				2x AND high power
75461	8	v17	K1102AP11		D461D	АП11				2x AND high power
75462	8	v17	K1102AP12			АП12				2x NAND high power

Identifier	Pins	Status	UdSSR	UdSSR	DDR	Cyrillic	Philips	Siemens	Setting	Remark
75463	8	v17	K1102AP13			АП13				2x OR high power
75464	8	v17	K1102AP14			АП14				2x NOR high power
75466	16	v18								7x Darlington Arrays
75467	16	v18								7x Darlington Arrays
75468	16	v17								7x Darlington Arrays
75469	16	v18								7x Darlington Arrays
75470	14	v18								2x AND high power
75471	8	v18								2x AND high power
75472	8	v18								2x NAND high power
75473	8	v18								2x OR high power
75474	8	v18								2x NOR high power
75491	14	v21								4x digital Driver MOS to LED
75492	14	v21								6x digital Driver MOS to LED
75494	16	v23	KR1010KT1			КТ1				Hex digit Driver
75497	16	v18								MOS to LED 7-channel Driver
75498	20	v18								MOS to LED 9-channel Driver
4000	14	v14		K176LP4		ЛП4				Dual 3-Input NOR Gate + 1 NOT Gate
4001	14	v14		K176LE5		ЛЕ5				Quad 2-Input NOR Gate
4002	14	v14		K176LE6		ЛЕ6				Dual 4-Input NOR Gate
4006	14	v20		K176IR10		ИР10				18-stage Shift Register (four independent with common Clock: two 4-stage, two 5-stage with Q4 tap)
4007	14	v17		K176LP1		ЛП1				Dual complementary transistor pair + 1 NOT Gate (pin 5 and 8 untested)
4008	16	v14		K176IM1		ИМ1				4-bit binary Full Adder
4009	16	v15		K176PU2		ПУ2				Hex inverter Gate, Dual power supply, can drive 1 TTL/DTL load (replaced by 4049)
4010	16	v15		K176PU3		ПУ4				Hex Buffer Gate, Dual power supply, can drive 1 TTL/DTL load (replaced by 4050)
4011	14	v14		K176LA7		ЛА7				Quad 2-Input NAND Gate
4012	14	v14		K176LA8		ЛА8				Dual 4-Input NAND Gate
4013	14	v15		K176TM2		ТМ2				Dual D-Type Flip-Flop
4014	16	v15								8-stage parallel in Shift Register (synchronous parallel load, serial in, Q6/Q7/Q8 out) (see 4021)
4015	16	v14		K176IR2		ИР2				Dual 4-stage Shift Register (two independent: serial in, Q1/Q2/Q3/Q4 out, reset, Clock)
4016	14	v14		K176KT1		КТ1				Quad bilateral switch
4017	16	v14		K176IE8		ИЕ8				Decade Counter with 10 decoded Outputs (5-stage Johnson Counter)
4018	16	v16		K561IE19		ИЕ19				Presettable divide-by-N Counter
4019	16	v14		K561LS2		ЛС2				Quad AND/OR select Gate
4020	16	v14		K561IE16		ИЕ16				14-stage binary ripple Counter
4021	16	v15								8-stage parallel in Shift Register (synchronous parallel load, serial in, Q6/Q7/Q8 out) (see 4014)
4022	16	v14		K561IE9		ИЕ9				Octal Counter with 8 decoded Outputs (4-stage Johnson Counter)
4023	14	v14		K176LA9		ЛА9				Triple 3-Input NAND Gate
4024	14	v14								7-stage binary ripple Counter
4025	14	v15		K176LE10		ЛЕ10				Triple 3-Input NOR Gate
4026	16	v15		K176IE4		ИЕ4				Decade Counter with decoded 7-segment display Outputs and display enable
4027	16	v14		K176TV1		ТВ1				Dual J-K master-slave Flip-Flop
4028	16	v14		K176ID1		ИД1				BCD to decimal (1-of-10) Decoder active HIGH Output
4029	16	v14		K561IE14		ИЕ14				Presettable up/down Counter, binary or BCD-decade
4030	14	v15		K176LP2		ЛП2				Quad XOR Gate (replaced by 4070)
4031	16	v23 (!)		K176IR4		ИР4				64-stage Shift Register (no loop test possible, not Fully tested, Output signal test only)
4032	16	v16								Triple serial Adder
4033	16	v15		K176IE5		ИЕ5				Decade Counter with decoded 7-segment display Outputs and ripple blanking
4034	24	v17		K561IR6		ИР6				8-stage bidirectional parallel/serial Input/Output Register
4035	16	v16		K561IR9		ИР9				4-stage parallel-in/parallel-out Shift Register
4036	24	RAM								RAM 4 x 8 bit
4038	16	v16								Triple serial Adder
4039	24	RAM								RAM 4 x 8 bit
4040	16	v14		KR1561IE20		ИЕ20				12-stage binary ripple Counter
4041	14	v14								Quad Buffer/inverter (two Outputs for each Input) (4 times standard "B" drive)
4042	16	v14		K561TM3		ТМ3				Quad D-Type Latch
4043	16	v14		K561TR2		ТР2				Quad NOR R-S Latch with tri-state Outputs
4044	16	v15								Quad NAND R-S Latch with tri-state Outputs
4045	16	n/a								21-stage Counter
4046	16	n/a		KR1561GG1		ГГ1				Phase-locked loop with VCO
4047	14	n/a								Monostable/astable Multivibrator
4048	16	v15								Multifunctional expandable 8-Input Gate with tri-state Output
4049	16	v15		K561LH2		ЛН2				Hex inverter Gate, can drive 2 TTL/RTL loads or 4 four 74LS loads
4050	16	v14		K561PU4		ПУ4				Hex Buffer Gate, can drive 2 TTL/RTL loads or 4 four 74LS loads
4051	16	v15		K561KP2		КП2				8-channel analog Multiplexer/Demultiplexer
4052	16	v15		K561KP1		КП1				Dual 4-channel analog Multiplexer/Demultiplexer
4053	16	v14		KR1561KP5		КП5				Triple 2-channel analog Multiplexer/Demultiplexer
4054	16	v15								BCD to 7-segment Decoder/LCD Driver
4055	16	v15								BCD to 7-segment Decoder/LCD Driver with "display-frequency" Output
4056	16	v15								BCD to 7-segment Decoder/LCD Driver with strobed-latch function
4059	24	v18		K561IE15		ИЕ15				Programmable divide-by-N Counter
4060	16	v16								14-stage binary ripple Counter and Oscillator, schmitt Trigger Inputs
4061	16	RAM		K176RU2		РУ2				RAM 256 x 1 bit
4063	16	v16								4-bit digital comparator
4066	14	v14		K561KT3		КТ3				Quad analog switch (low "ON" resistance)
4067	24	v16								16-channel analog Multiplexer/Demultiplexer (1-of-16 switch)
4068	14	v15								8-Input NAND/AND Gate (2 Outputs)
4069	14	v14		564PU7		ПУ7				Hex inverter
4070	14	v14		KR1561LP14		ЛП14				Quad 2-Input XOR Gate
4071	14	v14								Quad 2-Input OR Gate
4072	14	v14								Dual 4-Input OR Gate
4073	14	v14								Triple 3-Input AND Gate
4075	14	v14								Triple 3-Input OR Gate
4076	16	v14		KR1561IR14		ИР14				Quad D-Type Register with tri-state Outputs
4077	14	v14								Quad 2-Input XNOR Gate
4078	14	v14								8-Input NOR/OR Gate (2 Outputs)
4081	14	v14		K561LI2		ЛИ2				Quad 2-Input AND Gate
4082	14	v14								Dual 4-Input AND Gate
4085	14	v15								Dual 2-wide, 2-Input AND/OR invert (AOI)
4086	14	v15								Expandable 4-wide, 2-Input AND/OR invert (AOI)
4089	16	n/a								Binary rate Multiplier
4093	14	v14		K561TL1		ТЛ1				Quad 2-Input NAND Gate, schmitt Trigger Inputs
4094	16	v14		KR1561PR1		ПР1				8-stage Shift-and-store bus
4095	16	v16								Gated J-K Flip-Flop (non-inverting)
4096	16	v16								Gated J-K Flip-Flop (inverting and non-inverting)
4097	24	v24								Differential 8-channel analog Multiplexer/Demultiplexer
4098	16	v22		KR1561AG1		АГ1				Dual one-shot monostable
4099	16	v14								8-bit addressable Latch
4106	14	v18								Hex inverter Gate, schmitt Trigger Inputs
40014	14	v18								Hex Schmitt Trigger Inverter
40097	16	v18								Hex Buffer with non-inverted Three-State Outputs
40098	16	v18								Hex Buffer with inverted Three-State Outputs
40100	16	n/a								32-stage left/right Shift Register
40101	16	v13		1526IP6		ИП6				9-bit parity generator/checker
40102	16	v17								Presettable 2-decade BCD down Counter

Identifier	Pins	Status	UdSSR	UdSSR	DDR	Cyrillic	Philips	Siemens	Setting	Remark
40103	16	v17								Presettable 8-bit binary down Counter
40104	16	v16								4-bit bidirectional universal Shift Register with Output enable
40105	16	FIFO								4-bit x 16 word FIFO Register
40106	14	v14								Hex inverter Gate, schmitt Trigger Inputs
40107	8	v16		KR1561LA10		ЛА10				Dual 2-Input NAND Gate with 136mA Open-drain Driver (32 times standard "B" sink)
40109	16	v16		1526PU6		ПУ6				Quad level Shifter
40110	16	v16								Up/down decade Counter, Latch, 7-segment Decoder, LED Driver
40116	22	v17		564PU9		ПУ9				8-bit bidirectional CMOS-to-TTL level converter (checks logic only, not levels)
40117	14	n/a								Programmable Dual 4-bit terminator
40147	16	v23								10-line to 4-line (BCD) priority encoder
40160	16	v13								Decade Counter/asynchronous Clear
40161	16	v14		K561IE21		ИЕ21				Binary Counter/asynchronous Clear
40162	16	v14								Decade Counter/synchronous Clear
40163	16	v16								Binary Counter/synchronous Clear
40174	16	v14								Hex D-Type Flip-Flop
40175	16	v15								Quad D-Type Flip-Flop
40192	16	v16								Presettable 4-bit up/down BCD Counter
40193	16	v16								Presettable 4-bit up/down binary Counter
40194	16	v16								4-bit bidirectional universal Shift Register with reset
40240	20	v16								Buffer/Line Driver; inverting (tri-state)
40244	20	v16								Buffer/line Driver; non-inverting (tri-state)
40245	20	v16								Octal Bus Transceiver; (tri-state) Outputs
40257	16	v16								Quad 2-line to 1-line Data Selector/Multiplexer (tri-state)
40373	20	v16								Octal D-Type transparent Latch (tri-state)
40374	20	v16								Octal D-Type Flip-Flop; positive-edge Trigger (tri-state)
40511	16	v18								BCD to 7-segment Decoder/Driver
4316	16	v17								4x Analog Switch/Multiplexer/Demultiplexer (only digitally tested)
4428	14	v21								Binary to Octal Decoder
4501	16	v16								Triple Gate
4502	16	v15		KP561LN1		ЛН1				Hex inverting Buffer (tri-state)
4503	16	v13		K561LH3		ЛН3				Hex non-inverting Buffer with tri-state Outputs
4504	16	v13								Hex voltage level Shifter for TTL-to-CMOS or CMOS-to-CMOS operation
4505	14	RAM								RAM 64 x 1 bit
4506	16	v16								2x 2-2 AND-OR-INVERT
4507	14	v16								Quad 2-Input Exclusive-OR Gate
4508	24	v15								Dual 4-bit Latch with tri-state Outputs
4510	16	v15								Presettable 4-bit BCD up/down Counter
4511	16	v14		K1564ID23		ИД23				BCD to 7-segment Latch/decoder/Driver
4512	16	v14		KR1561KP3		КП3				8-Input Multiplexer (data Selector) with tri-state Output
4513	18	v16								BCD to 7-segment Latch/decoder/Driver
4514	24	v13								1-of-16 Decoder/Demultiplexer active HIGH Output
4515	24	v13								1-of-16 Decoder/Demultiplexer active LOW Output
4516	16	v16		KR1561IE11		ИЕ11				Presettable 4-bit binary up/down Counter
4517	16	n/a								Dual 64-stage Shift Register
4518	16	v13								Dual BCD up Counter
4519	16	v14		K561KP4		КП4				Quad 2-Input Multiplexer (data Selector)
4520	16	v14		K561IE10		ИЕ10				Dual 4-bit binary up Counter
4521	16	n/a								24-stage frequency divider
4522	16	v16								Programmable BCD divide-by-N Counter
4526	16	v17								Programmable 4-bit binary down Counter
4527	16	n/a								BCD rate Multiplier
4528	16	v22								Dual retriggerable monostable Multivibrator with reset
4529	16	v13								Dual 4-channel analog Data Selector/Multiplexer
4530	16	v16								Dual 5-Input majority logical Gate
4531	16	v15								13-Input parity checker/generator
4532	16	v15								8-bit priority encoder
4534	24	n/a								Cascaded BCD Counters
4536	16	n/a								Programmable Timer
4538	16	v22								Dual retriggerable precision monostable Multivibrator
4539	16	v16								Dual 4-Input Multiplexer
4541	14	n/a								Programmable Timer
4543	16	v15								BCD to 7-segment Latch/decoder/Driver with phase Input
4549	16	n/a								Successive approximation Registers
4551	16	n/a								Quad 2-channel analog Multiplexer/Demultiplexer
4553	16	n/a								3-digit BCD Counter
4555	16	v14		K561ID6		ИД6				Dual 1-of-4 Decoder/Demultiplexer active HIGH Output
4556	16	v14		K561ID7		ИД7				Dual 1-of-4 Decoder/Demultiplexer active LOW Output
4557	16	TBD								1-to-64 stage variable length Shift Register
4558	16	v16								BCD to 7-segment Decoder (enable, RBI and provides active–high Output)
4559	16	n/a								Successive approximation Registers
4560	16	v16								NBCD Adder
4561	14	v16								9's complementer
4566	16	n/a								Industrial time-base generator
4568	16	n/a								Phase Comparator and Programmable Counters
4569	16	TBD								Programmable divide-By-N, Dual 4-Bit binary/BCD down Counter
4572	16	v15								Hex Gate: Quad NOT, single NAND, single NOR
4574	16	v18 (*)								Quad Comparator
4583	16	n/a								Dual adjustable schmitt Trigger Inputs, each with Buffer and inverter Outputs, and XOR Output
4584	14	v14								Hex inverter Gate, schmitt Trigger Inputs
4585	16	v16		K561IP2		ИП2				4-bit digital comparator
4598	18	v17								8-bit addressable Latch
4720	16	RAM								RAM 256 x 1 bit
4723	16	v18								4-bit addressable Latch
4724	16	v16								8-bit addressable Latch
4929	16	v15						FLH251		2x NAND, 4x Inverter
4930	14	v13						FLH321		4x 2-Input NAND
4931	14	v13						FLH331		2x 5-Input NAND
4934	14	v18						FLH461		6x Inverter with Open Collector Outputs
4935	14	v18						FLH471		6x Inverter
49700	16	v15						FLL131		2x NAND Gate, 2x AND Gate with 15V Open Collector Outputs
49701	16	v15						FLL141		4x Drivers with Open Collector Outputs
49702	16	v15						FLJ491		4-bit D Register with Clear
49703	16	v13						FLH641		6x delay Gates
49704	16	v13						FLJ501		2x Binary Counter
49705	16	v15						FLJ511		2x Decimal Counter
49713	14	v13						FLH731		Dual 3-Input NAND Schmitt Trigger
49710	8	v24								Frequency Divider 50:1
49711	8	v24								Frequency Divider 60:1
49714	8	v13								2:4 Decoder
49805	20	v24								1:5 Buffer/Clock Driver
3212	24	v17							8212	use 8212, Multi-mode Buffered 8-bit Latches (equivalent to Intel 3212/8212, 74S412)
3216	16	v18							8216	use 8216, Quad parallel bidirectional bus Driver (equivalent to Intel 3216/8216/M5L8216)
3226	16	v18							8226	use 8226, Quad parallel bidirectional bus Driver, inverting Outputs (equivalent to Intel 3226/8226/M5L8226/M
3404	16	v24								High Speed 6-Bit Latch

Identifier	Pins	Status	UdSSR	UdSSR	DDR	Cyrillic	Philips	Siemens	Setting	Remark
6529	20	v24								MOS6529, Single Port Interface (SPI)
7707	14	v15								MOS7707, Hex Inverter with Open Collector (=74LS06)
7708	16	v15								MOS7708, Quad 2-line to 1-line Data Selector/Multiplexer with non-inverted TS-Outputs (=74LS257)
7709	16	v13								MOS7709, Quad 2-line to 1-line Data Selector/Multiplexer with inverted TS-Outputs (=74LS258)
7711	16	v15								MOS7711, Dual 2 to 4-line Decoder/Demultiplexer (=74LS139)
7712	14	v15								MOS7712, Quad 2-Input AND (=74LS08)
7713	14	v15								MOS7713, Hex Inverter (=74LS04)
7714	14	v15								MOS7714, Quad 2-Input NOR (=74LS02)
7715	20	v15								MOS7715, Octal Register with Three-State Outputs (=74SL373)
80C95	16	v13								Hex Buffer with non-inverted Three-State Outputs
80C96	16	v13								Hex Buffer with inverted Three-State Outputs
80C97	16	v13								Hex Buffer with non-inverted Three-State Outputs
80C98	16	v13								Hex Buffer with inverted Three-State Outputs
82C19	24	v17								16-line to 1-line Data Selector/Multiplexer
8205	16	v18								1 out of 8 binary Decoder (equivalent to SN74S405, MH3205)
8212	24	v17								Multi-mode Buffered 8-bit Latches (equivalent to Intel 3212/8212, 74S412)
8216	16	v18	K589AP16			АП16				Quad parallel bidirectional bus Driver (equivalent to Intel 3216/8216/M5L8216)
8226	16	v18	K589AP26			АП26				Quad parallel bidirectional bus Driver, inverting Outputs (equivalent to Intel 3226/8226/M5L8226/MH3226)
8233	16	v23								4x 2-Input Multiplexer
8234	16	v23								4x 2-Input Multiplexer, Open Collector Outputs
8235	16	v21								4x 2-Input Multiplexer, Open Collector Outputs (=DEC 8275)
8241	14	v19								Quad 2-Input XOR (Signetics)
8242	14	v19								4-bit comparator with Open Collector Outputs
8250	14	v20								1-of-8 Decoder active LOW Output
8251	16	v20								BCD to decimal (1-of-10) Decoder active LOW Output
8252	16	v20								BCD to decimal (1-of-10) Decoder active LOW Output (=DM9301)
82S62	14	v24								9-bit parity generator/checker
8263	24	v20								3-Input 4-Bit Digital Multiplexer (Signetics)
8264	24	v20								3-Input 4-Bit Digital Multiplexer with Enable and Open Collector Outputs (Signetics)
8266	16	v20								2-Input 4-Bit Digital Multiplexer (Signetics)
8267	16	v20								2-Input 4-Bit Digital Multiplexer and Open Collector Outputs (Signetics)
8270	14	v23								4-bit parallel-access Shift Register
8271	16	v23								4-bit parallel-access Shift Register, asynchronous Clear Input, complementary Qd Output
8280	14	v24								Presettable decade (bi-quinary) Counter/Latch
8281	14	v24								Presettable binary Counter/Latch
8282	20	v19								Octal Latch (=DS8282)
8283	20	v19								Octal Latch, inverted Outputs (=DS8283)
8286	20	v14								Octal Bus Transceiver, non-inverting Outputs (=DS8286)
8287	20	v14								Octal Bus Transceiver, inverting Outputs (=DS8287)
8290	14	v20								Presettable decade (bi-quinary) Counter/Latch
8291	14	v20								Presettable binary Counter/Latch
8292	14	v24								Presettable decade (bi-quinary) Counter/Latch, low power
8293	14	v24								Presettable binary Counter/Latch, low power
8415A	14	v20								Dual 5-Input NAND Gate, Open Collector Outputs
8455A	14	v20								Dual 4-Input NAND Gate
8470A	14	v20								Triple 3-Input NAND Gate
8471A	14	v20								Triple 3-Input NAND Gate, Open Collector Outputs
8480A	14	v20								Quad 2-Input NAND Gate
8481A	14	v20								Quad 2-Input NAND Gate, Open Collector Outputs
8490A	14	v20								Hex Inverter
8708	20	v15								MOS8708, Quad 2-line to 1-line Data Selector/Multiplexer with non-inverted TS-Outputs = 74LS257
8713	20	v15								MOS8713, Hex Inverter = 74LS04
8808A	14	v20								8-Input NAND Gate
8815A	14	v20								Dual 4-Input NOR Gate
8816A	14	v20								Dual 4-Input NAND Gate
8829A	14	v20								AND Gated J-K master-slave Flip-Flop, asynchronous Preset and Clear
8840A	14	v20								Dual 2-Wide 2-Input AND-OR-INVERT Gate
8848A	14	v20								2-2-2-3-Input AND-OR-INVERT Gate
8855A	14	v20								Dual 4-Input NAND Gate
8870A	14	v20								Triple 3-Input NAND Gate
8875A	14	v20								Triple 3-Input NOR Gate
8880A	14	v20								Quad 2-Input NAND
8881A	14	v20								Quad 2-Input NAND, Open Collector Outputs
8885A	14	v20								Quad 2-Input NOR Gate
8890A	14	v20								Hex Inverter
8891A	14	v20								Hex Inverter, Open Collector Outputs
8H16	14	v20								Dual 4-Input NAND Gate
8H70	14	v20								Triple 3-Input NOR Gate
8H80	14	v20								Quad 2-Input NOR Gate
8H90	14	v20								Hex Inverter
8T09	14	v24								Quad Bus Driver with Three-State Outputs, inverting
8T10	16	v17								Quad D Flip-Flop with Three-State Outputs
8T13	16	v17								Dual line Driver
8T14	16	v17								Triple line Driver
8T22	14	v22								retriggerable monostable Multivibrator (adapter required)
8T23	16	v17								Dual line Driver
8T24	16	v17								Triple line Driver
8T26	16	v17								Quad Bus Driver/Receiver Inverting Outputs
8T28	16	v17								Quad Bus Driver/Receiver
8T37	16	v20								Hex Line Driver (=8837/7837)
8T38	16	v20								Quad NOR Unified Driver (=8641)
8T80	14	v24								Quad 2-Input NAND, Open Collector Outputs
8T90	14	v24								Hex Inverter, Open Collector Outputs
8T93	14	v22								Hex Inverter
8T94	14	v22								Hex Inverter
8T95	16	v17								Hex Buffer with non-inverted Three-State Outputs
8T96	16	v17								Hex Buffer with inverted Three-State Outputs
8T97	16	v17								Hex Buffer with non-inverted Three-State Outputs
8T98	16	v17								Hex Buffer with inverted Three-State Outputs
8T125	20	v22								Octal Bus Transceiver, inverting Outputs
8T245	20	v17								Octal Buffers
Am25LS07	16	v20								6-Bit Register with Clock Enable
Am25LS08	16	v20								4-bit Register, Clock enable and complementary Outputs
Am25LS09	16	v20								Quad 2-Input Multiplexer, storage
Am25S10	16	v20								4-bit Shifter
Am25LS14	16	v23								8-bit by 1-bit two's complement Multipliers
Am25LS15	20	v20								Quad serial Adder/Subtractor
Am25S18	16	v24								Quad D Register with Standard and Three-State Outputs
Am25LS22	20	v20								8-Bit Shift Register, Sign Extend
Am25LS23	20	v20								8-bit Bidirectional Universal Shift/Storage Register, synchronous Clear
Am25LS2518	16	v20								4-bit D-Type Register
Am25LS2519	20	v21								4-bit D-Type Register with two Outputs
Am25LS2521	20	v20								8-bit Magnitude Comparator, enable
Am2946	20	v24								Octal Bus Transceiver, inverting Outputs (=Am7307)

Identifier	Pins	Status	UdSSR	UdSSR	DDR	Cyrillic	Philips	Siemens	Setting	Remark
Am2947	20	v24								Octal Bus Transceiver, non-inverting Outputs (=Am7308)
Am29827	24	v24								10-bit Buffer, non-inverting
Am29828	24	v24								10-bit Buffer, inverting
Am29841	24	v24								10-bit D-Type Flip-Flop
Am29842	24	v24								10-bit D-Type Flip-Flop, inverting Inputs
Am29843	24	v24								9-bit D Flip-Flops, Clear and set Inputs
Am29844	24	v24								9-bit D Flip-Flops, Clear and set Inputs, inverting Inputs
Am29845	24	v24								8-bit D Flip-Flops, Clear and set Inputs
Am29846	24	v24								8-bit D Flip-Flops, Clear and set Inputs, inverting Inputs
Am7303	20	v14								Octal Bus Transceiver, inverting Outputs
Am7304	20	v14								Octal Bus Transceiver, non-inverting Outputs
Am7307	20	v20								Octal Bus Transceiver, inverting Outputs
Am7308	20	v20								Octal Bus Transceiver, non-inverting Outputs
AmZ8121	20	v20								8-bit Magnitude Comparator, enable
Am81LS95	20	v17								Octal Buffer, non-inverting, common enable
Am81LS96	20	v17								Octal Buffer, inverting, common enable
Am81LS97	20	v17								Octal Buffer, non-inverting, enable for 4 Buffers each
Am81LS98	20	v17								Octal Buffer, inverting, enable for 4 Buffers each
Am8303	20	v14								Octal Bus Transceiver, inverting Outputs (=Am7303)
Am8304	20	v14								Octal Bus Transceiver, non-inverting Outputs (=Am7304)
Am8307	20	v20	KR559IP13	K555IP13		ИП13				Octal Bus Transceiver, inverting Outputs
Am8308	20	v20	KR559IP14	K555IP14		ИП13				Octal Bus Transceiver, non-inverting Outputs
Am93S10	16	v20								Synchronous 4-Bit Decade Counter with asynchronous Clear
Am93S16	16	v20								Synchronous 4-Bit Binary Counter with asynchronous Clear
Am9341	24	v20								4-Bit Arithmetic Logic Unit and Function Generator
DM7093	14	v22							DM8093	Quad Bus Buffer with Three-State Outputs
DM7094	14	v22							DM8094	Quad Bus Buffer with Three-State Outputs
DM7131	16	v20							DM8131	use DM8131, 6-Bit Unified Bus Comparator
DM7160	16	v20							DM8160	use DM8160, 6-Bit Unified Bus Comparator
DM7220	14	v24							DM8220	9-bit parity generator/checker (=DM8220)
DM74L90	14	v23							7490	4-Bit Decade Counter
DM74L93	14	v23							74C93	4-Bit Binary Counter
DM7833	16	v24							DM8833	Quad Three-State Bus Transceivers (=DM7833)
DM7835	16	v24							DM8835	Quad Three-State Bus Transceivers, inverting Outputs (=DM7835)
DM7837	16	v20							DM8837	use DM8837, Hex Line Driver
DM7838	16	v20							DM8838	use DM8838, Quad NOR Unified Driver
DM8090	16	v23								2x NAND, 4x Inverter
DM8091	14	v23								4x 2-Input NAND
DM8092	14	v23								Dual 5-Input NAND Gate
DM8093	14	v22								Quad Bus Buffer with Three-State Outputs
DM8094	14	v22								Quad Bus Buffer with Three-State Outputs
DM8095	16	v23								Hex Buffer with non-inverted Three-State Outputs
DM8096	16	v23								Hex Buffer with inverted Three-State Outputs
DM8097	16	v23								Hex Buffer with non-inverted Three-State Outputs
DM8098	16	v23								Hex Buffer with inverted Three-State Outputs
DM8121	16	v20								8-line to 1-Line Data Selector/Multiplexer
DM8123	16	v20								Quad 2-Line to 1-Line Data Selector/Multiplexer
DM8131	16	v20								6-Bit Unified Bus Comparator (=DM7131)
DM8160	16	v20								6-Bit Unified Bus Comparator (=DM7160)
DM8220	14	v24								9-bit parity generator/checker
DM8530	14	v19 (!)								4-Bit Decade Counter
DM8532	14	v19 (!)								Divide-by-12 Counter
DM8533	14	v19 (!)								4-Bit Binary Counter
DM8560	16	v19								Synchronous Up/Down Decade Counter with Clear
DM8563	16	v19								Synchronous Up/Down Binary Counter with Clear
DM8570	14	v19								8-Bit Parallel-Out Serial Shift Register with asynchronous Clear
DM8590	16	v19								8-Bit Serial Shift Register
DM86L75	16	v19							74160	Synchronous 4-Bit Decade Counter with asynchronous Clear
DM86L76	16	v19							74161	Synchronous 4-Bit Binary Counter with asynchronous Clear
DM86L93	14	v14 (!)							7493	4-Bit Binary Counter
DM8830	14	v15								Dual Differential Line Driver
DM8833	16	v24								Quad Three-State Bus Transceivers (=DM7833)
DM8835	16	v24								Quad Three-State Bus Transceivers, inverting Outputs (=DM7835)
DM8837	16	v20								Hex Line Driver (=DM7837)
DM8838	16	v20								Quad NOR Unified Driver (=DM7838)
DM9002	14	v18								Quad 2-Input NAND Gate
DM9003	14	v18								Triple 3-Input NAND Gate
DM9004	14	v18								Dual 4-Input NAND Gate
DM9009	14	v20								Dual Schmitt Trigger 4-Input NAND Gate
DM9012	14	v18								Quad 2-Input NAND Gate with Open Collector Outputs
DM9016	14	v18								Hex Inverter
DM9024	16	v18								Dual J-Not-K Positive-Edge-triggered Flip-Flop with Clear and Preset
DM9300	16	v20								4-Bit Parallel-Access Shift Register (=DM8300)
DM9301	16	v20								BCD to decimal (1-of-10) Decoder active LOW Output (=DM8301)
DM9310	16	v23								Synchronous 4-Bit Decade Counter with asynchronous Clear
DM9314	16	v18								Quad Latch
DM9316	16	v23								Synchronous 4-Bit Binary Counter with asynchronous Clear
DM9322	16	v20								Quad 2-Line to 1-Line Data Selector/Multiplexer (=DM8322)
DM9334	16	v24								8-Bit addressable Latch
DM93S47	16	v24								High Speed 6-Bit Identity Comparator
DM9368	16	v18								BCD to 7-segment Decoder/Driver (=F9368)
DM9370	16	v18								BCD to 7-segment Decoder/Driver with Open Collector Outputs (F9370)
DM9602	16	v24							4098	Dual retriggerable precision monostable Multivibrator (=4098,4528,4538)
MC3482	20	v24							MC6882	Octal Register, inverting Outputs (=MC6882)
MC6880	16	v17								Quad Bus Driver/Receiver Inverting Outputs (=8T26)
MC6882	20	v24								Octal Register, inverting Outputs (=3482)
MC6885	16	v17								Hex Buffer with non-inverted Three-State Outputs (=8T95)
MC6886	16	v17								Hex Buffer with inverted Three-State Outputs (=8T96)
MC6887	16	v17								Hex Buffer with non-inverted Three-State Outputs (=8T97)
MC6888	16	v17								Hex Buffer with inverted Three-State Outputs (=8T98)
MC6889	16	v17								Quad Bus Driver/Receiver (=8T28)
DS1630	14	v18								Hex TTL Buffer
DS1631	8	v18								2x AND high power
DS1632	8	v18								2x NAND high power
DS1633	8	v18								2x OR high power
DS1634	8	v18								2x NOR high power
DS3630	14	v18								Hex TTL Buffer
DS3631	8	v18								2x AND high power
DS3632	8	v18								2x NAND high power
DS3633	8	v18								2x OR high power
DS3634	8	v18								2x NOR high power
DS3662	16	v20								Quad NOR Unified Driver (=DS8641)
DS7640	14	v18							DS8640	Quad NOR Unified Driver (=DS8640)
DS7641	16	v19							DS8641	Quad NOR Unified Driver (=DS8641)

Identifier	Pins	Status	UdSSR	UdSSR	DDR	Cyrillic	Philips	Siemens	Setting	Remark
DS7810	14	v18							DS8810	Quad 2-Input NAND Gate with Open Collector Outputs (=DS8810)
DS7811	14	v18							DS8811	Quad 2-Input NAND Gate with Open Collector Outputs (=DS8811)
DS7812	14	v18							DS8812	Hex Inverter (=DS8812)
DS7819	14	v18							DS8819	Quad 2-Input AND Gate with Open Collector Outputs (=DS8819)
DS7833	16	v23							DS8833	Quad Three-State Bus Transceivers
DS7835	16	v23							DS8835	Quad Three-State Bus Transceivers, inverting Outputs
DS7837	16	v20							DS8837	use DS8837, Hex Line Driver
DS7838	16	v20							DS8838	use DS8838, Quad NOR Unified Driver
DS8282	20	v19							8282	Octal Latch (=Intel 8282)
DS8283	20	v19							8283	Octal Latch, inverted Outputs (=Intel 8283)
DS8286	20	v14							8286	Octal Bus Transceiver, non-inverting Outputs (=Intel 8286)
DS8287	20	v14							8287	OOctal Bus Transceiver, inverting Outputs (=Intel 8287)
DS8640	14	v18								Quad NOR Unified Driver (=DS7640)
DS8641	16	v19								Quad NOR Unified Driver (=DS7641)
DS8810	14	v18								Quad 2-Input NAND Gate with Open Collector Outputs (=DS7810)
DS8811	14	v18								Quad 2-Input NAND Gate with Open Collector Outputs (=DS7811)
DS8812	14	v18								Hex Inverter (=DS7812)
DS8819	14	v18								Quad 2-Input AND Gate with Open Collector Outputs (=DS7819)
DS8833	16	v23								Quad Three-State Bus Transceivers (=DS7833)
DS8835	16	v23								Quad Three-State Bus Transceivers, inverting Outputs (=DS7835)
DS8837	16	v20								Hex Line Driver (=DS7837)
DS8838	16	v20								Quad NOR Unified Driver (=DS7838)
DS16149	16	v24								Hex Buffer with inverted Three-State Outputs
DS16179	16	v24								Hex Buffer with inverted Three-State Outputs
DS36149	16	v24							DS16149	Hex Buffer with inverted Three-State Outputs (=DS16149)
DS36179	16	v24							DS16179	Hex Buffer with inverted Three-State Outputs (=DS16179)
BA12003	16	v20								7x Darlington Arrays (=ULN200x)
CA3045	14	v22								5x NPN arrays
CA3046	14	v22								5x NPN arrays
CA3081	16	v22								7x NPN arrays
CA3082	16	v17								7x NPN arrays
CA3083	16	v22								5x NPN arrays
CA3086	14	v22								5x NPN arrays
CA3161	16	v18								BCD to 7-segment Decoder/Driver
L20x	16	v18								4x Darlington Arrays
L70x	16	v18								7x Darlington Arrays
QS3384	24	v18								10x high-speed switch
TD6208x	18	v18								8x Darlington Arrays (TD62081/TD62082/TD62083/TD62084)
TD6278x	18	v18								8x Darlington Arrays (TD62783/TD62784)
ULN200x	16	v14								7x Darlington Arrays (ULN2001/ULN2003/ULN2004/ULN2005)
ULN201x	16	v20								7x Darlington Arrays (ULN2011/ULN2013/ULN2014/ULN2015)
ULN202x	16	v18								7x Darlington Arrays (ULN2021/ULN2023/ULN2024/ULN2025)
ULN2064	16	v18								4x Darlington Arrays (also ULN2066)
ULN2074	16	v18	K1109KT3			КТ3				4x Darlington Arrays (also ULN2076)
ULN280x	18	v14								8x Darlington Arrays
ULN282x	18	v17								8x Darlington Arrays
UDN6118	18	v17								8x VFD Driver
UDN298x	18	v18								8x Darlington Arrays
LMx39	14	v18 (*)								Quad Differential Comparators LM139, LM239, LM339 (Analog Device)
uA741	8	v18 (*)								General-Purpose Operational Amplifiers (Analog Device)
FCJ121	14	v19								J-K Flip-Flop
D345	16	v20								BCD to 7-segment Decoder/Driver
D346	16	v20								BCD to 7-segment Decoder/Driver
D492	14	v21								6x digital Driver
D718	24	v21								16-bit serial-in, serial/parallel-out Shift Register, Output storage Registers
K155IE1	14	v23		K155IE1		ИЕ1				10:1 frequency divider (no compatible western device)
KR559IP1	16	v20		KR559IP1		ИП1				Quad 2-Input NAND (no compatible western device)
KR559IP2	16	v20		KR559IP2		ИП2				Quad 2-Input NOR (no compatible western device)
SNG40	14	v20								2x 4-Input NAND (= SNG 4x)
SNG60	14	v20								8-Input NAND (= SNG 6x)
SNG90	14	v20								2x 2-3-Input AND/NOR (= SNG 0x)
SNG130	14	v20								2x 4-Input NAND (= SNG 13x)
SNG140	14	v20								4x 2-Input NAND (= SNG 14x)
SNG150	14	v20								2-2-2-3-inp AND-OR (= SNG 15x)
SNG160	14	v20								3x 2-Input NAND (= SNG 16x)
SNG190	14	v20								3x 3-Input NAND (= SNG 19x)
SNG220	14	v20								4x 2-Input NAND (= SNG 22x)
SNG230	14	v19								2-2-2-3-inp AND-OR (= SNG 23x)
SNG240	14	v20								2x 4-Input NAND (= SNG 24x)
SNG260	14	v20								8-Input NAND (= SNG 26x)
SP302A	14	v20								4x 2-Input AND
SP304A	14	v20								2x 4-Input AND
SP305A	14	v20								6-Input AND
SP306A	14	v20								2x 3-Input AND
SP314A	14	v20								7-Input NOR
SP317A	14	v20								2x 4-Input NOR
SP334A	14	v20								2x 4-Input OR
SP337A	14	v20								2x 4-Input NAND
SP357A	14	v20								4x 2-Input NAND
SP358A	14	v20								4x 2-Input NAND
SP370A	14	v20								3x 3-Input NOR
SP374A	14	v20								3x 3-Input OR
SP375A	14	v20								3x 2-Input OR
SP377A	14	v20								3x 3-Input NAND
SP380A	14	v20								4x 2-Input NOR
SP381A	14	v20								4x 2-Input NOR
SP384A	14	v20								4x 2-Input OR
SP387A	14	v20								4x 2-Input NAND
SP391A	14	v20								6x Inverter
CBM251641-02	28	v19								PLA Plus/4
CBM906114-01	28	v19								PLA C64
DIS1417	14	v18								Hex Display (DIS1417), visual test, returns always "Chip OK"
DL1414	8	v18								4-Character Display (DL1414), visual test (0123/****/OOOO), returns always "Chip OK"
DL2416	18	v18								4-Character Display (DL2416), insert reverse, visual test (0123/****/OOOO), returns always "Chip OK"
HP730x	8	v18								Dec Display (HP5082-7300 and HP5082-7302), visual test, returns always "Chip OK"
HP7340	8	v18								Hex Display (HP5082-7340), visual test, returns always "Chip OK"
TIL306/307	14	v19								Dec Display (TIL306/TIL307)
TIL308/309	14	v19								Dec Display (TIL308/TIL309)
TIL311	14	v18								Hex Display (TIL311), visual test, returns always "Chip OK"
VQB76	14	v22								7 Segment LED with DP
1x7 Seg. CC: 3, 8	10	v17								1x 7 Segment LED with DP, CC: 3, 8
1x7 Seg. CA: 3, 8	10	v17								1x 7 Segment LED with DP, CA: 3, 8
1x7 Seg. CC: 1, 6	10	v17								1x 7 Segment LED with DP, CC: 1, 6
1x7 Seg. CA: 1, 6	10	v17								1x 7 Segment LED with DP, CA: 1, 6

Identifier	Pins	Status	UdSSR	UdSSR	DDR	Cyrillic	Philips	Siemens	Setting	Remark
1x7 Seg. CC: 7, 9	10	v17								1x 7 Segment LED with DP, CC: 7, 9
1x7 Seg. CA: 7, 9	10	v17								1x 7 Segment LED with DP, CA: 7, 9
2x7 Seg. CC: 4, 5	18	v22			VQE23					2x 7 Segment LED with DP, CC: 4, 5
2x7 Seg. CA: 4, 5	18	v22			VQE24					2x 7 Segment LED with DP, CA: 4, 5
2x7 Seg. CC: 5, 10	10	v17								2x 7 Segment LED with DP, CC: 5, 10
2x7 Seg. CA: 5, 10	10	v17								2x 7 Segment LED with DP, CA: 5, 10
2x7 Seg. CC: 7, 8	10	v17								2x 7 Segment LED with DP, CC: 7, 8
2x7 Seg. CA: 7, 8	10	v17								2x 7 Segment LED with DP, CA: 7, 8
2x7 Seg. CC: 13, 14	18	v17								2x 7 Segment LED with DP, CC: 13, 14
2x7 Seg. CA: 13, 14	18	v17								2x 7 Segment LED with DP, CA: 13, 14
3x7 Seg. CC: 8, 9, 12	12	v17								3x 7 Segment LED with DP, CC: 8, 9, 12
3x7 Seg. CA: 8, 9, 12	12	v17								3x 7 Segment LED with DP, CA: 8, 9, 12
4x7 Seg. CC: 6, 8, 9, 12	12	v17								4x 7 Segment LED with DP, CC: 6, 8, 9, 12
4x7 Seg. CA: 6, 8, 9, 12	12	v17								4x 7 Segment LED with DP, CA: 6, 8, 9, 12
8x8 Dot Matrix CC	16	v17								8x8 Dot Matrix, CC: 13, 3, 4, 10, 6, 11...
8x8 Dot Matrix CA	16	v17								8x8 Dot Matrix, CA: 13, 3, 4, 10, 6, 11...
MB425	24	v17							8212	use 8212, Multi-mode Buffered 8-bit Latches (equivalent to Intel 3212/8212, 74S412)
TC5012	16	v14							74367	Hex Buffer with non-inverted Three-State Outputs
FJH311	14	v15							7410	Triple 3-Input NAND Gate
FJH321	14	v15							7405	Hex Inverter
FLH591	14	v15							7409	Quad 2-Input AND Gate with Open Collector Outputs