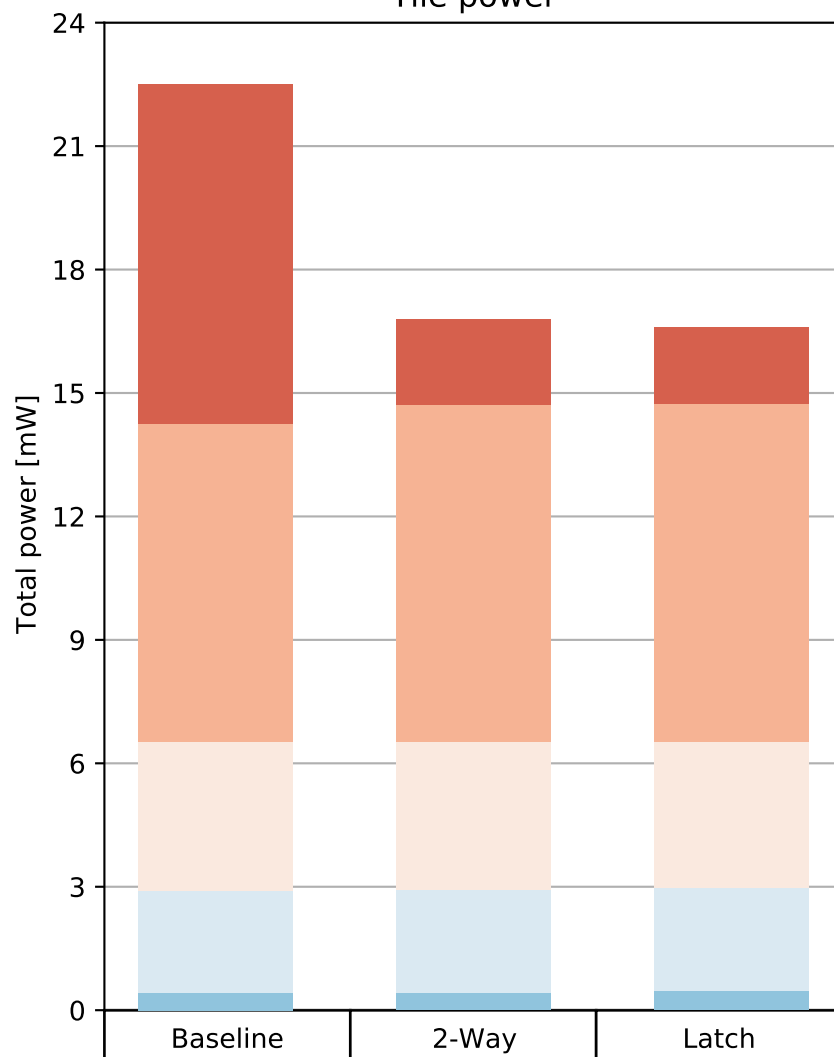
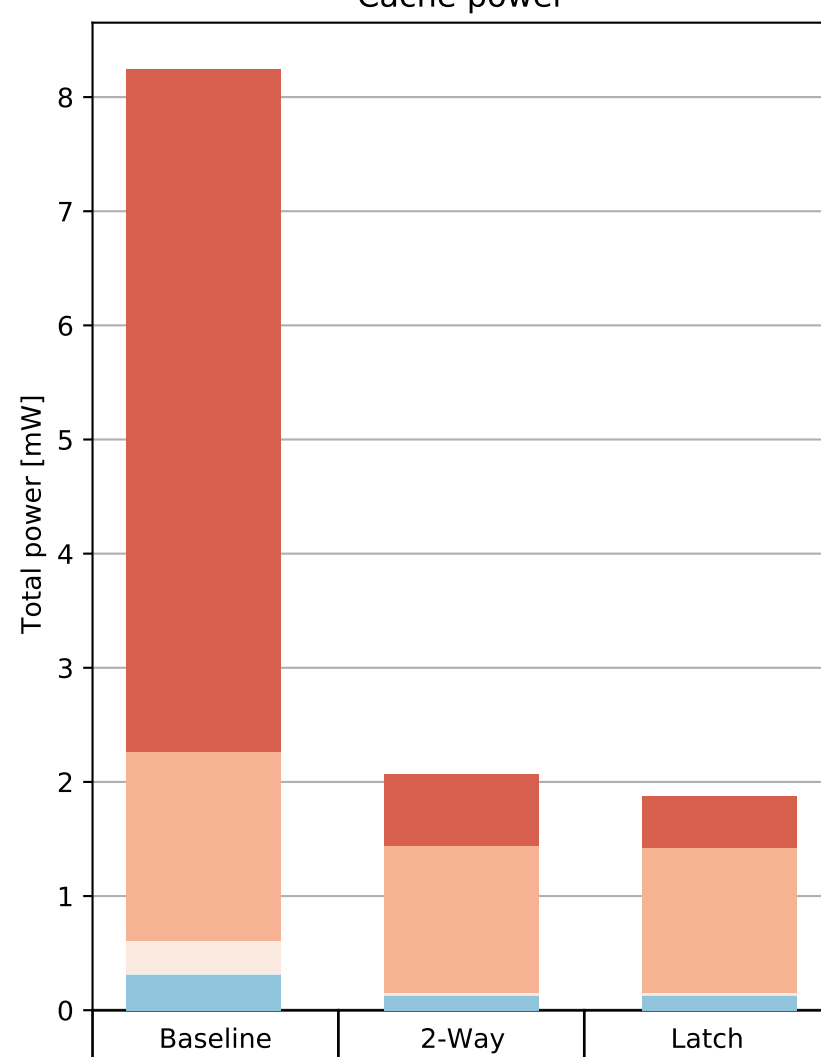


Tile power



	Baseline	2-Way	Latch
Cache	8.24	2.07	1.87
Cores	7.73	8.21	8.20
Data Banks	3.64	3.58	3.56
Interco	2.48	2.50	2.50
Remainder	0.41	0.43	0.47
Total	22.50	16.80	16.60

Cache power



	Baseline	2-Way	Latch
L1 lookup	5.98	0.63	0.45
L0 cache	1.66	1.29	1.27
L1 handler	0.29	0.02	0.02
L1 refill	0.01	0.01	0.01
Remainder	0.31	0.12	0.12
Total	8.24	2.07	1.87