

# Computer Organization and Design

## Procedures & Stacks



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Slides adapted from Montek Singh, who adapted them  
from Leonard McMillan and from Gary Bishop  
Back to McMillan & Chris Terman, MIT 6.004 1999

Thursday, Feb. 19, 2015

Lecture 8

# Today

## \* Procedures

- What are procedures?
- Why use them?
- How is call/return implemented in assembly?
- Recursion

## \* Stacks

- Push and pop
- How useful for implementing procedures?

# What are Procedures?

## \* Also called:

- functions
- methods
- subroutines

## \* Key Idea:

- main routine  $M$  **calls** a procedure  $P$
- $P$  does some work, then **returns** to  $M$ 
  - execution in  $M$  picks up where left off
  - i.e., the instruction in  $M$  right after the one that called  $P$

# Why Use Procedures?

## \* Readability

- divide up long program into smaller procedures

## \* Reusability

- call same procedure from many parts of code
- programmers can use each others' code

## \* Parameterizability

- same function can be called with different arguments/parameters at runtime

## \* Polymorphism (in OOP)

- in C++/Java, behavior can be determined at runtime as opposed to compile time

## \* Any other reason...?

# Why Use Procedures?

## \* Examples:

- Reusable code fragments (modular design)

```
clear_screen();  
...  
# code to draw a bunch of lines  
clear_screen();  
...  
...
```

- Parameterized functions (variable behaviors)

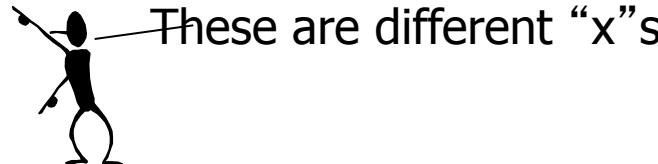
```
line(x1, y1, x2, y2, color);  
line(x2,y2,x3,y3, color);  
...  
# Draw a polygon  
for (i=0; i<N-1; i++)  
    line(x[i],y[i],x[i+1],y[i+1],color);  
  
line(x[i],y[i],x[0],y[0],color);
```

# Another Reason: Scope of Variables

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## \* Local scope (Independence)

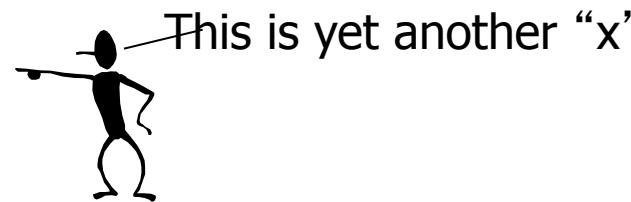
```
int x = 9;
```



```
int fee(int x) {  
    return x+x-1;  
}
```

```
int foo(int i) {  
    int x = 0;  
    while (i > 0) {  
        x = x + fee(i);  
        i = i - 1;  
    }  
    return x;  
}
```

```
main() {  
    fee(foo(y));  
}
```



Removes need  
to keep track  
of all of the  
variable names!

# Using Procedures

## \* A “calling” program (Caller) must:

- Provide procedure parameters
  - put the arguments in a place where the procedure can access them
- Transfer control to the procedure
  - jump to it

## \* A “called” procedure (Callee) must:

- Acquire the resources needed to perform the function
- Perform the function
- Place results in a place where the Caller can find them
- Return control back to the Caller

## \* Solution (at least a partial one):

- Allocate registers & memory for *each* of these specific functions

# MIPS Register Usage

- \* The ISA (Instruction Set Architecture) designates a “linkage register” for calling procedures (\$31)
  - Transfer control to Callee using the jal instruction
  - Return to Caller with the jr \$31 or jr \$ra instruction
- \* Conventions designate registers for procedure **arguments** (\$4-\$7) and return **values** (\$2-\$3).



The “linkage register” is where the return address of back to the callee is stored. This allows procedures to be called from any place, and for the caller to come back to the place where it was invoked.

Name	Register number	Usage
\$zero	0	the constant value 0
\$at	1	assembler temporary
\$v0-\$v1	2-3	procedure return values
\$a0-\$a3	4-7	procedure arguments
\$t0-\$t7	8-15	temporaries
\$s0-\$s7	16-23	saved by callee
\$t8-\$t9	24-25	more temporaries
\$k0-\$k1	26-27	reserved for operating system
\$gp	28	global pointer
\$sp	29	stack pointer
\$fp	30	frame pointer
\$ra	31	return address

# And It "Sort Of" Works

## \* Example:

```
.globl x
.data
x: .word 9

.globl fee
.text
fee:
    add $v0,$a0,$a0
    addi $v0,$v0,-1
    jr $ra
```

```
.globl main
.text
main:
    lw $a0,x
    jal fee
    jr $ra
```

Callee

Caller

Works for special cases where the Callee needs few resources and calls no other functions.

This type of function is called a **LEAF** function.

But there are lots of issues:

How can fee call functions?

More than 4 arguments?

Local variables?

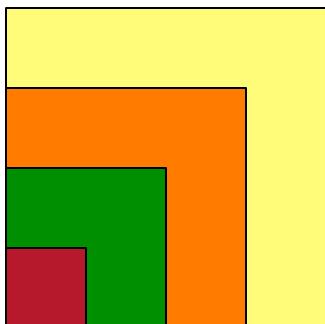
Where will main return to?

Let's consider the worst case of a Callee as a Caller...

# Writing Procedures

```
int sqr(int x) {
    if (x > 1)
        x = sqr(x-1)+x+x-1;
    return x;
}
```

```
main()
{
    sqr(10);
}
```



How do we go about writing callable procedures? We'd like to support not only LEAF procedures, but also procedures that call other procedures, ad infinitum (e.g. a recursive function).

$$\begin{aligned} \text{sqr}(10) &= \text{sqr}(9)+10+10-1 = 100 \\ \text{sqr}(9) &= \text{sqr}(8)+9+9-1 = 81 \\ \text{sqr}(8) &= \text{sqr}(7)+8+8-1 = 64 \\ \text{sqr}(7) &= \text{sqr}(6)+7+7-1 = 49 \\ \text{sqr}(6) &= \text{sqr}(5)+6+6-1 = 36 \\ \text{sqr}(5) &= \text{sqr}(4)+5+5-1 = 25 \\ \text{sqr}(4) &= \text{sqr}(3)+4+4-1 = 16 \\ \text{sqr}(3) &= \text{sqr}(2)+3+3-1 = 9 \\ \text{sqr}(2) &= \text{sqr}(1)+2+2-1 = 4 \\ \text{sqr}(1) &= 1 \\ \text{sqr}(0) &= 0 \end{aligned}$$

# Procedure Linkage: First Try

Callee/Caller

```
int sqr(int x) {
    if (x > 1)
        x = sqr(x-1)+x+x-1;
    return x;
}
```

Caller

```
main()
{
    sqr(10);
}
```

**OOPS!**

\$t0 is clobbered on successive calls.

Will saving “x” in some register or at some fixed location in memory help?

MIPS Convention:

- pass 1<sup>st</sup> arg x in \$a0
- save return addr in \$ra
- return result in \$v0
- use only temp registers to avoid saving stuff

sqr:	slti      \$t0,\$a0,2
	beq      \$t0,\$0,then    #! (x<2)
	add      \$v0,\$0,\$a0
	beq      \$0,\$0,rtn

then:	add      \$t0,\$0,\$a0
	addi     \$a0,\$a0,-1
	jal      sqr
	add      \$v0,\$v0,\$t0
	add      \$v0,\$v0,\$t0
	addi     \$v0,\$v0,-1

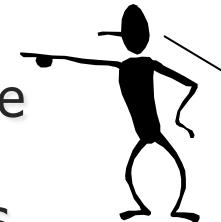
rtn:	jr      \$ra
------	--------------

 We also clobber our return address, so there's no way back!

# A Procedure's Storage Needs

## Basic Overhead for Procedures/Functions:

- **Caller** sets up ARGUMENTs for **callee**  
 $f(x, y, z)$  or even...  $\sin(a+b)$
- **Caller** invokes **Callee** while saving the Return Address to get back
- **Callee** saves stuff that **Caller** expects to remain unchanged
- **Callee** executes
- **Callee** passes results back to **Caller**.



In C it's the caller's job to evaluate its arguments as expressions, and pass the resulting values to the callee... Therefore, the CALLEE has to save arguments if it wants access to them after calling some other procedure, because they might not be around in any variable, to look up later.

## Local variables of Callee:

```
...
{
    int x, y;
    ... x ... y ...
}
```

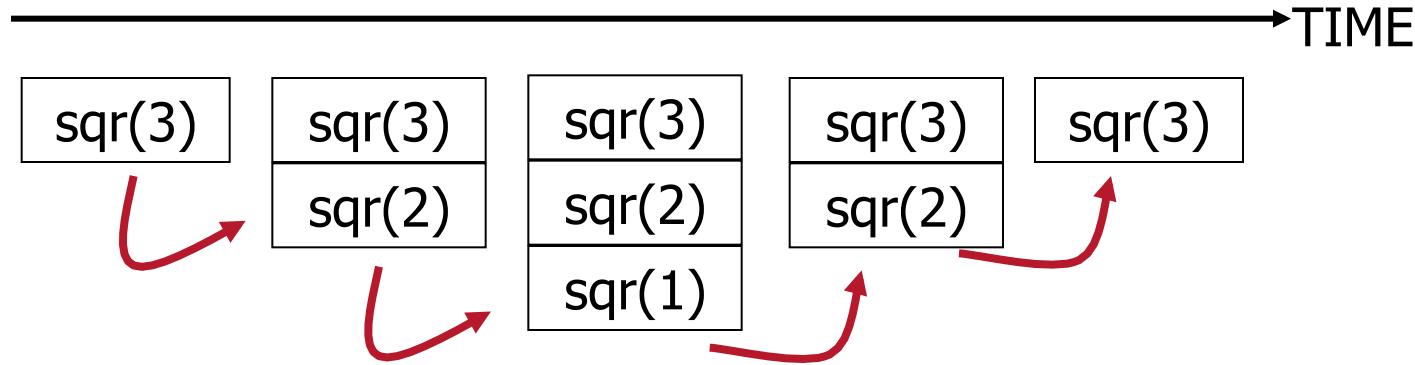
Each of these is specific to a “particular” invocation or *activation* of the Callee. Collectively, the arguments passed in, the return address, and the callee's local variables are its *activation record, or call frame*.

# Lives of Activation Records

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```
int sqr(int x) {  
    if (x > 1)  
        x = sqr(x-1)+x+x-1;  
    return x;  
}
```

Where do we  
store activation  
records?



A procedure call creates a new activation record. Caller's record is preserved because we'll need it when call finally returns.

Return to previous activation record when procedure finishes, permanently discarding activation record created by call we are returning from.

# We Need Dynamic Storage!

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What we need is a SCRATCH memory for holding temporary variables. We'd like for this memory to grow and shrink as needed. And, we'd like it to have an easy management policy.

One possibility is a

## STACK

A last-in-first-out (LIFO) data structure.



Some interesting properties of stacks:

**SMALL OVERHEAD.**  
Only the top is directly visible, the so-called “top-of-stack”

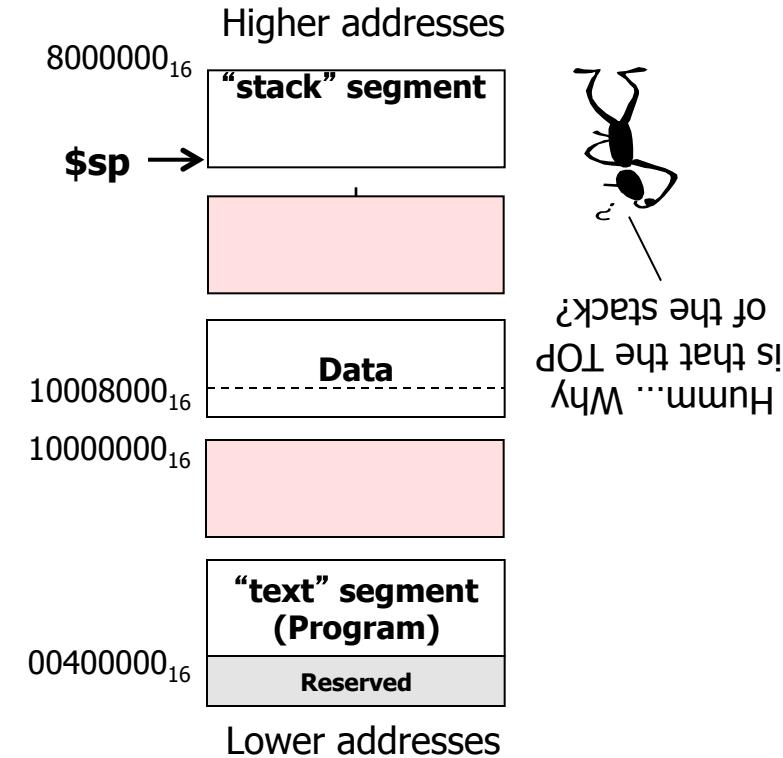
Add things by PUSHING new values on top.

Remove things by POPPING off values.

# MIPS Stack Convention

## CONVENTIONS:

- Waste a register for the Stack Pointer ( $\$sp = \$29$ ).
- Stack grows DOWN (towards lower addresses) on pushes and allocates
- $\$sp$  points to the **TOP** \*used\* location.
- Place stack far away from our program and its data



Other possible implementations include:

- 1) stacks that grow "UP"
- 2) SP points to first UNUSED location

# Stack Management Primitives

**ALLOCATE**  $k$ : reserve  $k$  WORDS of stack

$$\text{Reg[SP]} = \text{Reg[SP]} - 4*k$$

```
addi $sp,$sp,-4*k
```

**DEALLOCATE**  $k$ : release  $k$  WORDS of stack

$$\text{Reg[SP]} = \text{Reg[SP]} + 4*k$$

```
addi $sp,$sp,4*k
```

**PUSH**  $rx$ : push  $\text{Reg}[x]$  onto stack

$$\text{Reg[SP]} = \text{Reg[SP]} - 4$$

$$\text{Mem}[\text{Reg[SP]}] = \text{Reg}[x]$$

```
addi $sp,$sp,-4  
sw $rx, 0($sp)
```

**POP**  $rx$ : pop the value on the top of the stack into  $\text{Reg}[x]$

$$\text{Reg}[x] = \text{Mem}[\text{Reg[SP]}]$$

$$\text{Reg[SP]} = \text{Reg[SP]} + 4;$$

```
lw $rx, 0($sp)  
addi $sp,$sp,4
```

## \* In case you forgot, a reminder of our problems

- We need a way to pass arguments into procedures
- Procedures need storage for their LOCAL variables
- Procedures need to call other procedures
- Procedures might call themselves (Recursion)

## \* But first: Let's “waste” some more registers:

- \$30 = \$fp (frame pointer)
  - points to the callee's local variables on the stack
  - we also use it to access extra args (>4)
- \$31 = \$ra (return address back to caller)
- \$29 = \$sp (stack pointer, points to TOP of stack)

## \* Now we can define a STACK FRAME

- a.k.a. the procedure's Activation Record

## \* What needs to be saved?

- CHOICE 1... anything that a Callee touches
  - except the return value registers
- CHOICE 2... Give the Callee access to everything
  - Caller saves those registers it expects to remain unchanged
- CHOICE 3... Something in between
  - Give the Callee some “scratch” registers to play with
    - If the Caller cares about these, it must preserve them
  - Give the Caller some registers that the Callee won’t clobber
    - If the Callee touches them, it must restore them

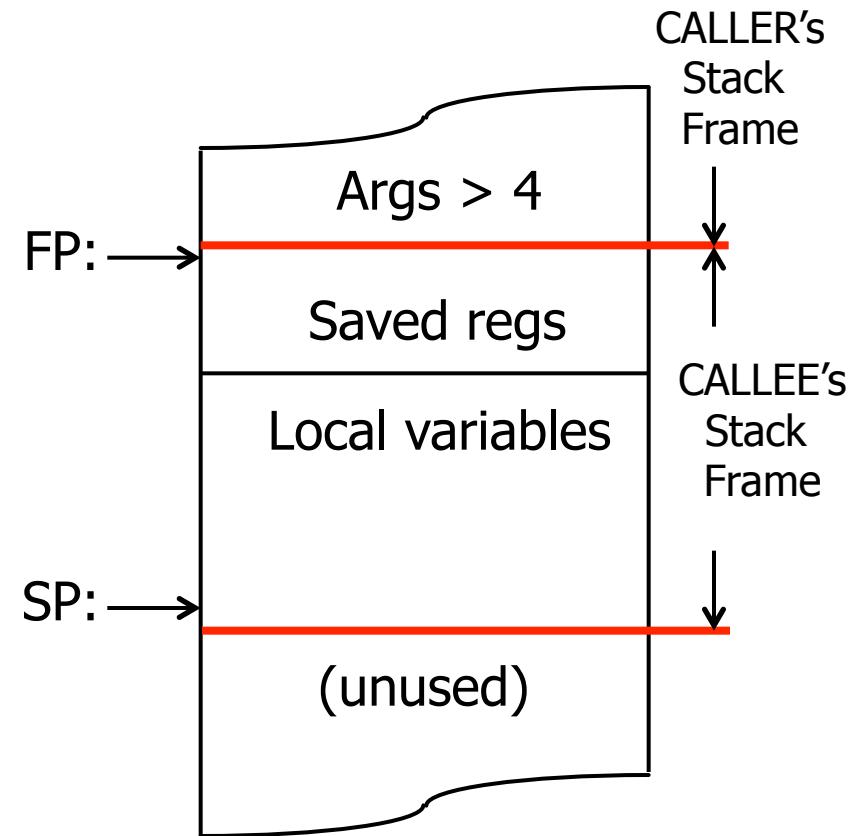
# Stack Frame Overview

The STACK FRAME contains storage for the CALLER's volatile state that it wants preserved after the invocation of CALLEEs.

In addition, the CALLEE will use the stack for the following:

- 1) Accessing the arguments that the CALLER passes to it (specifically, the 5<sup>th</sup> and greater)
- 2) Saving non-temporary registers that it wishes to modify
- 3) Accessing its own local variables

The boundary between stack frames falls at the first word of state saved by the CALLEE, and just after the extra arguments (>4, if used) passed in from the CALLER. The FRAME POINTER keeps track of this boundary between stack frames.



It is possible to use only the SP to access a stack frame, but offsets may change due to ALLOCATEs and DEALLOCATEs. For convenience a \$fp is used to provide CONSTANT offsets to local variables and arguments

# Procedure Stack Usage

ADDITIONAL space must be allocated in the stack frame for:

1. Any SAVED registers the procedure uses (\$s0-\$s7)
2. Any TEMPORARY registers that the procedure wants preserved IF it calls other procedures (\$t0-\$t9)
3. Any LOCAL variables declared within the procedure
4. Other TEMP space IF the procedure runs out of registers (RARE)
5. Enough “outgoing” arguments to satisfy the worse case **ARGUMENT SPILL** of ANY procedure it calls.  
(SPILL is the number of arguments greater than 4).



Each procedure has to keep track of how many SAVED and TEMPORARY registers are on the stack in order to calculate the offsets to LOCAL VARIABLES.

# More MIPS Register Usage

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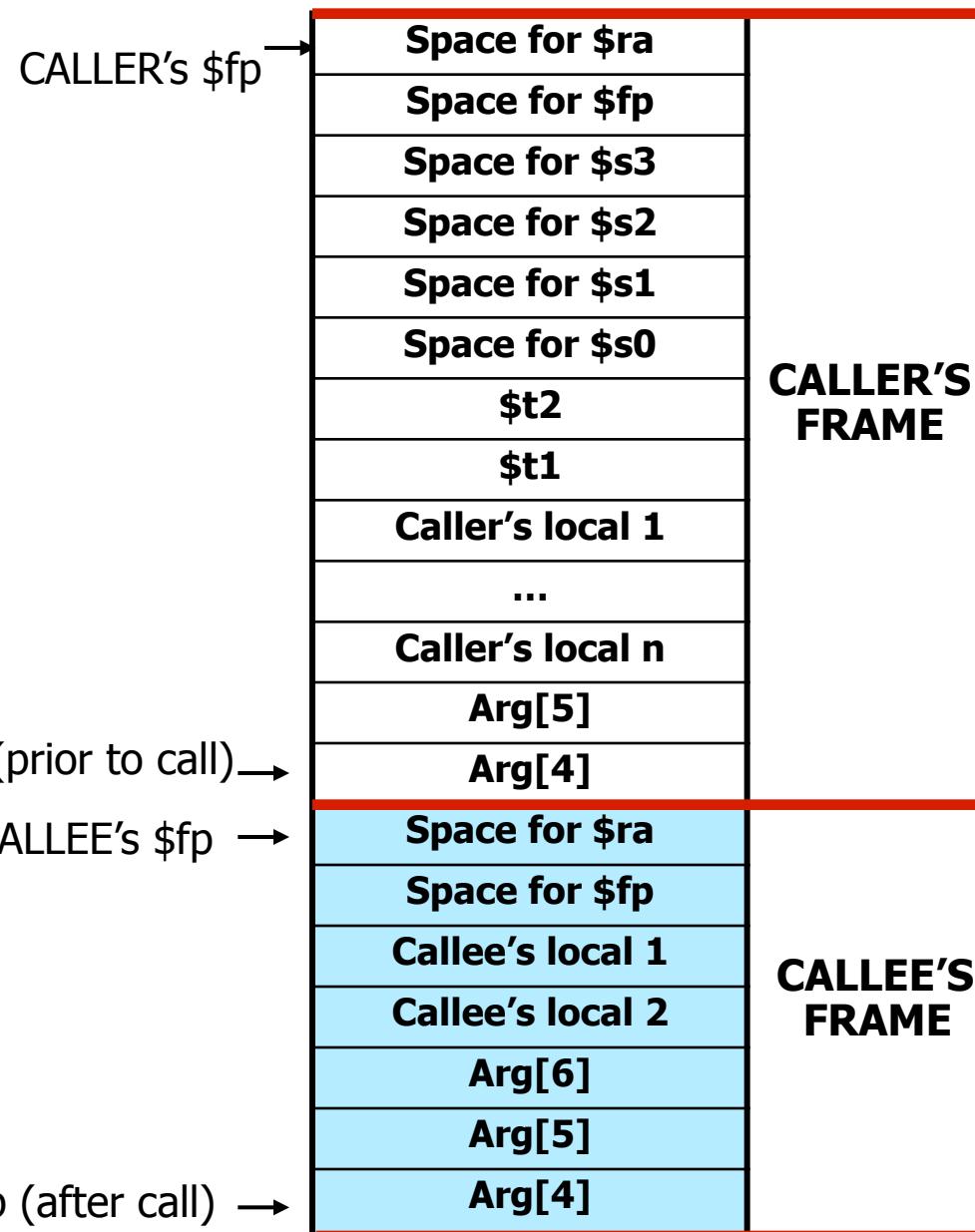
- The registers \$s0-\$s7, \$sp, \$ra, \$gp, \$fp, and the stack above the memory above the stack pointer must be preserved by the CALLEE
- The CALLEE is free to use \$t0-\$t9, \$a0-\$a3, and \$v0-\$v1, and the memory below the stack pointer.
- No “user” program can use \$k0-\$k1, or \$at

Name	Register number	Usage
\$zero	0	the constant value 0
\$at	1	assembler temporary
\$v0-\$v1	2-3	procedure return values
\$a0-\$a3	4-7	procedure arguments
\$t0-\$t7	8-15	temporaries
\$s0-\$s7	16-23	saved by callee
\$t8-\$t9	24-25	more temporaries
\$k0-\$k1	26-27	reserved for operating system
\$gp	28	global pointer
\$sp	29	stack pointer
\$fp	30	frame pointer
\$ra	31	return address

# Stack Snap Shots

\* Shown on the right is a snap shot of a program's stack contents

- Can you tell the number of CALLEE args?
  - NOPE!
- Can you tell the max number of args needed by any procedure called by CALLER?
  - Yes, 6
- Where in CALLEE's stack frame might one find CALLER's \$fp?
  - At -4(\$fp)



# Back to Reality

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\* Now let's make our example work, using the MIPS procedure linking and stack conventions.

```
int sqr(int x) {  
    if (x > 1)  
        x = sqr(x-1)+x+x-1;  
    return x;  
}
```

```
main()  
{  
    sqr(10);  
}
```



Q: Why didn't we save and update \$fp?

A: Don't have local variables or spilled args.

Save registers that must survive the call.

sqr:	addi	\$sp,\$sp,-8
	sw	\$ra,4(\$sp)
	sw	\$a0,0(\$sp)
	slti	\$t0,\$a0,2
	beq	\$t0,\$0,then
	add	\$v0,\$0,\$a0
	beq	\$0,\$0,rtn

then:

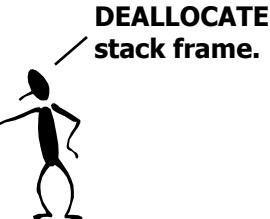
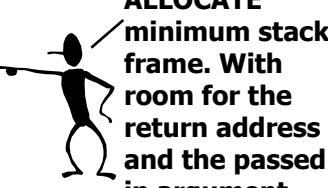
Pass arguments

	addi	\$a0,\$a0,-1
	jal	sqr
	lw	\$a0,0(\$sp)
	add	\$v0,\$v0,\$a0
	add	\$v0,\$v0,\$a0
	addi	\$v0,\$v0,-1

rtn:

Restore saved registers.

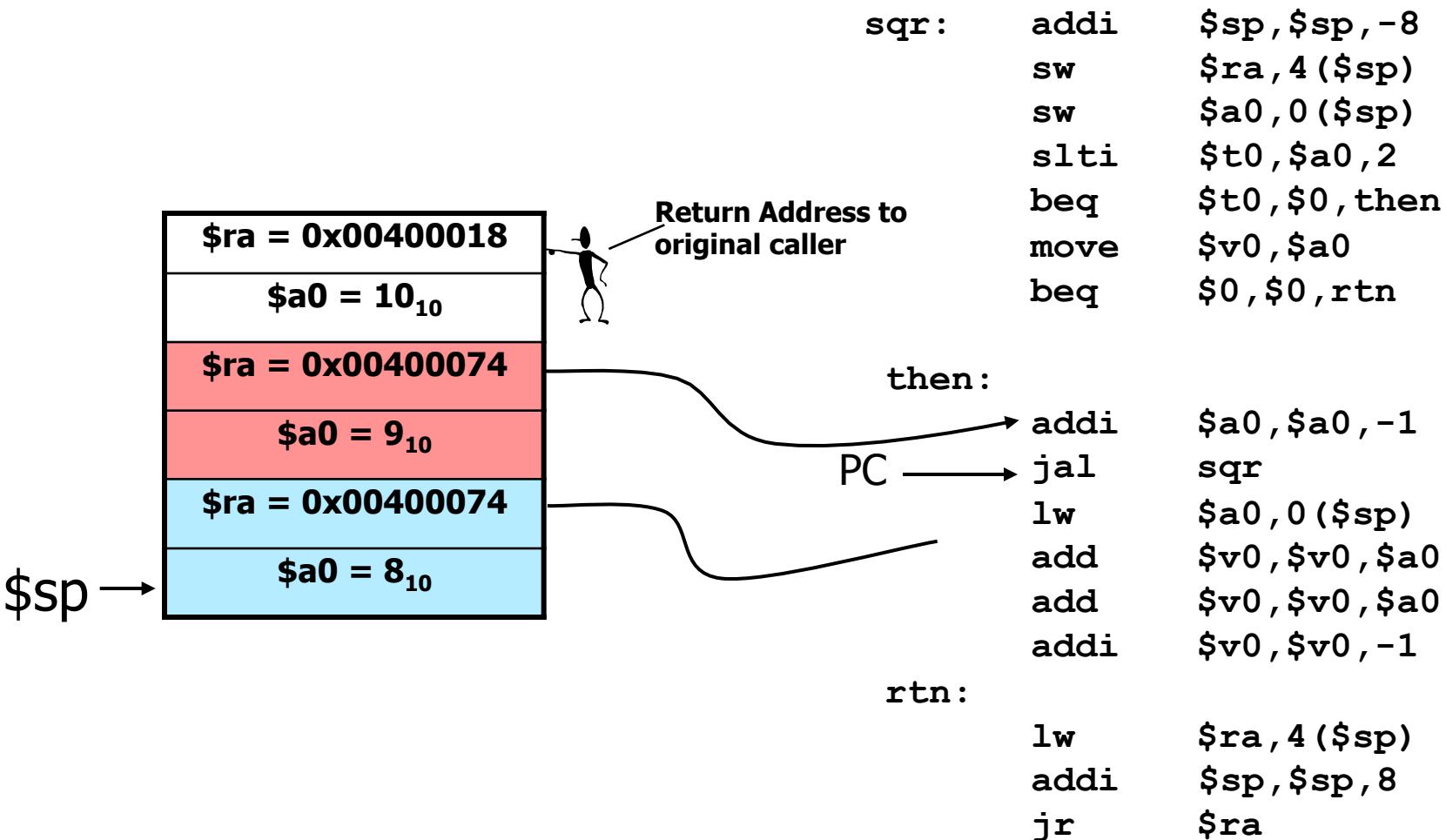
	lw	\$ra,4(\$sp)
	addi	\$sp,\$sp,8
	jr	\$ra



# Testing Reality's Boundaries

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- \* Now let's take a look at the active stack frames at some point during the procedure's execution.



# Procedure Linkage is Nontrivial

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\* The details can be overwhelming. How do we manage this complexity?

- Abstraction: High-level languages hide the details

\* There are great many implementation choices:

- which variables are saved
- who saves them
- where are arguments stored?

\* Solution: CONTRACTS!

- Caller and Callee must agree on the details

# Procedure Linkage: Caller Contract

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The CALLER will:

- Save all temp registers that it wants to survive subsequent calls in its stack frame  
**(t0-\$t9, \$a0-\$a3, and \$v0-\$v1)**
- Pass the first 4 arguments in registers \$a0-\$a3, and save subsequent arguments on stack, in **\*reverse\*** order. **Why?**
- Call procedure, using a `jal` instruction (places return address in \$ra).
- Access procedure's return values in \$v0-\$v1

# Code Lawyer

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Our running example is a CALLER.  
Let's make sure it obeys its contractual obligations

## Procedure Linkage: Caller Contract

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The CALLER will:

- Save all temp registers that it wants to survive subsequent calls in its stack frame  
*(t0-\$t9, \$a0-\$a3, and \$v0-\$v1)*
- Pass the first 4 arguments in registers \$a0-\$a3, and save subsequent arguments on stack, in \*reverse\* order. **Why?**
- Call procedure, using a `jal` instruction (places return address in \$ra).
- Access procedure's return values in \$v0-\$v1

```
int sqr(int x) {  
    if (x > 1)  
        x = sqr(x-1)+x+x-1;  
    return x;  
}
```



sqr:	addiu	\$sp,\$sp,-8
	sw	\$ra,4(\$sp)
	sw	\$a0,0(\$sp)
	slti	\$t0,\$a0,2
	beq	\$t0,\$0,then
	add	\$v0,\$0,\$a0
	beq	\$0,\$0,rtn
then:	addi	\$a0,\$a0,-1
	jal	sqr
	lw	\$a0,0(\$sp)
	add	\$v0,\$v0,\$a0
	add	\$v0,\$v0,\$a0
	addi	\$v0,\$v0,-1
rtn:	lw	\$ra,4(\$sp)
	addiu	\$sp,\$sp,8
	jr	\$ra

# Procedure Linkage: Callee Contract

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If needed the CALLEE will:

- 1) Allocate a stack frame with space for saved registers, local variables, and spilled args
- 2) Save any “preserved” registers used:  
(\$ra, \$sp, \$fp, \$gp, \$s0-\$s7)
- 3) If CALLEE has local variables -or- needs access to args on the stack, save CALLER’s frame pointer and set \$fp to 1<sup>st</sup> entry of CALLEE’s stack
- 4) EXECUTE procedure
- 5) Place return values in \$v0-\$v1
- 6) Restore saved registers
- 7) Fix \$sp to its original value
- 8) Return to CALLER with jr \$ra

# More Legalese

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Our running example is also a CALLEE. Are these contractual obligations satisfied?

## Procedure Linkage: Callee Contract

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If needed the CALLEE will:

- 1) Allocate a stack frame with space for saved registers, local variables, and spilled args
- 2) Save any “preserved” registers used: (\$ra, \$sp, \$fp, \$gp, \$s0-\$s7)
- 3) If CALLEE has local variables -or- needs access to args on the stack, save CALLER’s frame pointer and set \$fp to 1<sup>st</sup> entry of CALLEE’s stack
- 4) EXECUTE procedure
- 5) Place return values in \$v0-\$v1
- 6) Restore saved registers
- 7) Fix \$sp to its original value
- 8) Return to CALLER with jr \$ra

```
int sqr(int x) {  
    if (x > 1)  
        x = sqr(x-1)+x+x-1;  
    return x;  
}
```



sqr:

addiu	\$sp,\$sp,-8
sw	\$ra,4(\$sp)
sw	\$a0,0(\$sp)
slti	\$t0,\$a0,2
beq	\$t0,\$0,then
add	\$v0,\$0,\$a0
beq	\$0,\$0,rtn

then:

addi	\$a0,\$a0,-1
jal	sqr
lw	\$a0,0(\$sp)
add	\$v0,\$v0,\$a0
add	\$v0,\$v0,\$a0
addi	\$v0,\$v0,-1

rtn:

lw	\$ra,4(\$sp)
addiu	\$sp,\$sp,8
jr	\$ra

# Conclusions

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- \* Need a convention (contract) between caller and callee
- \* Implement stack for storing each procedure's variables
- \* Procedure calls can now be arbitrarily nested
  - Recursion possible too
- \* FOLLOW the convention meticulously!