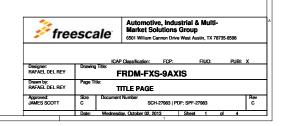
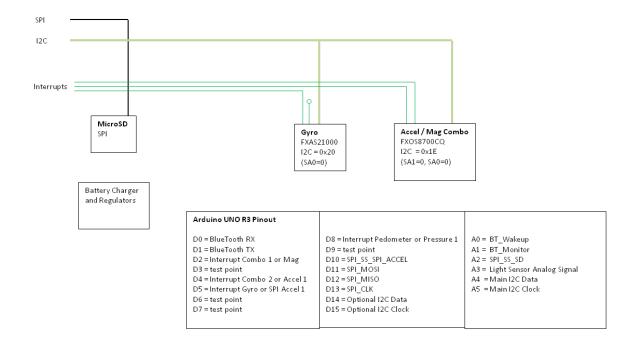
5	4		3	2		11		
Table of Contents						Revisions &	Change	e Log
1 Title						Description	Date	Approved
2 Block Diagram								J. SCOTT
3 Sensors						27909 Prototype Release		J. SCOTT
3 Power Supply, Battery, uSD						Under Development	07/16/13	J. SCOTT
					AX1	27982 (BT Version) PPL & A070 RELEASE	07/22/13	J. SCOTT
					AX2	27982 (BT Version) Block Diagram Update	08/07/13	J. SCOTT
					В		08/23/13	J. SCOTT
					BX1	27982 (UNDER DEVELOPMENT) I2C swap fix	09/25/13	J. SCOTT
					С		10/02/13	J. SCOTT
-								
c								

FRDM-FXS-MULTI



- Unless Otherwise Specified:
 All resistors are in ohms, 5%, 1/8 Watt
 All capacitors are in uF, 20%, 50V
 All voltages are DC
 - All polarized capacitors are aluminum electrolytic
- Interrupted lines coded with the same letter or letter combinations are electrically connected.
- 3. Device type number is for reference only. The number varies with the manufacturer.
- 4. Special signal usage:
 - _B Denotes Active-Low Signal
 - <> or [] Denotes Vectored Signals
- 5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.

FRDM-FXS-MULTI Block Diagram



Freescale •

FRDM-FXS-9AXIS
BLOCK DIAGRAM

SCH-27983 | PDF: SPF-27983

