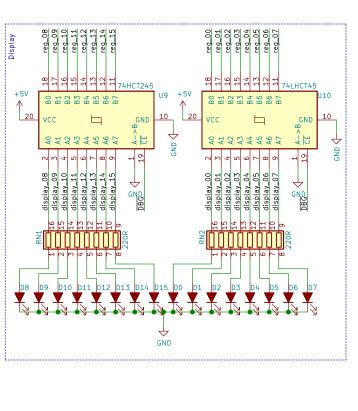


Interconnect

PWR\_FLAG



VCC

 $\rightarrow$ 

VCC

87 86 85 84 83 82 81 80 80

Does not use CLK signal for operation

Sheet: /

0.1uF

0.1uF

0.1uF

0.1uF

C12

C13

+|(-

File: GPRModule.sch

Title: Genera	l Purpose Register
Size: USLedger	Date: 2020-12-25

Rev: 1.3 KiCad E.D.A. eeschema (5.1.8)-1 ld: 1/1