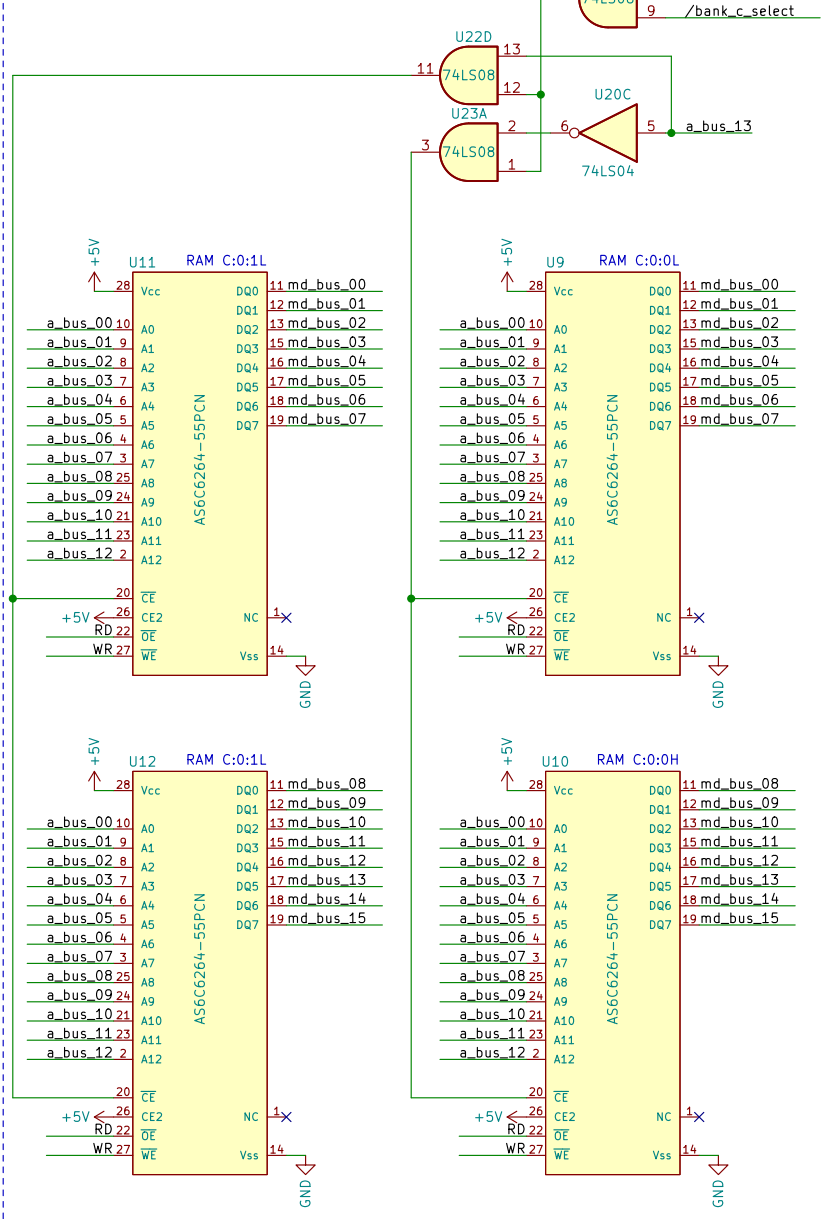
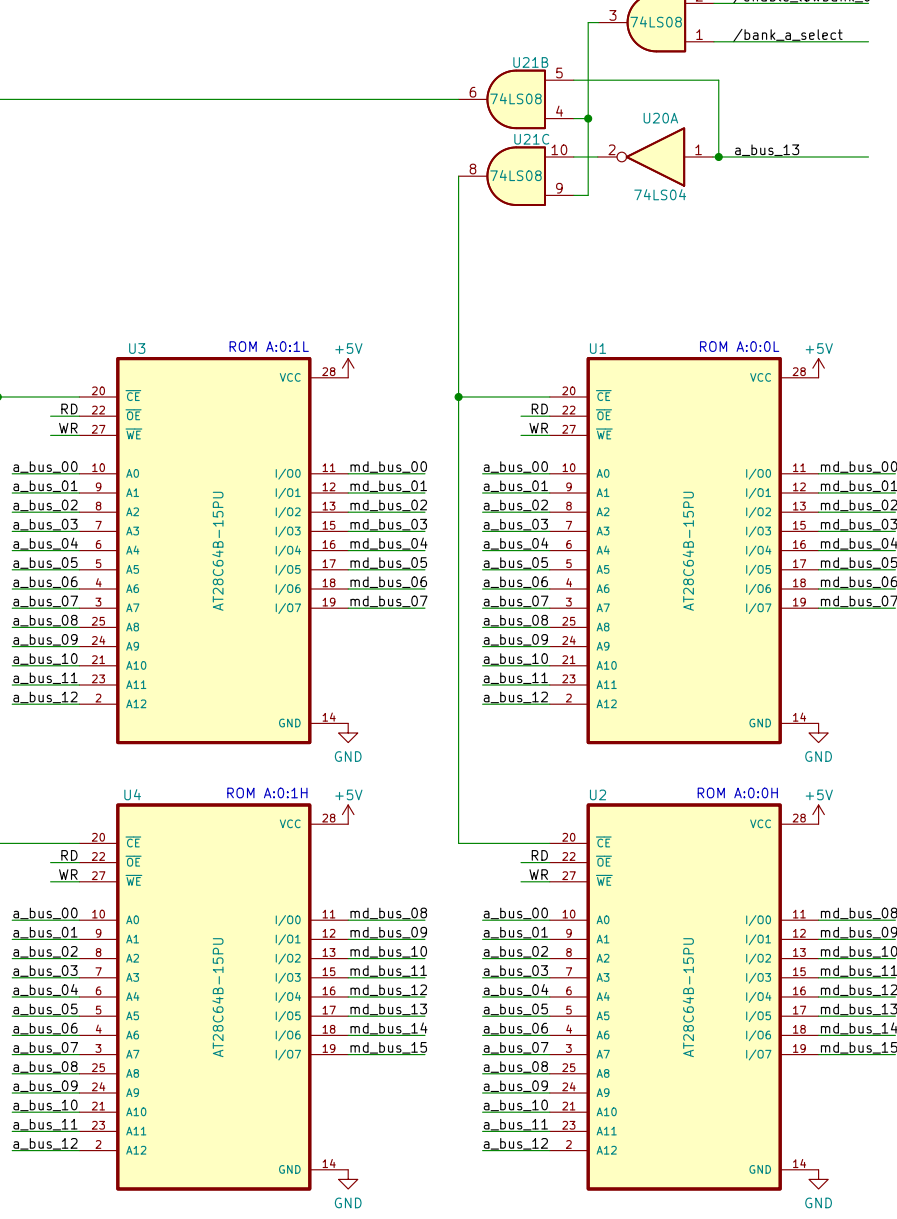


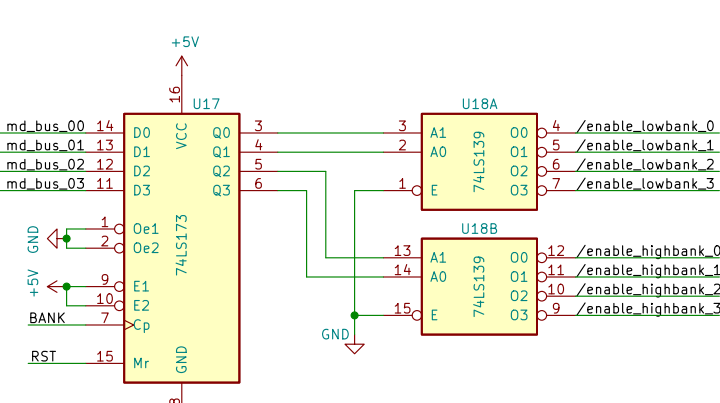
RAM Bank C:0 (16 KB)



ROM Bank A:0 (16 Kb)



Bank Switching Decoding



The 64 Kb address space is split into four 16 Kb blocks (A, B, C, D). Blocks are further grouped into 32 Kb groups: the low block group (A & B) and high block group (C & D).

Four banks are available to be selected for blocks A, C and D. Block B is static and can not be changed, that is where the stack lives. The value set in the bank register selects the banking configuration.

Configurations are denoted by block:bank where blocks are A, B, C, D and banks are 1, 2, 3, 4. This defines which bank is assigned to which block.

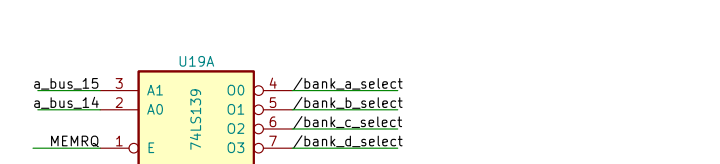
Low Block Bank Configuration (bit 0 & 1)

- 00 - C:0 B:0
- 01 - A:1 B:0
- 10 - A:2 B:0
- 11 - A:3 B:0

High Block Bank Configuration (bit 2 & 3)

- 00 - B:0 B:1
- 01 - C:1 B:1
- 10 - C:2 B:2
- 11 - C:3 B:3

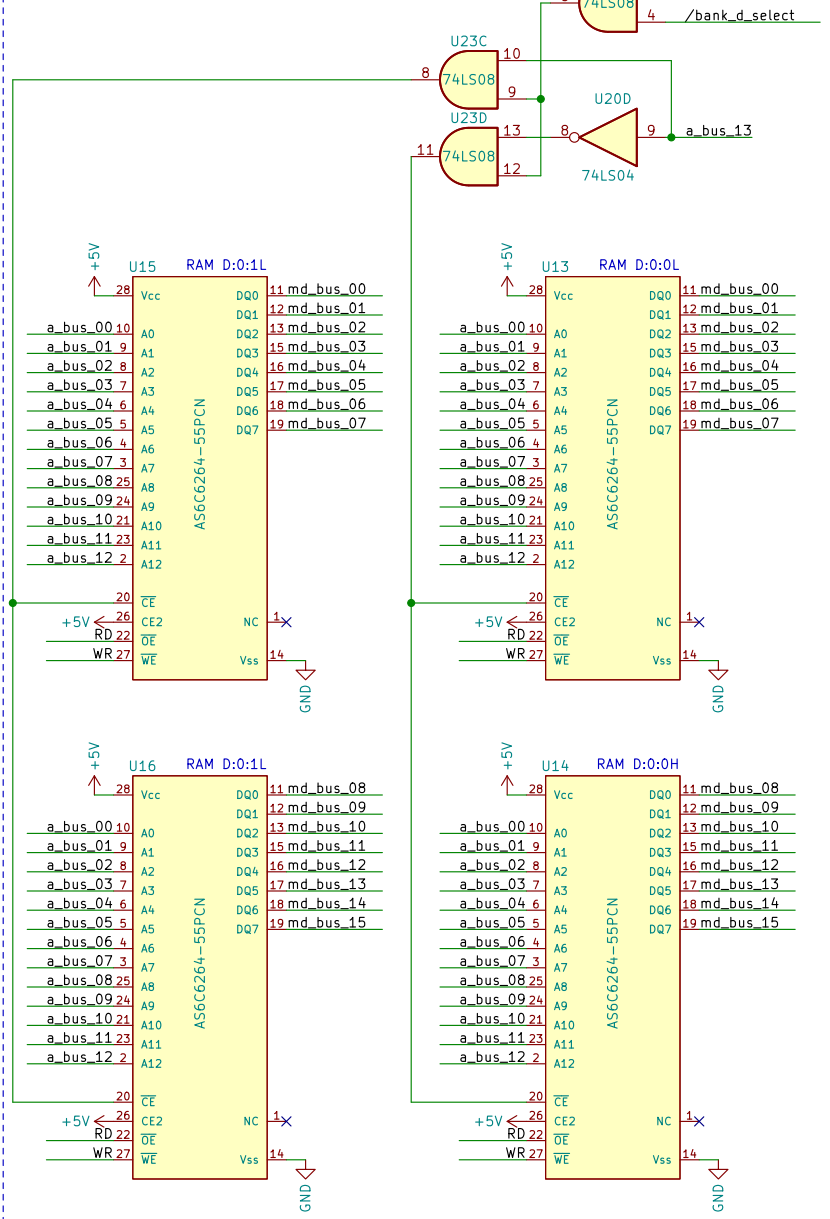
Bank Selection Decoding



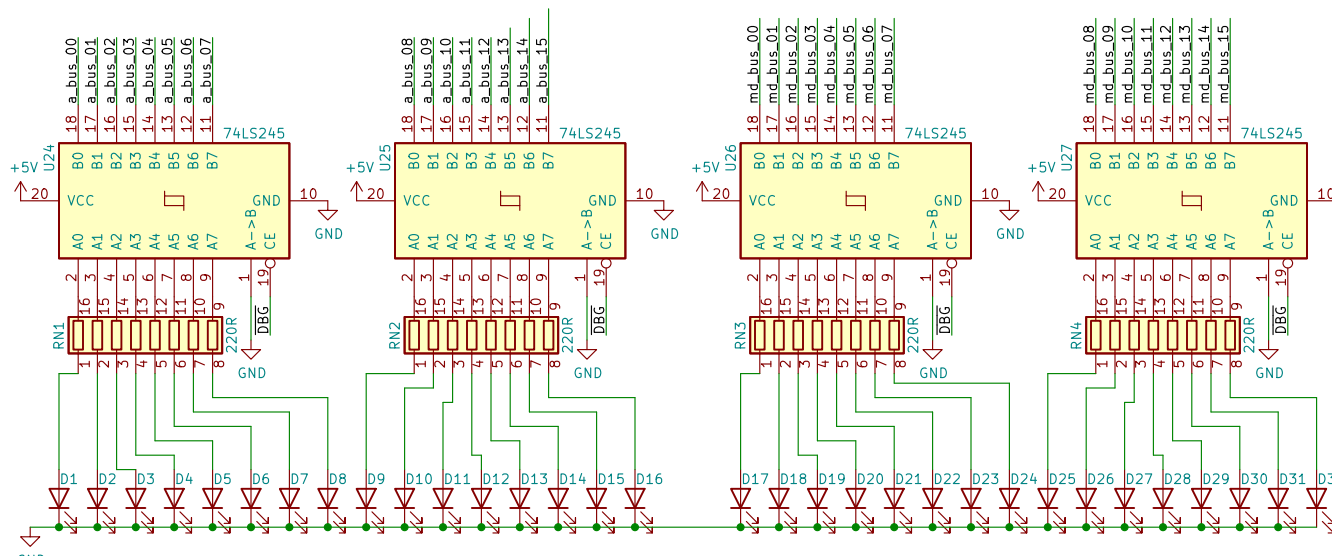
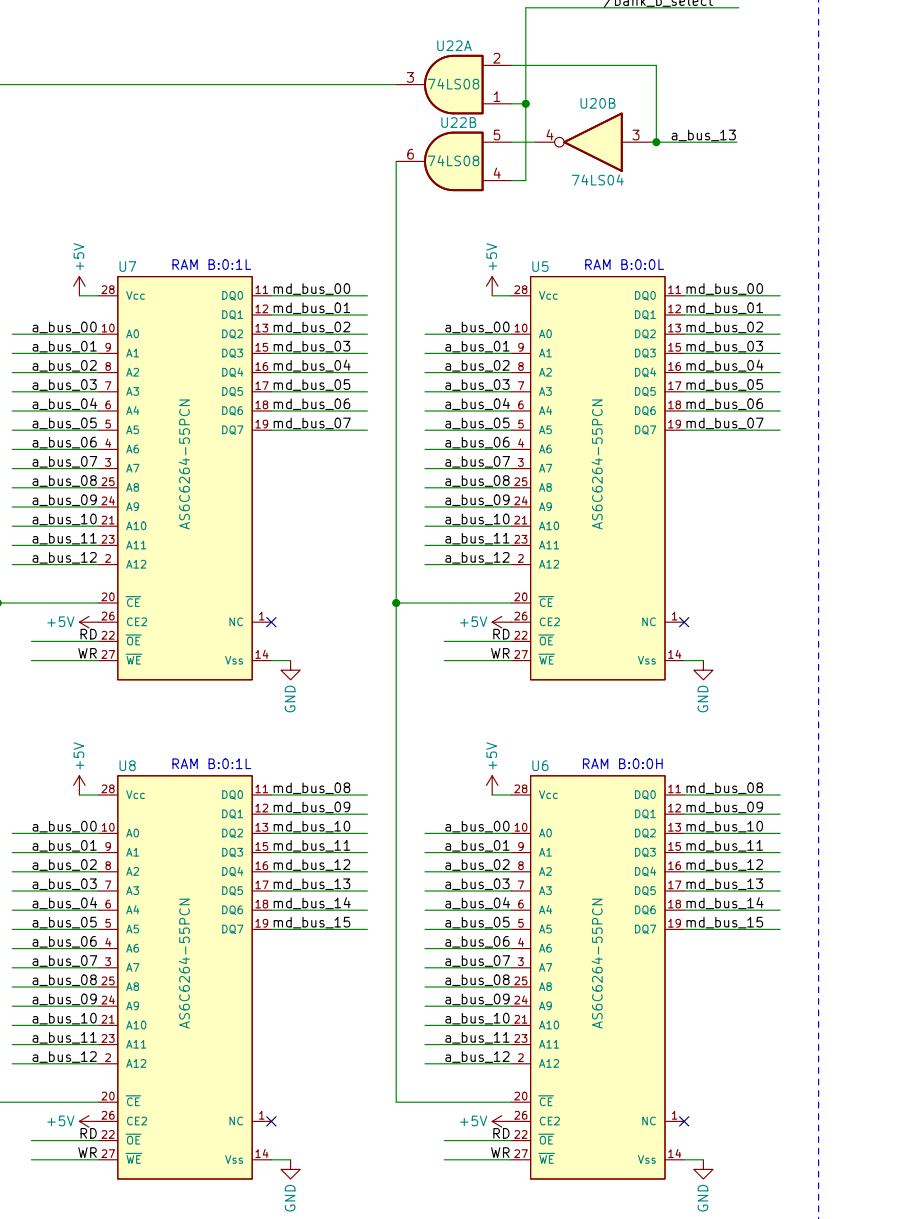
The top two address lines (A14, A15) select which 16 Kb bank is active.

- 00 - Bank A
- 01 - Bank B
- 10 - Bank C
- 11 - Bank D

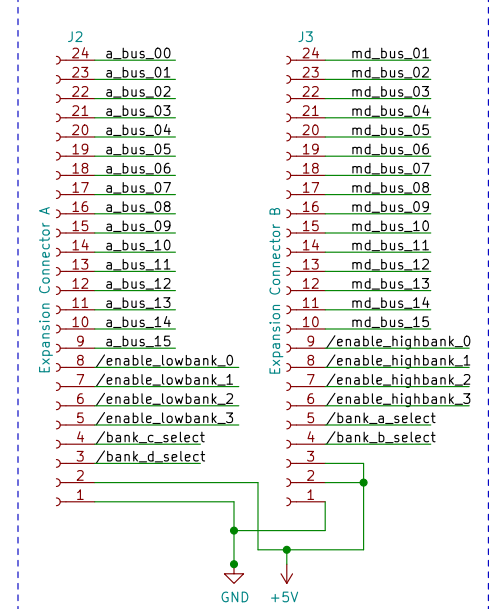
RAM Bank D:0 (16 KB)



RAM Bank B:0 (16 KB)



Expansion Connectors



TODD:
* Validate RAM footprints are ok

