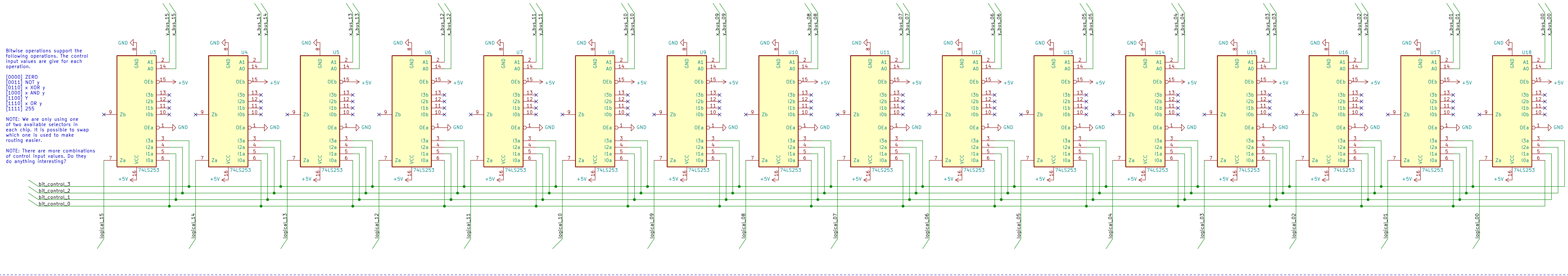
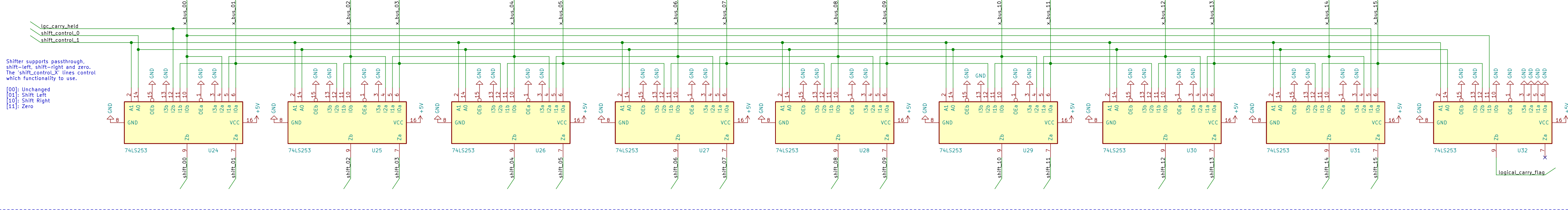


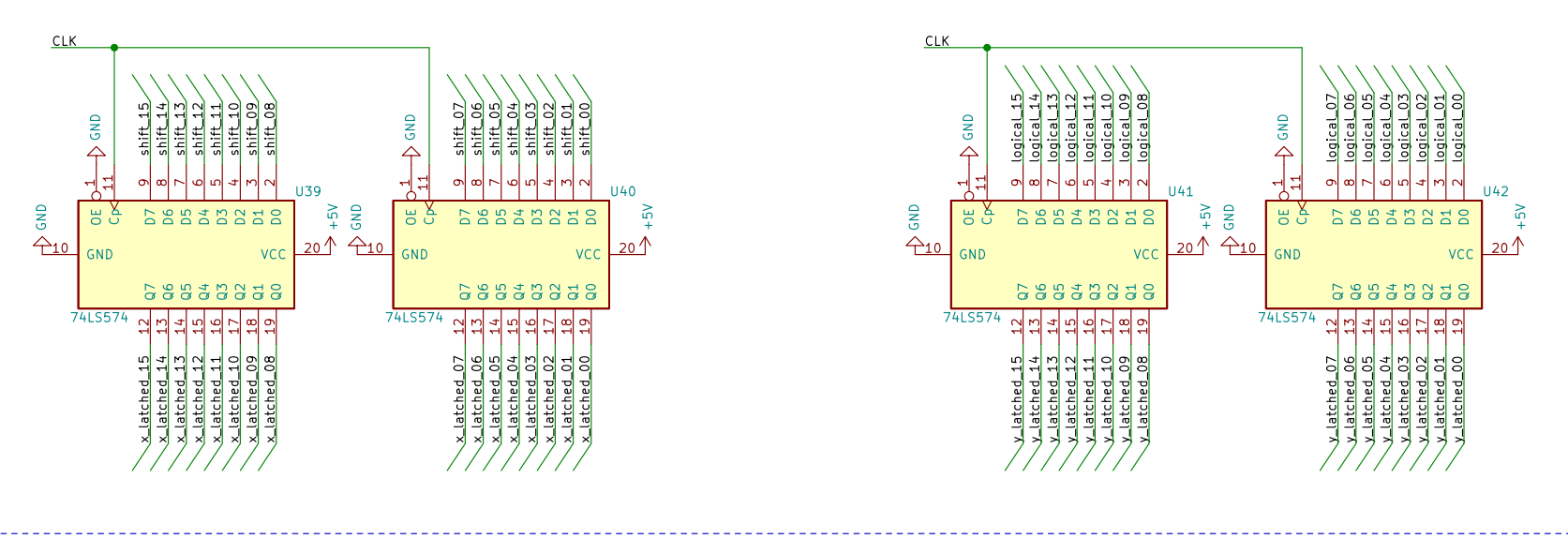
Bitwise Operations



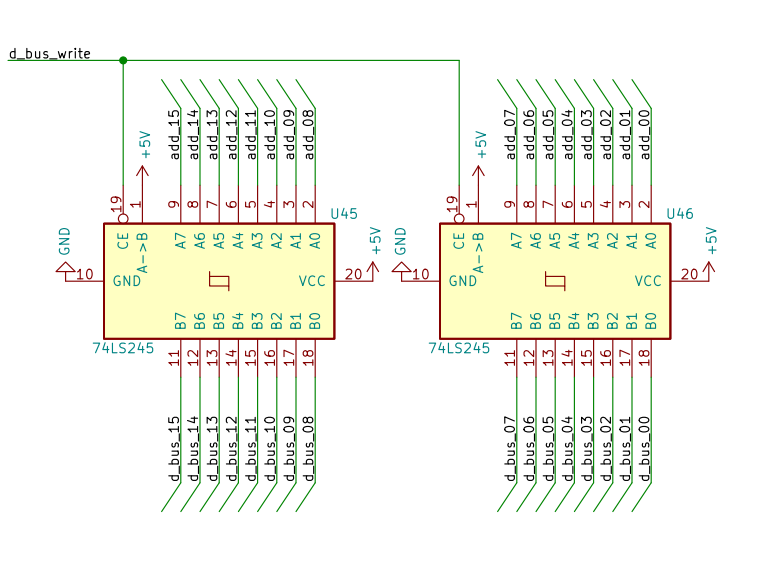
Shift Operations



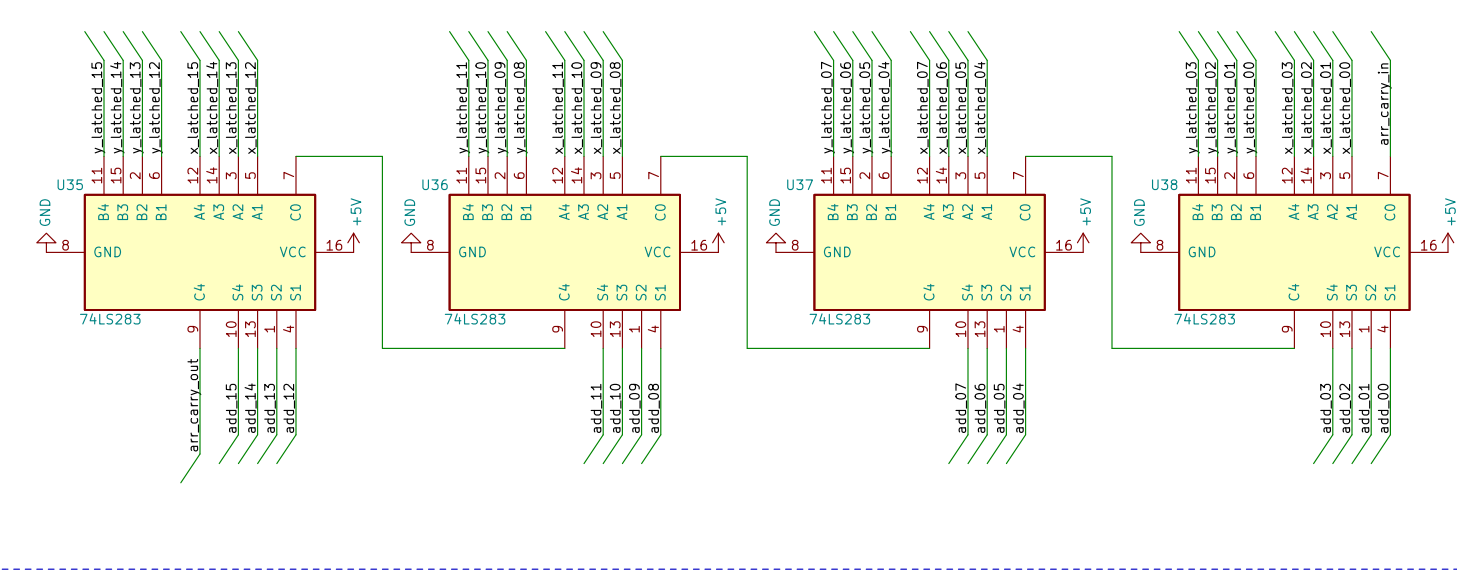
Intermediate Latches



D\_BUS Buffer



Adder



Flags

