

BIGTREE TECH



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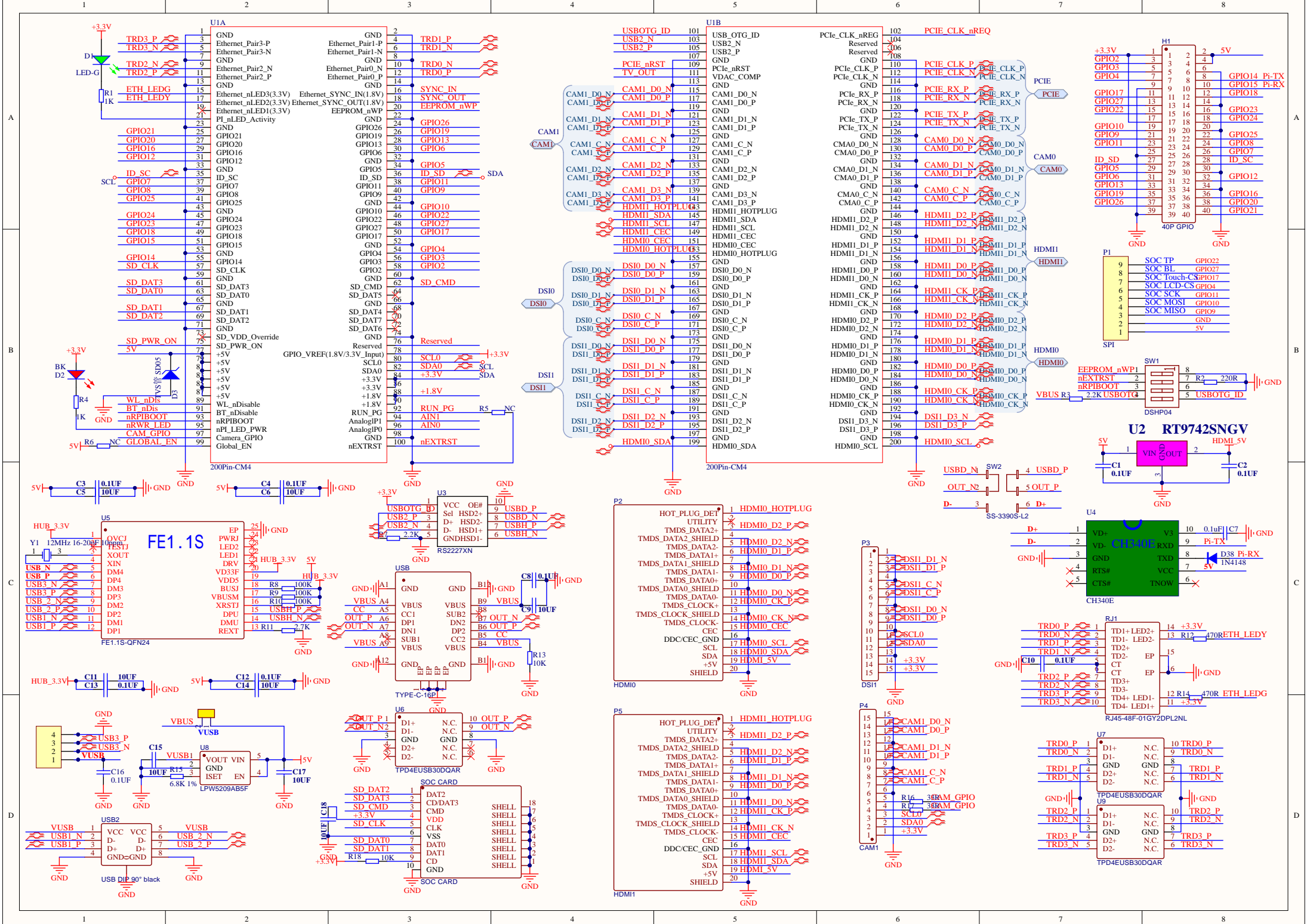
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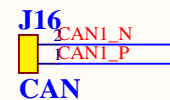
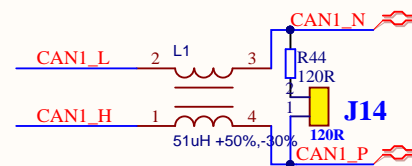
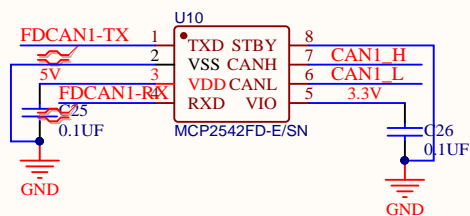
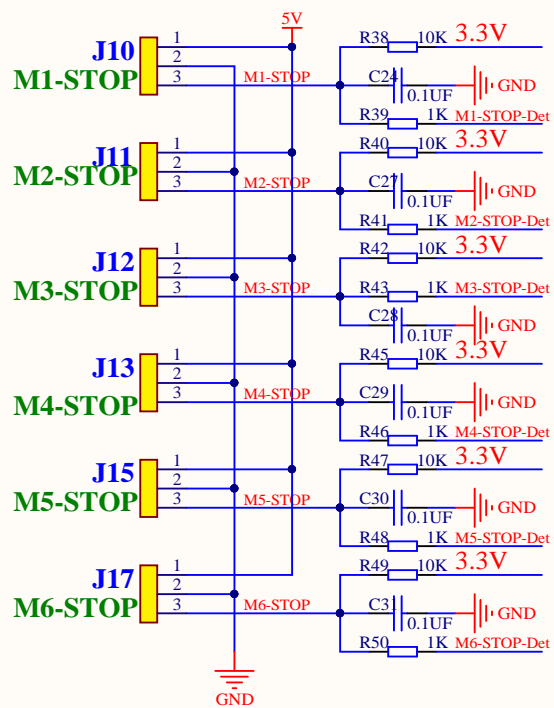
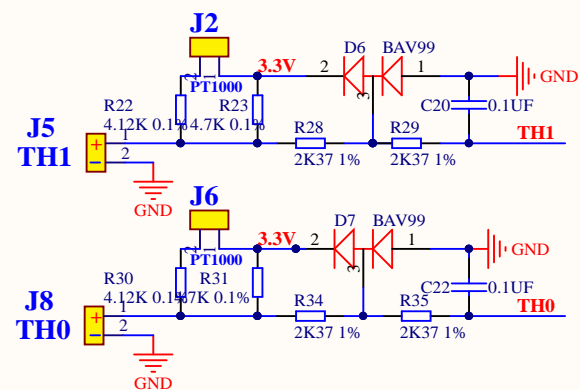
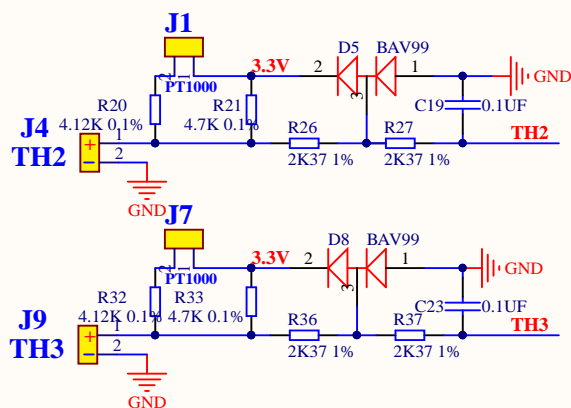
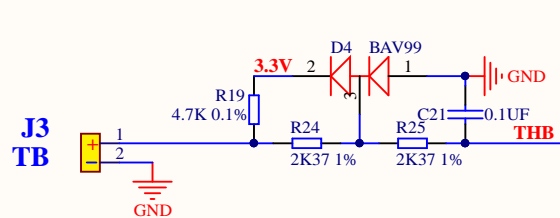
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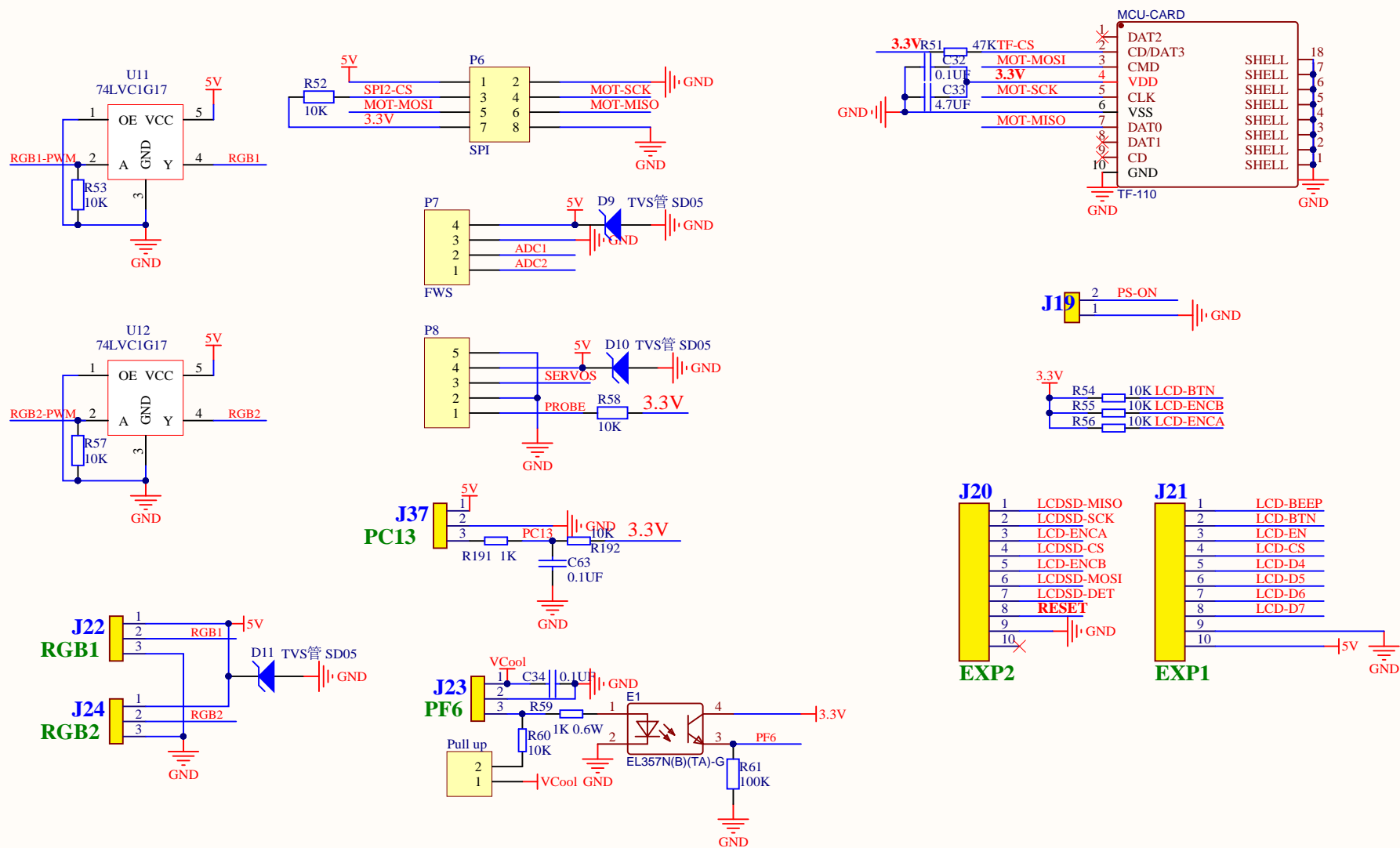
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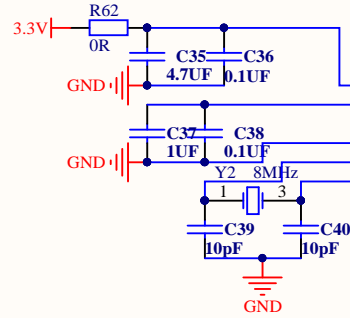
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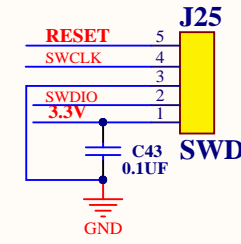
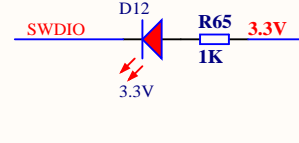
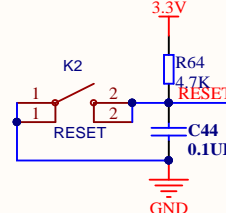
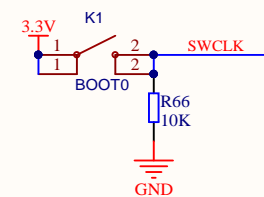
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STM32G0B1VET6



Cool_PWM	1	PB9/TIM17_CH1,FDCAN1_TX, USART3_RX, SPI2_NSS,I2C1_SDA	100	FAN5_PWM
M1-CS	2	PC10/USART3_TX, USART4_TX, TIM1_CH3, SPI3_SCK	99	HB_PWM
M1-EN	3	PC11/USART3_RX, USART4_RX, TIM1_CH4, SPI3_MISO	98	HE2_PWM
FAN4_PWM	4	PE4/TIM3_CH2	97	HE0_PWM
FAN3_PWM	5	PE5/TIM3_CH3	96	M1-STP
FAN0_PWM	6	PE6/TIM3_CH4	95	HE3_PWM
FAN2_PWM	7	PC12/TIM14_CH1, USART5_TX, SPI3_MOSI	94	FAN1_PWM
PC13	8	PC13/TIM1_BKIN	93	HE1_PWM
FAN5-DET	9	PC14/OSC32_IN/TIM1_BKIN2	92	M1-DIR
Cool-DET	10	PC15/OSC32_OUT(EN)/TIM15_BKIN	91	M2-EN
VDD	11	VBAT	90	M2-CS
VDD	12	VREF+	89	M2-STP
	13	VDD/VDDA	88	M2-DIR
	14	VSS/VSSA	87	M3-EN
	15	PF0/OSC_IN/TIM14_CH1	86	M3-CS
RESET	16	PF1/OSC_OUT/TIM15_CH1N	85	M3-STP
M1-STOP-Det	17	PF2/NRST/LPUART2_TX, LPUART2_RTS_DE	84	M3-DIR
M2-STOP-Det	18	PF3/LPUART2_RX, USART6_RTS_DE_CK	83	M4-EN
M3-STOP-Det	19	PF4/LPUART1_TX	82	M4-CS
M4-STOP-Det	20	PF5/LPUART1_RX	81	M4-STP
M5-STOP-Det	21	PC0/LPUART1_RX, LPUART2_TX, USART6_TX, I2C3_SCL	80	M4-DIR
M6-STOP-Det	22	PC1/LPUART1_TX, TIM15_CH1, LPUART2_RX, USART6_RX, I2C3_SDA	79	M5-EN
PS-ON	23	PC2/SPI2_MISO/I2S2_MCK, TIM15_CH2, FDCAN2_RX	78	M5-CS
THB	24	PC3/SPI2_MOSI/I2S2_SD, LPTIM2_ETR, FDCAN2_TX	77	M5-STP
	25	PA0/SPI2_SCK/I2S2_CK,TIM2_CH1_ETR, USART4_TX,ADC_IN0	76	M5-DIR
TH0	26	PA1/SPI1_SCK/I2S1_CK,TIM2_CH2, USART4_RX, TIM15_CH1N,ADC_IN1	75	M6-EN
TH1	27	PA2/SPI1_MOSI/I2S1_SD, USART2_TX, TIM2_CH3,TIM15_CH1,ADC_IN2	74	SWCLK
TH2	28	PA3/SPI2_MISO/I2S2_MCK, USART2_RX, TIM2_CH4,TIM15_CH2,ADC_IN3	73	SWDIO
TH3	29	PA4/SPI1_NSS/I2S1_WS, SPI2_MOSI/USART6_TX, TIM14_CH1,ADC123_IN4	72	M6-CS
MOT-SCK	30	PA5/SPI1_SCK/I2S1_CK,USART6_RX, USART3_TX,ADC12_IN5	71	USB P
MOT-MISO	31	PA6/SPI1_MISO/TIM3_CH1,TIM16_CH1,I2C2_SDA,I2C3_SDA/ADC_IN6	70	USB N
MOT-MOSI	32	PA7/SPI1_MOSI/TIM3_CH2,I2C2_SCL,I2C3_SCL/ADC_IN7	69	M6-STP
SPI2-CS	33	PC4/USART3_TX, USART1_TX, TIM2_CH1_ETR, FDCAN1_RX/ADC_IN17	68	M7-EN
ADC1	34	PC5/USART3_RX, USART1_RX, TIM2_CH2, FDCAN1_TX/ADC_IN18	67	M7-CS
ADC2	35	PB0/SPI1_NSS,TIM3_CH3,FDCAN2_RX,USART5_TX/ADC_IN8	66	FDCAN1-TX
SERVOs	36	PB1/TIM14_CH1,FDCAN2_TX,USART5_RX/ADC_IN9	65	FDCAN1-RX
PROBE	37	PB2/SPI2_MISO/I2S2_MCK,USART3_TX/ADC_IN10	64	VDDIO
PF6	38	PF6/LPUART1_RTS_DE	63	VSS
LCD-ENCA	39	PF7/LPUART1_CTS, USART5_CTS	62	M7-STP
LCD-ENCB	40	PE7/TIM1_ETR, USART5_RTS_DE_CK	61	M8-CS
LCDSD-DET	41	PE8/USART4_TX, TIM1_CH1N	60	M7-DIR
LCD-BEEP	42	PE9/USART4_RX, TIM1_CH1	59	M8-STP
LCD-BTN	43	PE10/TIM1_CH2N, USART5_TX	58	M8-EN
LCD-EN	44	PE11/TIM1_CH2, USART5_RX	57	M8-DIR
LCD-CS	45	PE12/SPI1_NSS/I2S1_WS, TIM1_CH3N	56	RGB1-PWM
LCD-D4	46	PE13/SPI1_SCK/I2S1_CK, TIM1_CH3	55	TF-CS
LCD-D5	47	PE14/SPI1_MISO/I2S1_MCK, TIM1_CH4, TIM1_BKIN2	54	RGB2-PWM
LCD-D6	48	PE15/SPI1_MOSI/I2S1_SD, TIM1_BKIN	53	LCDSD-MISO
LCD-D7	49	PB10/TIM2_CH3, USART3_TX, SPI2_SCK/I2S2_CK, I2C2_SCL,ADC_IN11	52	LCDSD-SCK
LCDSD-MOSI	50	PB11/SPI2_MOSI,TIM2_CH4, USART3_RX, I2C2_SDA,ADC_IN15	51	LCDSD-CS

USART1_RX, SPI2_MOSI,I2C1_SDA,TIM4_CH2/PB7	99	FAN5_PWM
USART1_TX, TIM1_CH3,FDCAN2_TX, SPI2_MISO,I2C1_SCL,TIM4_CH1/PB6	98	HB_PWM
TIM3_CH1/PE3	97	HE2_PWM
TIM3_ETR/PE2	96	HE0_PWM
TIM17_CH1/PE1	95	M1-STP
TIM16_CH1/PE0	94	HE3_PWM
SPI1_MOSI,TIM3_CH2,FDCAN2_RX, SPI3_MOSI/PB5	93	FAN1_PWM
SPI1_MISO,TIM3_CH1, USART5_RX,I2C3_SDA,I2C2_SDA, SPI3_MISO/PB4	92	HE1_PWM
SPI1_SCK,TIM1_CH2, TIM2_CH2, USART5_TX,I2C3_SCL,I2C2_SCL, SPI3_SCK/PB3	91	M1-DIR
TIM15_CH2/PI13	90	M2-EN
TIM15_CH1/PI12	89	M2-CS
PF11	88	M2-STP
USART6_RX/PI10	87	M2-DIR
USART6_TX/PI9	86	M3-EN
MCO2/PD7	85	M3-CS
USART2_RX, SPI1_MOSI/PD6	84	M3-STP
USART2_TX, SPI1_MISO/PD5	83	M4-EN
SPI2_MOSI/I2S2_SD, TIM1_CH3N/PD4	82	M4-CS
SPI2_MISO/I2S2_MCK, TIM1_CH2N, USART5_TX/PD3	81	M4-STP
TIM1_CH1N, USART5_RX/PD2	80	M4-DIR
SPI2_SCK/I2S2_CK, TIM17_CH1, FDCAN1_TX/PD1	79	M5-EN
SPI2_NSS, TIM16_CH1, FDCAN1_RX/PD0	78	M5-CS
TIM3_CH4, TIM1_CH2/PC9	77	M5-STP
UCPD2_FRSTX, TIM3_CH3, TIM1_CH1/PC8	76	M5-DIR
SPI1_NSS,USART2_RX,SPI3_NSS/PA15	75	M6-EN
USART2_TX,SWCLK,BOOT0/PA14	74	SWCLK
SWDIO/PA13	73	SWDIO
PF8	72	M6-CS
SPI1_MOSI/FDCAN1_TX,I2C2_SDA,USB_DP/PA12	71	USB P
SPI1_MISO,TIM1_CH4, FDCAN1_RX,I2C2_SCL,USB_DM/PA11	70	USB N
SPI2_MOSI/USART1_RX, TIM1_CH3,I2C1_SDA,I2C2_SDA/PA10	69	M6-STP
TIM4_CH4, FDCAN2_TX/PD15	68	M7-EN
TIM4_CH3, FDCAN2_RX/PD14	67	M7-CS
TIM4_CH2, FDCAN1_TX/PD13	66	FDCAN1-TX
LPTIM2_IN1, TIM4_CH1, FDCAN1_RX/PD12	65	FDCAN1-RX
VDDIO2	64	VDDIO
VSS	63	VSS
USART3_CTS, LPTIM2_ETR/PD11	62	M7-STP
MCO/PD10	61	M8-CS
USART3_RX, SPI1_NSS/I2S1_WS/PD9	60	M7-DIR
USART3_TX, SPI1_SCK/I2S1_CK/PD8	59	M8-STP
TIM3_CH2, TIM2_CH4, LPUART2_RX/PC7	58	M8-EN
TIM3_CH1, TIM2_CH3, LPUART2_TX/PC6	57	M8-DIR
USART1_TX, TIM1_CH2, SPI2_MISO,I2C1_SCL,I2C2_SCL/PA9	56	RGB1-PWM
SPI2_NSS/I2S2_WS, TIM1_CH1/PA8	55	TF-CS
SPI2_MOSI/TIM15_CH1, I2C2_SDA/PB15	54	RGB2-PWM
SPI2_MISO/TIM15_CH1, I2C2_SDA/PB14	53	LCDSD-MISO
SPI2_SCK,TIM1_CH1N, FDCAN2_TX,I2C2_SCL/PB13	52	LCDSD-SCK
SPI2_NSS/FDCAN2_RX,ADC_IN16/PB12	51	LCDSD-CS



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