

USB Virtual COM Port

mit USB-to-UART Chip

This circuit diagram shows a USB-to-UART chip (U3: MCP2200-I/SS) connected to a USB interface (J1) and a UART interface (SWD_TX, SWD_RX). The chip is powered by a +5V supply (USB_5V) and a +3V3 supply. The USB interface includes a USB connector (J1) with pins for VBUS, D+, D-, GND, and SHIELD1*3. The UART interface includes a USB connector (J2) with pins for VBUS, D+, D-, GND, and SHIELD1*3. The chip is connected to the USB interface via a USB-to-UART chip (U3: MCP2200-I/SS). The chip is connected to the UART interface via a USB-to-UART chip (U3: MCP2200-I/SS). The chip is connected to the UART interface via a USB-to-UART chip (U3: MCP2200-I/SS).

mit USB des STM

This circuit diagram shows a USB interface (J2) connected to a USB-to-UART chip (U4: TPD3E001DRLR). The chip is powered by a +5V supply (USB_5V) and a +3V3 supply. The USB interface includes a USB connector (J2) with pins for VBUS, D+, D-, GND, and SHIELD1*3. The UART interface includes a USB connector (J2) with pins for VBUS, D+, D-, GND, and SHIELD1*3. The chip is connected to the USB interface via a USB-to-UART chip (U4: TPD3E001DRLR). The chip is connected to the UART interface via a USB-to-UART chip (U4: TPD3E001DRLR). The chip is connected to the UART interface via a USB-to-UART chip (U4: TPD3E001DRLR).

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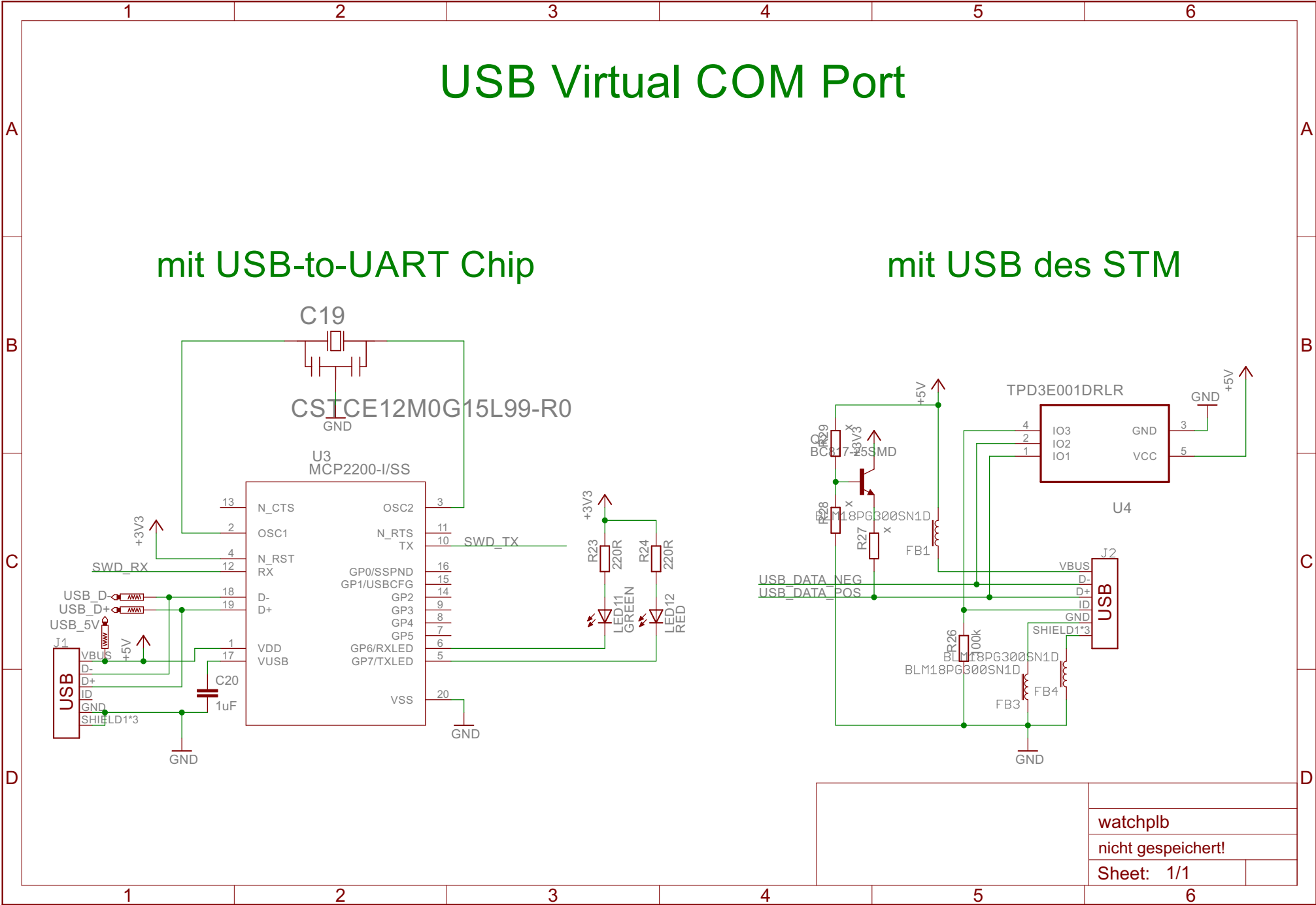
mit USB-to-UART Chip

This circuit diagram shows the connection of a USB-to-UART chip (U3: MCP2200-I/SS) to a USB connector (J1) and an SWD connector (J2). The chip is powered by +3V3 and +5V. It has two status LEDs: a green LED (LED11) and a red LED (LED12). The SWD interface is connected to the chip's N_CTS, N_RTS, N_RST, and TX pins. The USB connector provides VBUS, D+, D-, and GND connections. A 1uF capacitor (C20) is connected between VBUS and GND. A 15L99-R0 component (C19) is connected between the +3V3 supply and GND.

mit USB des STM

This circuit diagram shows the connection of the STM32's built-in USB interface to a USB connector (J2). The STM32 is powered by +3V3 and +5V. The USB connector provides VBUS, D+, D-, and GND connections. The STM32's USB pins (IO1, IO2, IO3, IO4) are connected to the USB connector. The STM32's SWD interface is connected to the SWD connector (J2). A 15L99-R0 component (C19) is connected between the +3V3 supply and GND. The STM32's status LEDs (LED11 GREEN and LED12 RED) are connected to the +3V3 supply and GND.

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USB Virtual COM Port

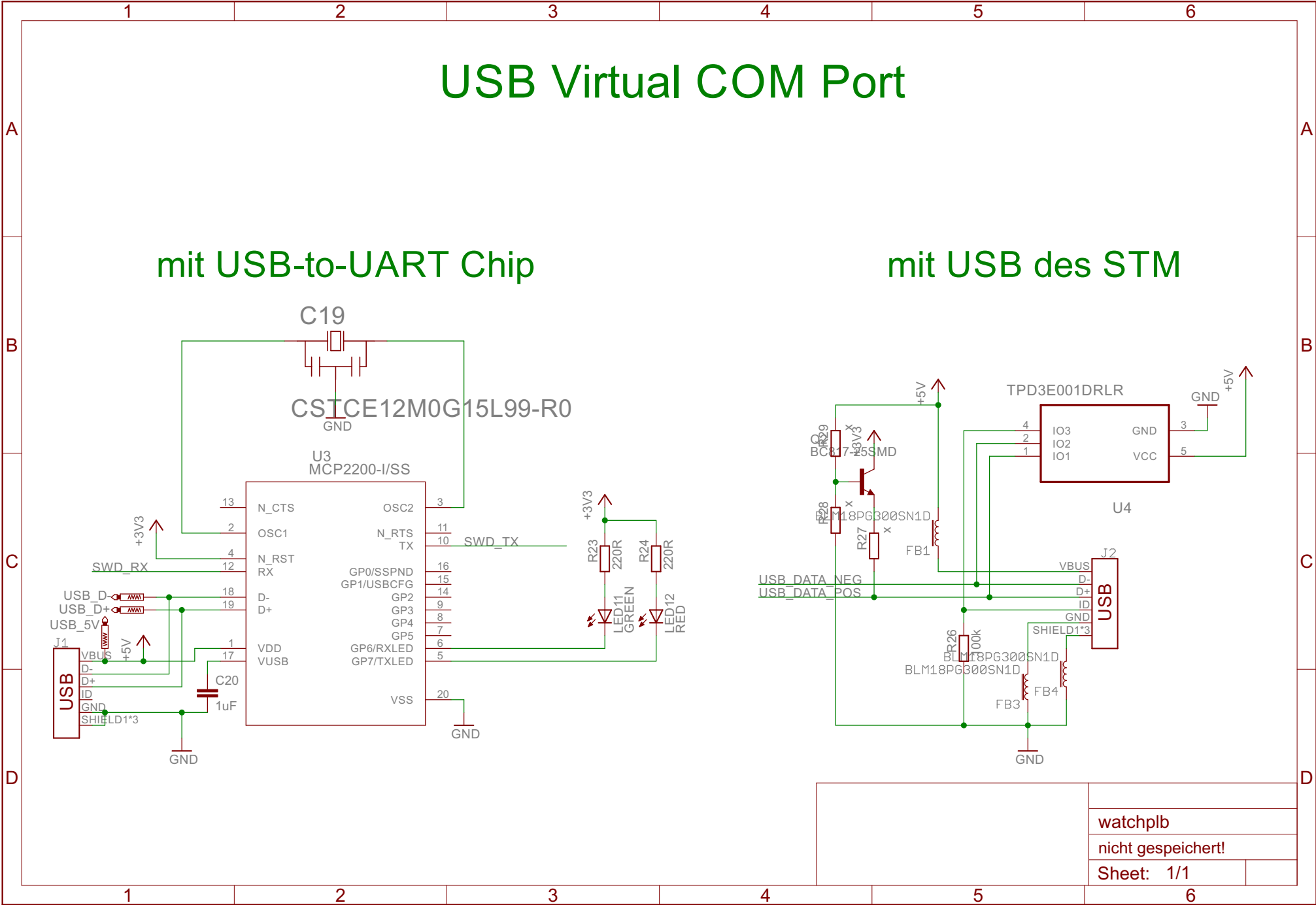
mit USB-to-UART Chip

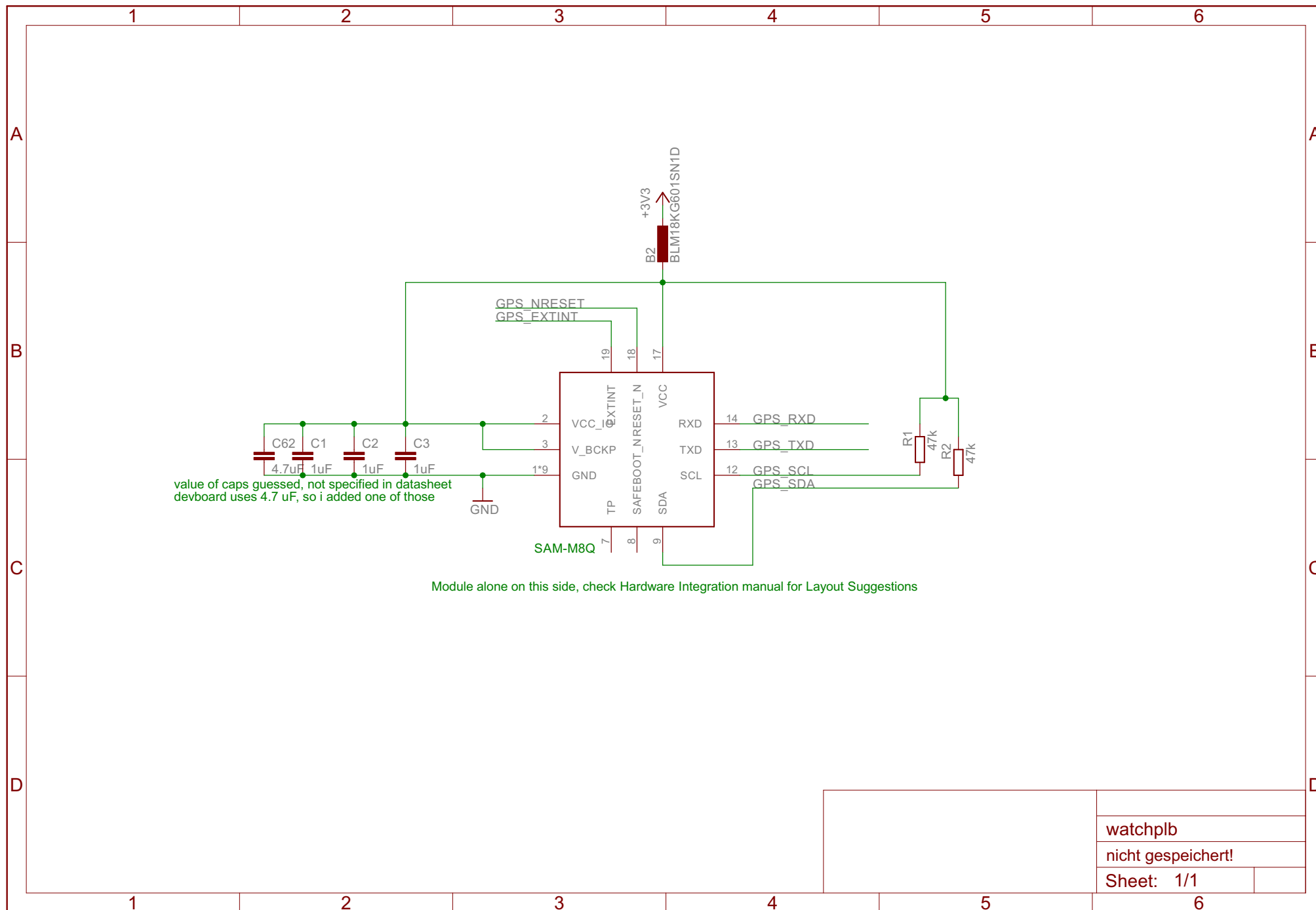
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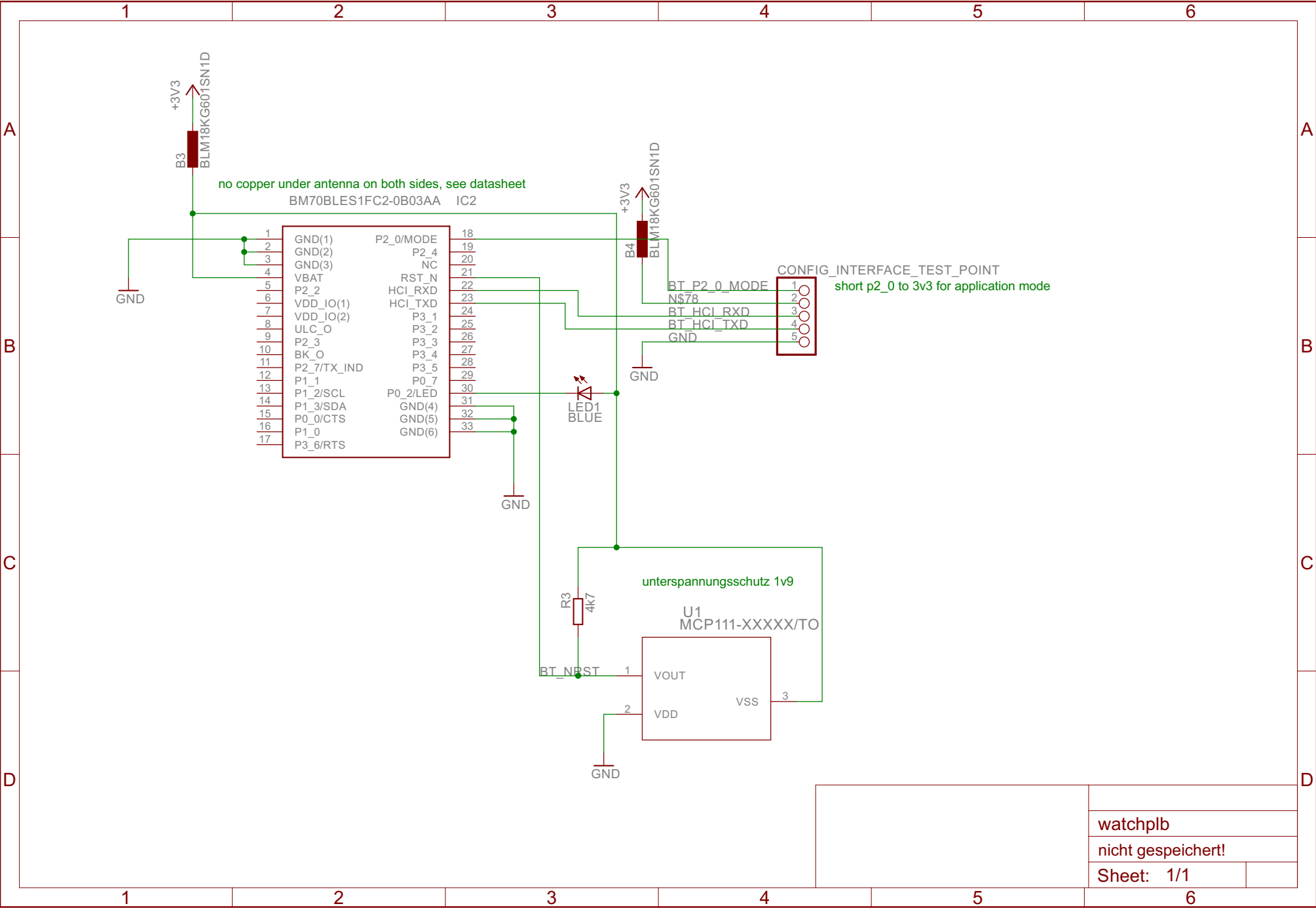
mit USB des STM

This circuit diagram shows the connection of the STM32's built-in USB interface to a USB connector (J2). The STM32 is powered by +3V3 and +5V. The USB connector provides VBUS, D+, D-, and GND connections. The STM32's USB pins (IO1, IO2, IO3, IO4) are connected to the USB connector. The STM32's SWD interface is connected to the SWD connector (J2). A 15L99-R0 component (C19) is connected between the +3V3 supply and GND. The STM32's status LEDs (LED11 GREEN and LED12 RED) are connected to the +3V3 supply and GND.

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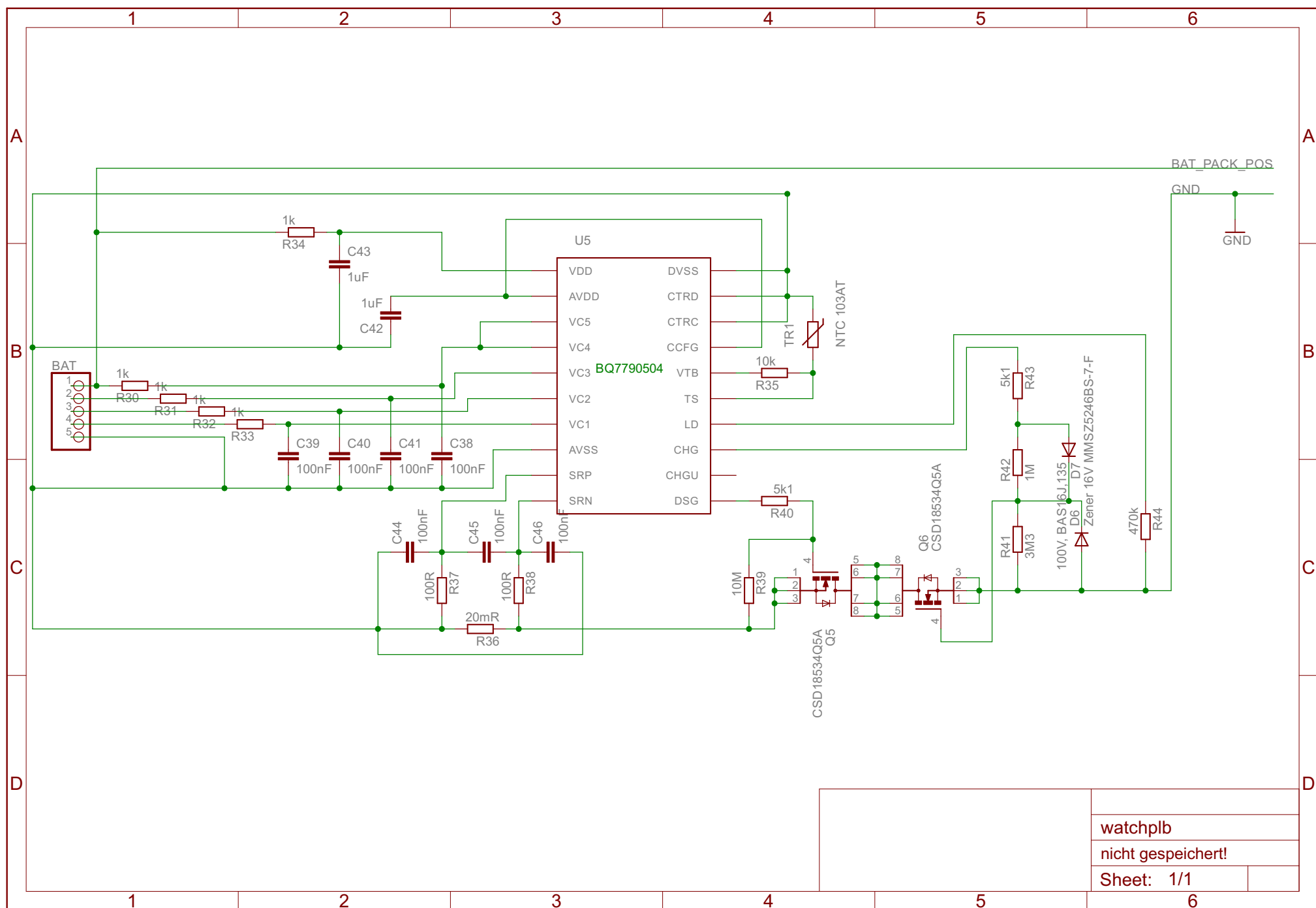




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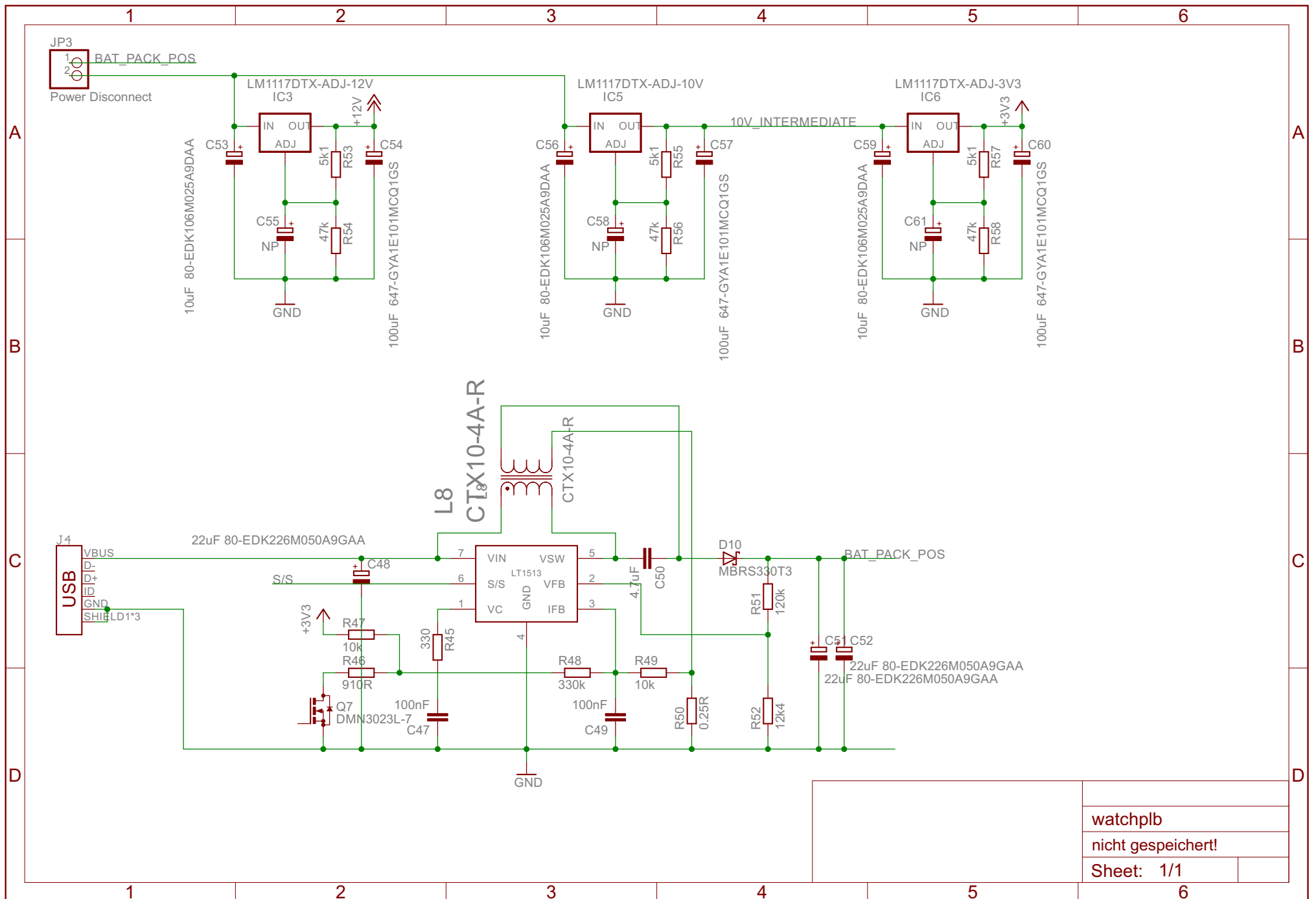
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