

USB Virtual COM Port

mit USB-to-UART Chip

This circuit diagram shows a USB-to-UART chip (U3: MCP2200-I/SS) connected to a USB port (J1) and a UART interface. The USB port provides VBUS (+5V), D-, D+, ID, GND, and SHIELD1*3. The chip's VDD and VUSB pins are connected to +5V, while VSS is connected to GND. A 1uF capacitor (C20) is connected between VDD and GND. The chip's N_CTS, OSC1, N_RST, RX, D-, and D+ pins are connected to the USB lines. The chip's OSC2, N_RTS, TX, GP0/SSPND, GP1/USBCFG, GP2, GP3, GP4, GP5, GP6/RXLED, and GP7/TXLED pins are connected to the UART interface. The TX and RX lines are connected to the SWD_TX and SWD_RX pins, respectively. The TX and RX lines are also connected to two LEDs (LED11 GREEN and LED12 RED) through 220R resistors (R23 and R24). A 3V3 supply is connected to the TX and RX lines. A crystal (C19) is connected to the OSC1 and OSC2 pins. The chip's GP0/SSPND pin is connected to the TX line, and the GP1/USBCFG pin is connected to the RX line.

mit USB des STM

This circuit diagram shows the STM32 USB peripheral connected to a USB port (J2) and a UART interface. The USB port provides VBUS (+5V), D-, D+, ID, GND, and SHIELD1*3. The STM32's VBUS, D-, D+, ID, and GND pins are connected to the USB lines. The STM32's SHIELD1*3 pin is connected to GND. The STM32's TX and RX pins are connected to the UART interface. The TX and RX lines are connected to two LEDs (LED11 GREEN and LED12 RED) through 220R resistors (R23 and R24). A 3V3 supply is connected to the TX and RX lines. A crystal (C19) is connected to the OSC1 and OSC2 pins. The chip's GP0/SSPND pin is connected to the TX line, and the GP1/USBCFG pin is connected to the RX line.

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USB Virtual COM Port

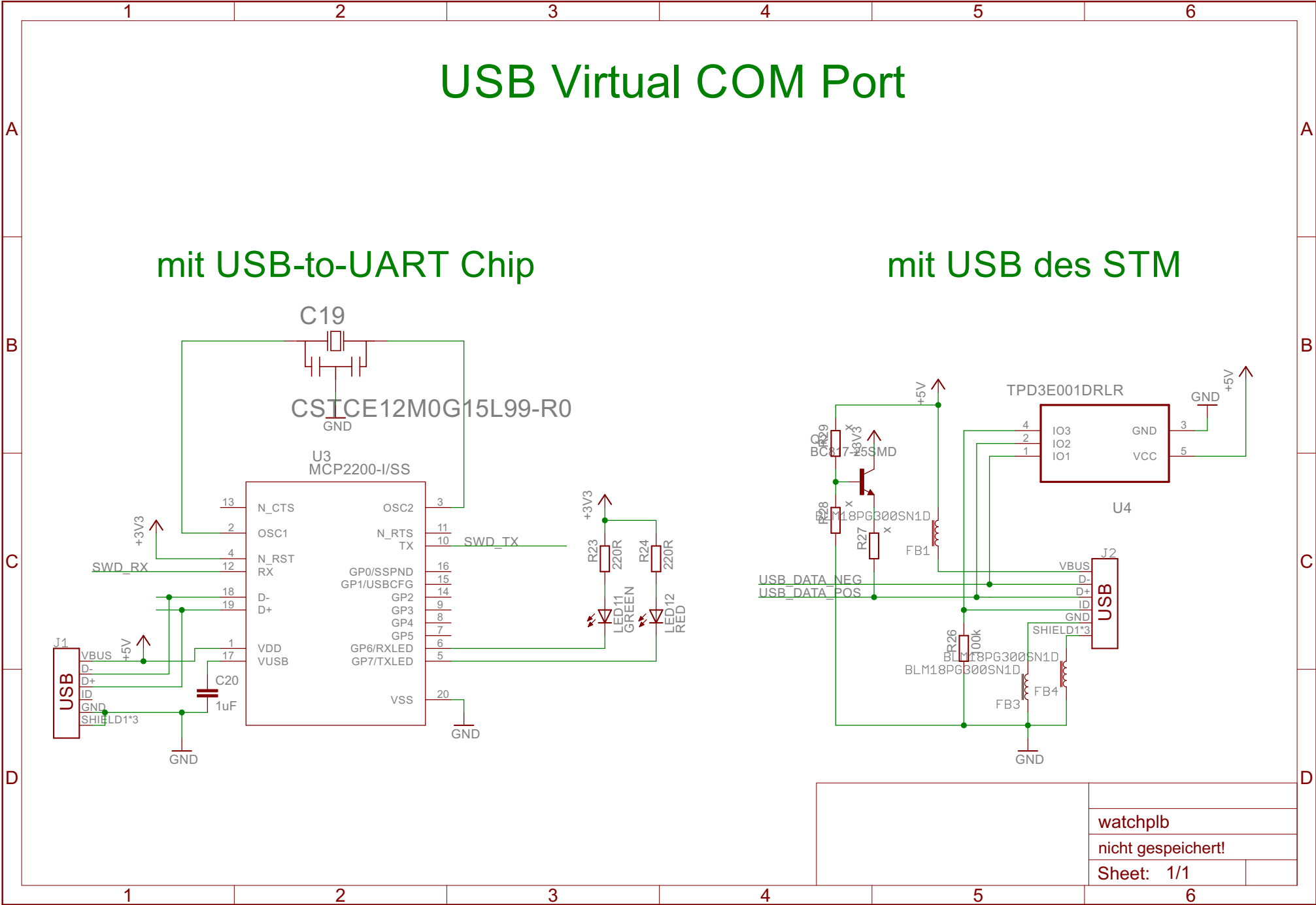
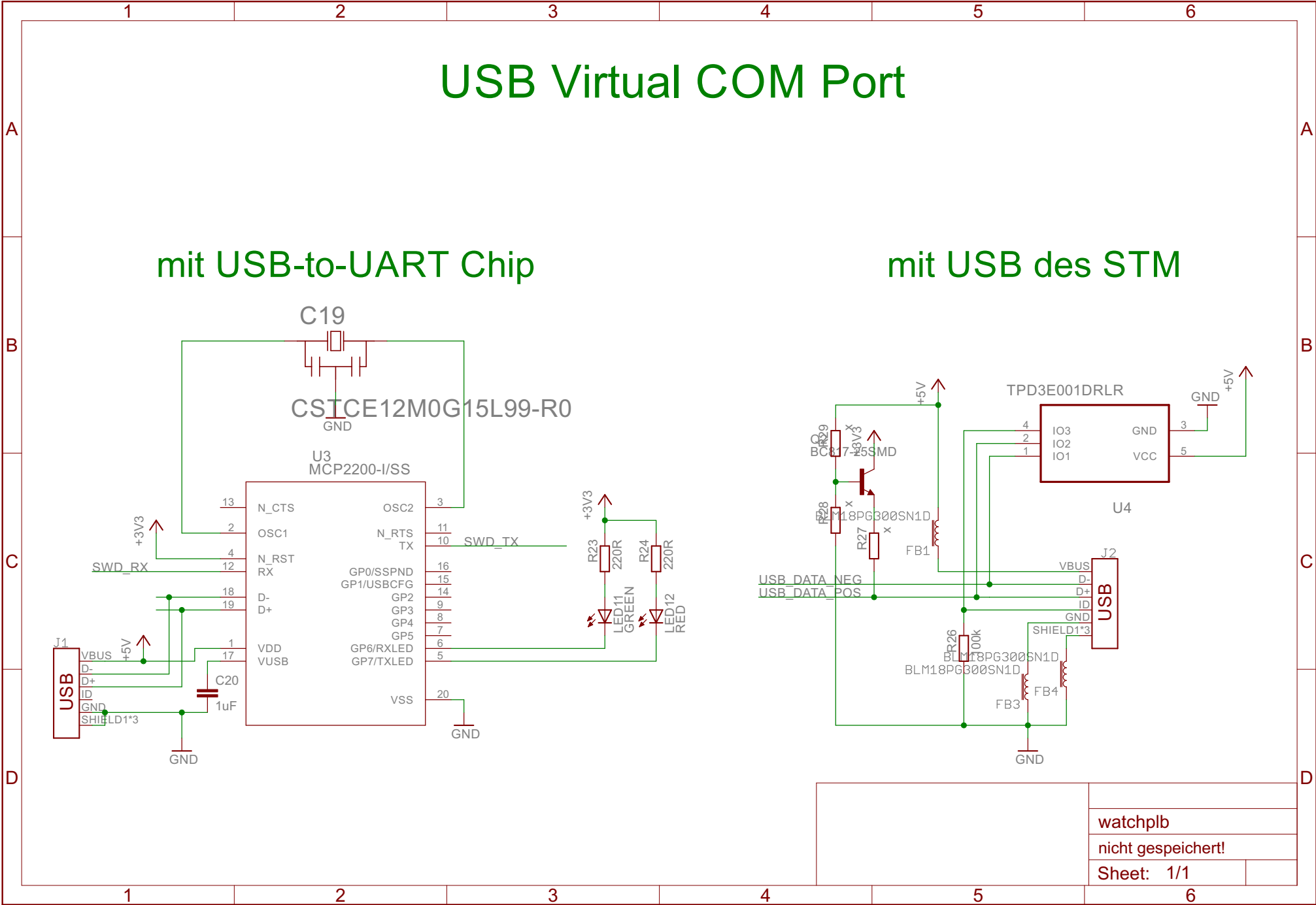
mit USB-to-UART Chip

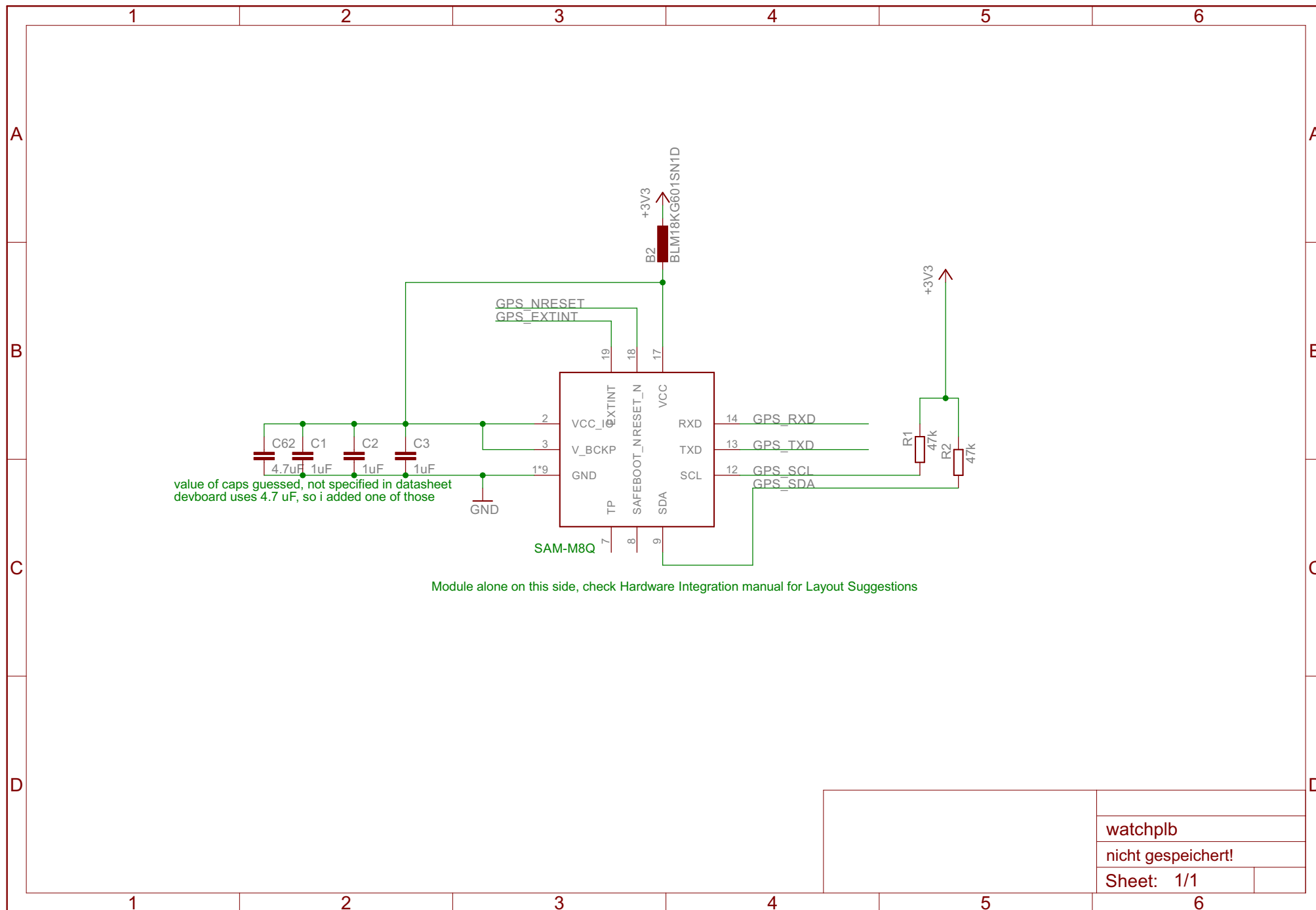
This circuit diagram shows a USB-to-UART chip (U3: MCP2200-I/SS) connected to a USB port (J1) and a UART interface. The USB port provides VBUS (+5V), D-, D+, ID, GND, and SHIELD1*3. The chip's VDD and VUSB pins are connected to +5V, while VSS is connected to GND. A 1uF capacitor (C20) is connected between VDD and GND. The chip's N_CTS, OSC1, N_RST, RX, D-, and D+ pins are connected to the UART interface. The chip's OSC2, N_RTS, TX, GP0/SSPND, GP1/USBCFG, GP2, GP3, GP4, GP5, GP6/RXLED, and GP7/TXLED pins are connected to the UART interface. The chip's SWD_TX and SWD_RX pins are connected to the UART interface. The chip's SWD_TX and SWD_RX pins are connected to the UART interface. The chip's SWD_TX and SWD_RX pins are connected to the UART interface.

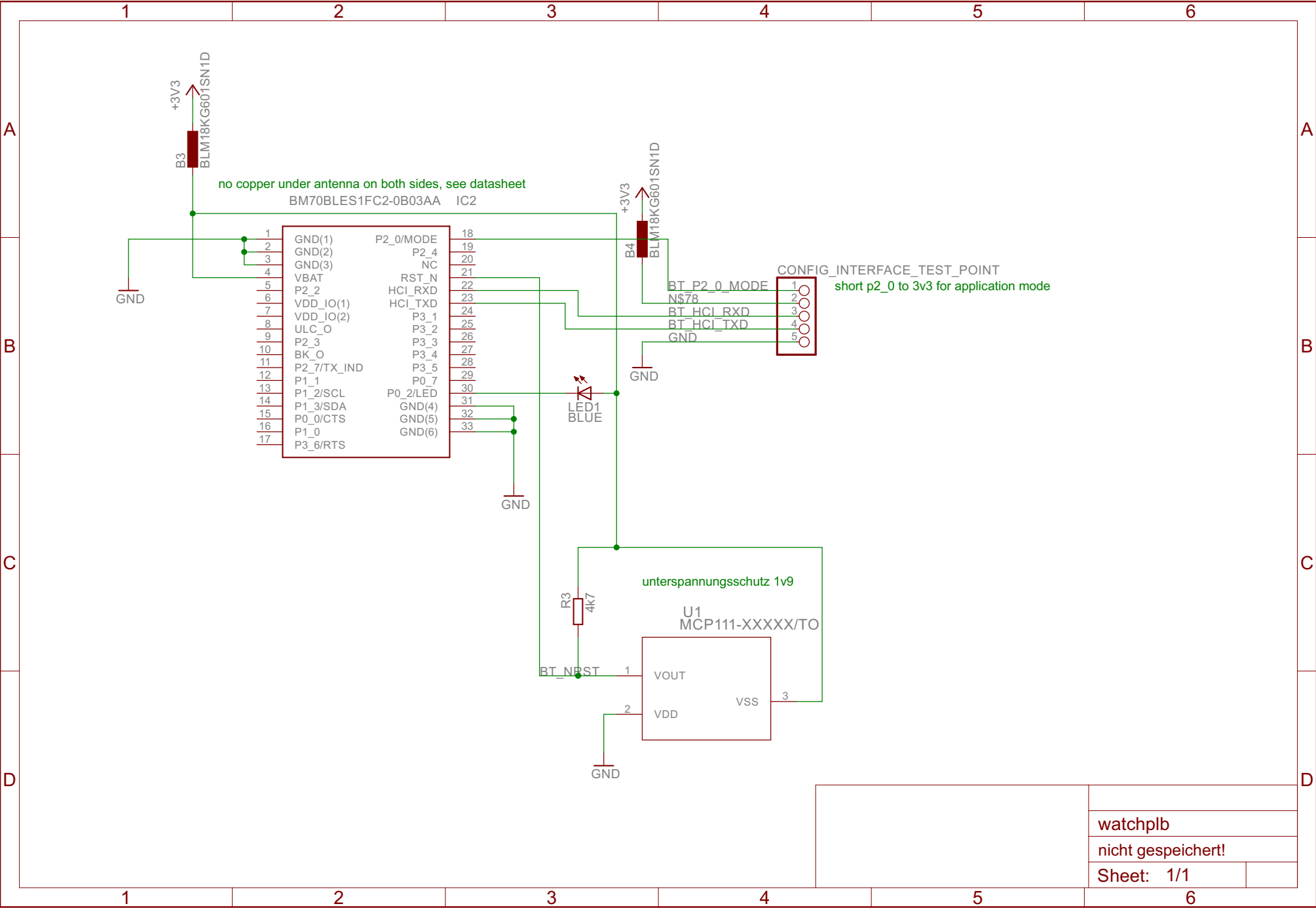
mit USB des STM

This circuit diagram shows the STM32 USB interface connected to a USB port (J2) and a UART interface. The USB port provides VBUS (+5V), D-, D+, ID, GND, and SHIELD1*3. The STM32's VBUS, D-, D+, ID, GND, and SHIELD1*3 pins are connected to the USB port. The STM32's VBUS, D-, D+, ID, GND, and SHIELD1*3 pins are connected to the USB port. The STM32's VBUS, D-, D+, ID, GND, and SHIELD1*3 pins are connected to the USB port.

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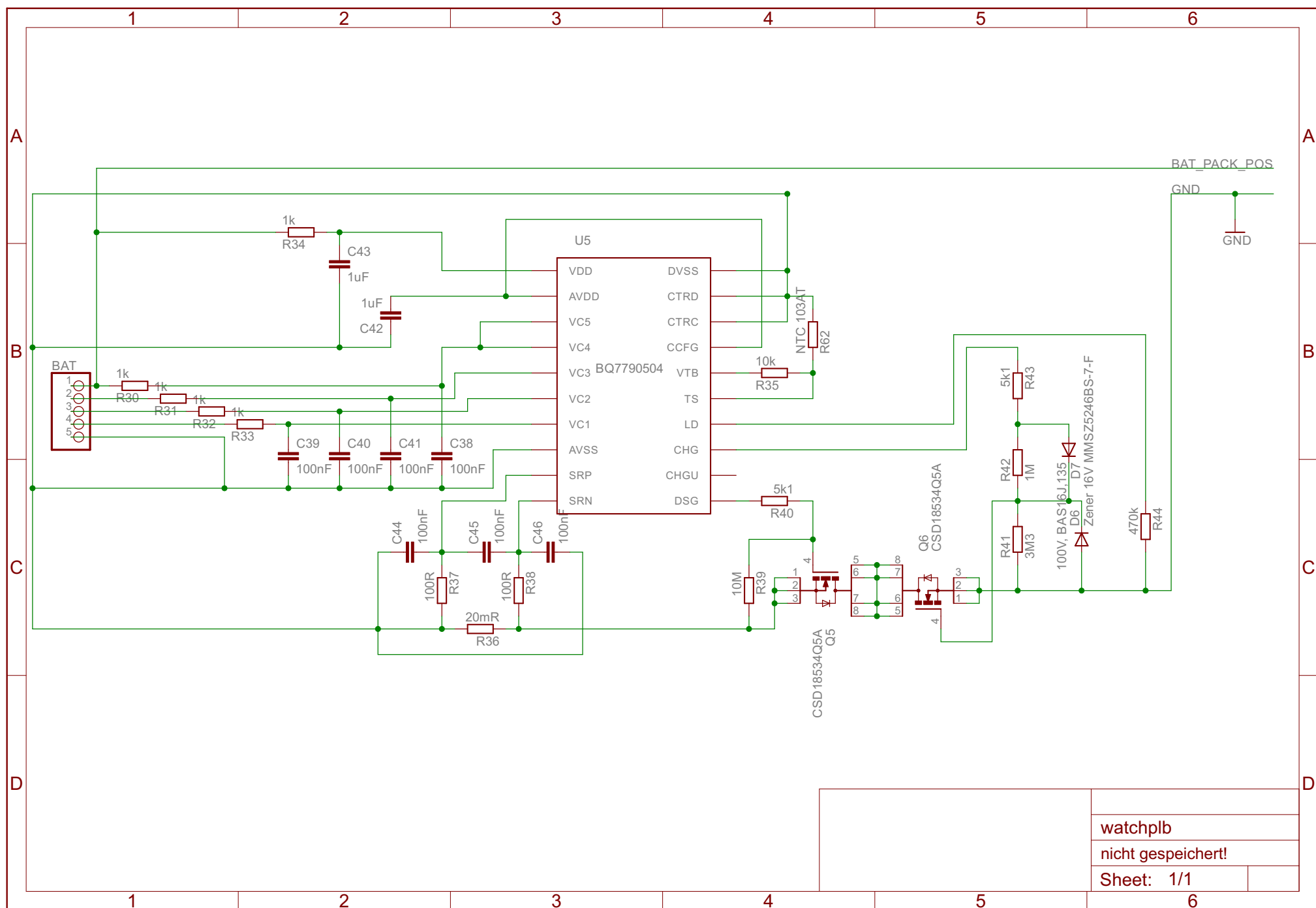




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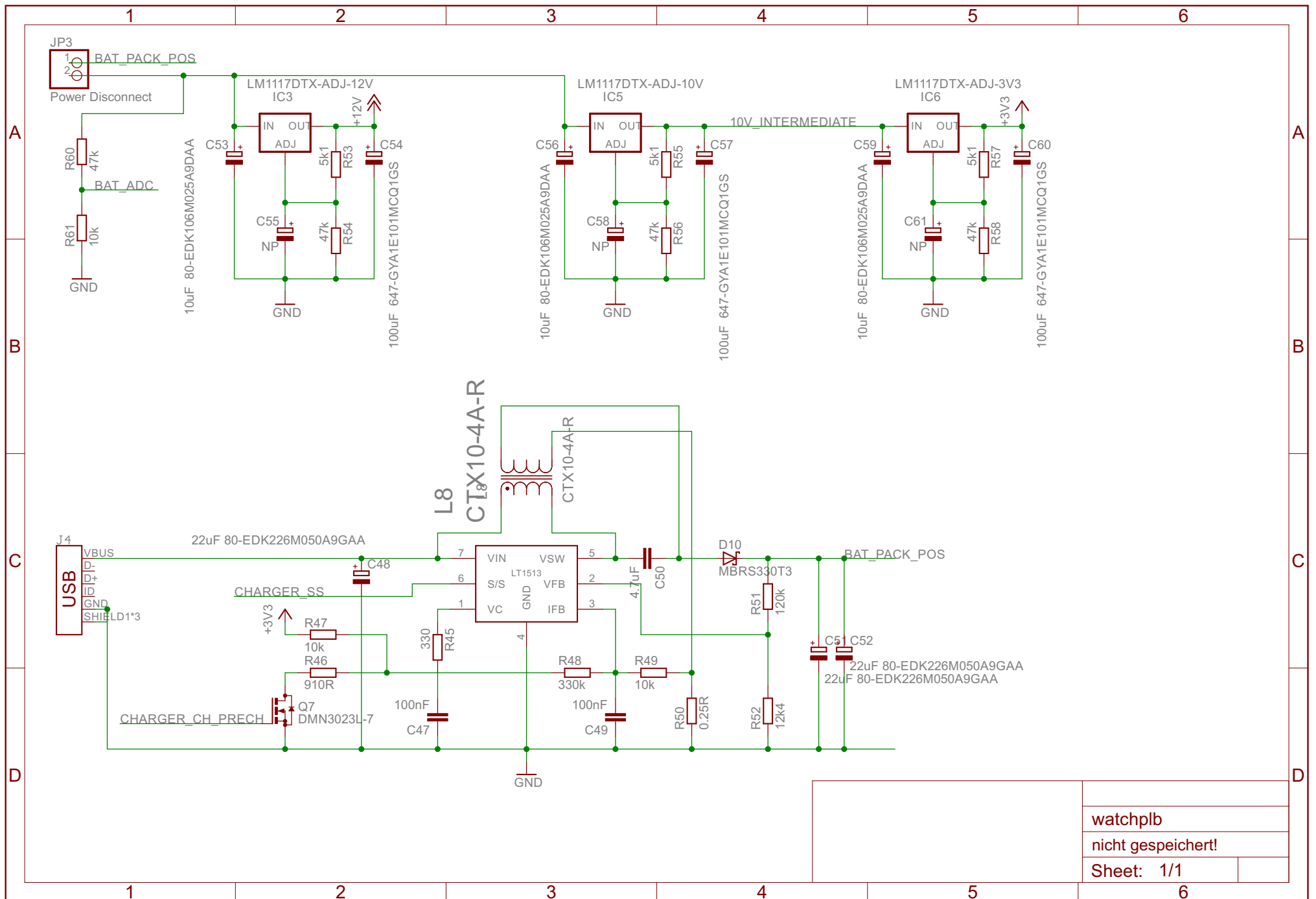
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