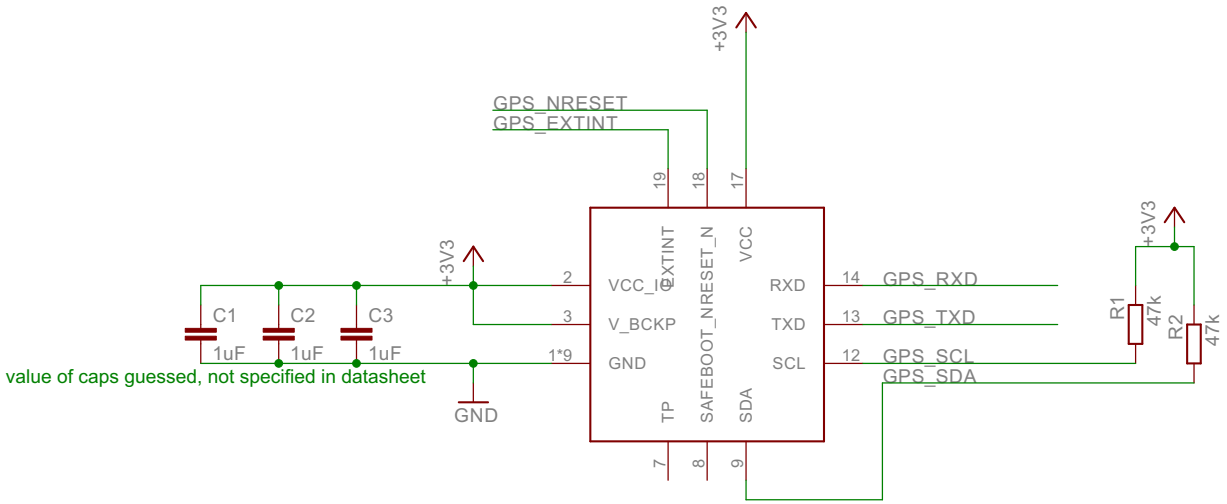

Module alone on this side, check Hardware Integration manual for Layout Suggestions

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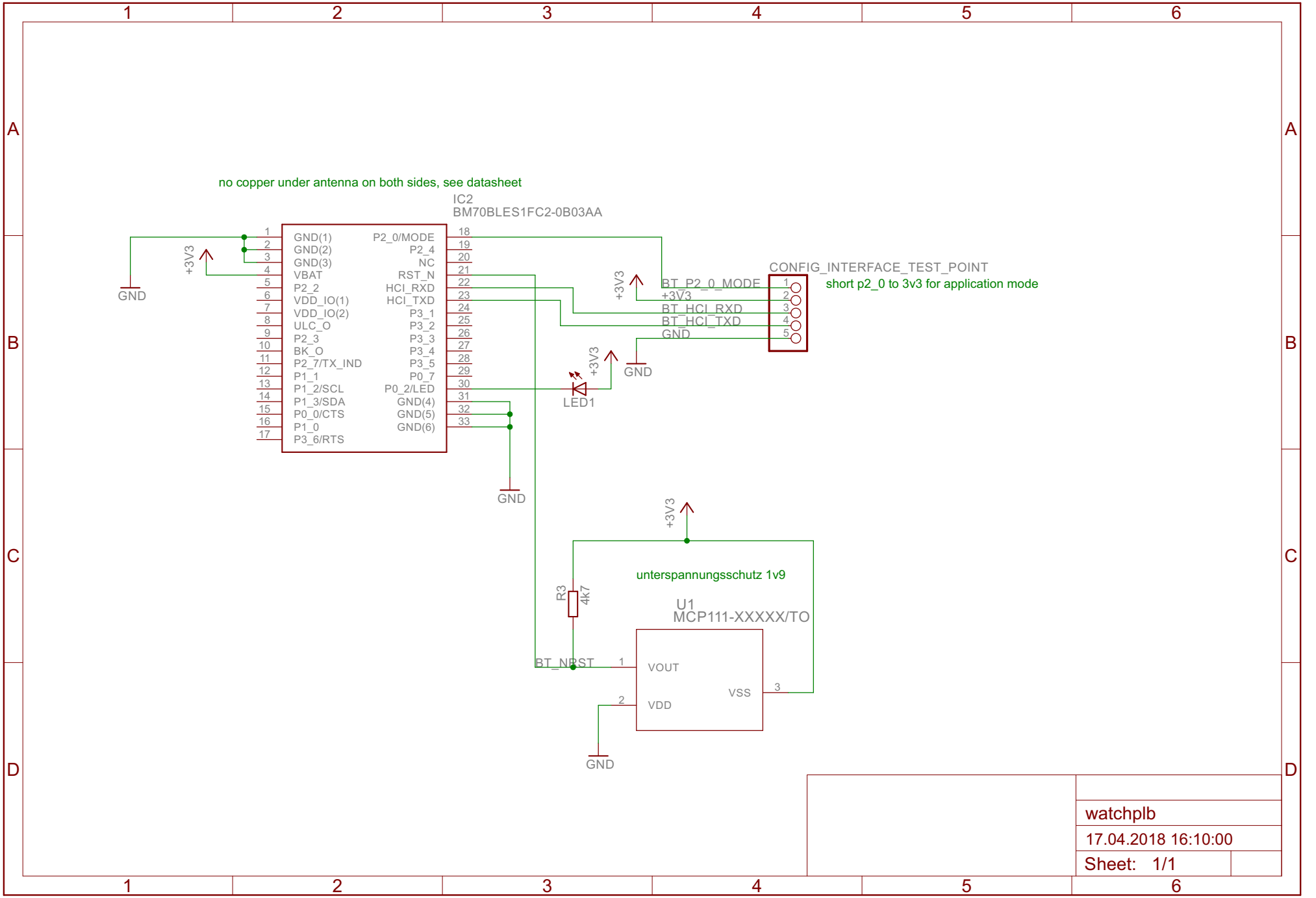
no copper under antenna on both sides, see datasheet
IC2
BM70BLES1FC2-0B03AA
GND(1)
GND(2)
GND(3)
VBAT
P2_2
VDD_IO(1)
VDD_IO(2)
ULC_O
P2_3
BK_O
P2_7/TX_IND
P1_1
P1_2/SCL
P1_3/SDA
P0_0/CTS
P1_0
P3_6/RTS
P2_0/MODE
P2_4
NC
RST_N
HCI_RXD
HCI_TXD
P3_1
P3_2
P3_3
P3_4
P3_5
P0_7
P0_2/LED
GND(4)
GND(5)
GND(6)
+3V3
GND
CONFIG_INTERFACE_TEST_POINT
short p2_0 to 3v3 for application mode
BT_P2_0_MODE
BT_HCI_RXD
BT_HCI_TXD
LED1
unterspannungsschutz 1v9
R3
4k7
U1
MCP111-XXXXX/TO
BT_NRST
VOUT
VDD
VSS
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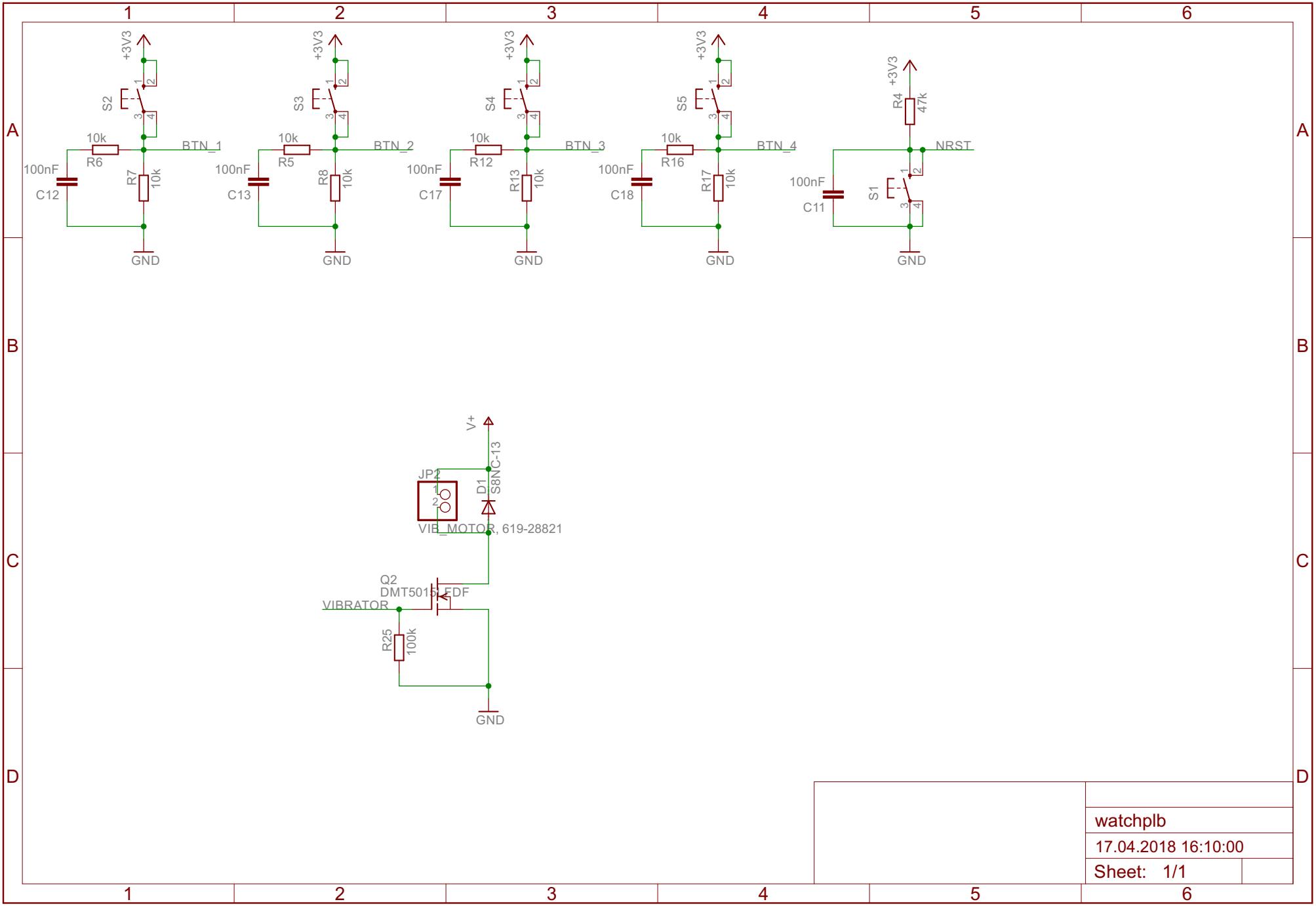
1
2
3
4
5
6
A
B
C
D
+3V3
S2
10k
R6
BTN_1
100nF
C12
R7
10k
GND
S3
R5
BTN_2
C13
R8
S4
R12
BTN_3
C17
R13
S5
R16
BTN_4
C18
R17
R4
47k
NRST
C11
S1
V+
JP2
D1
S8NC-13
VIB_MOTOR, 619-28821
Q2
DMT5015LFDF
VIBRATOR
R25
100k
watchplb
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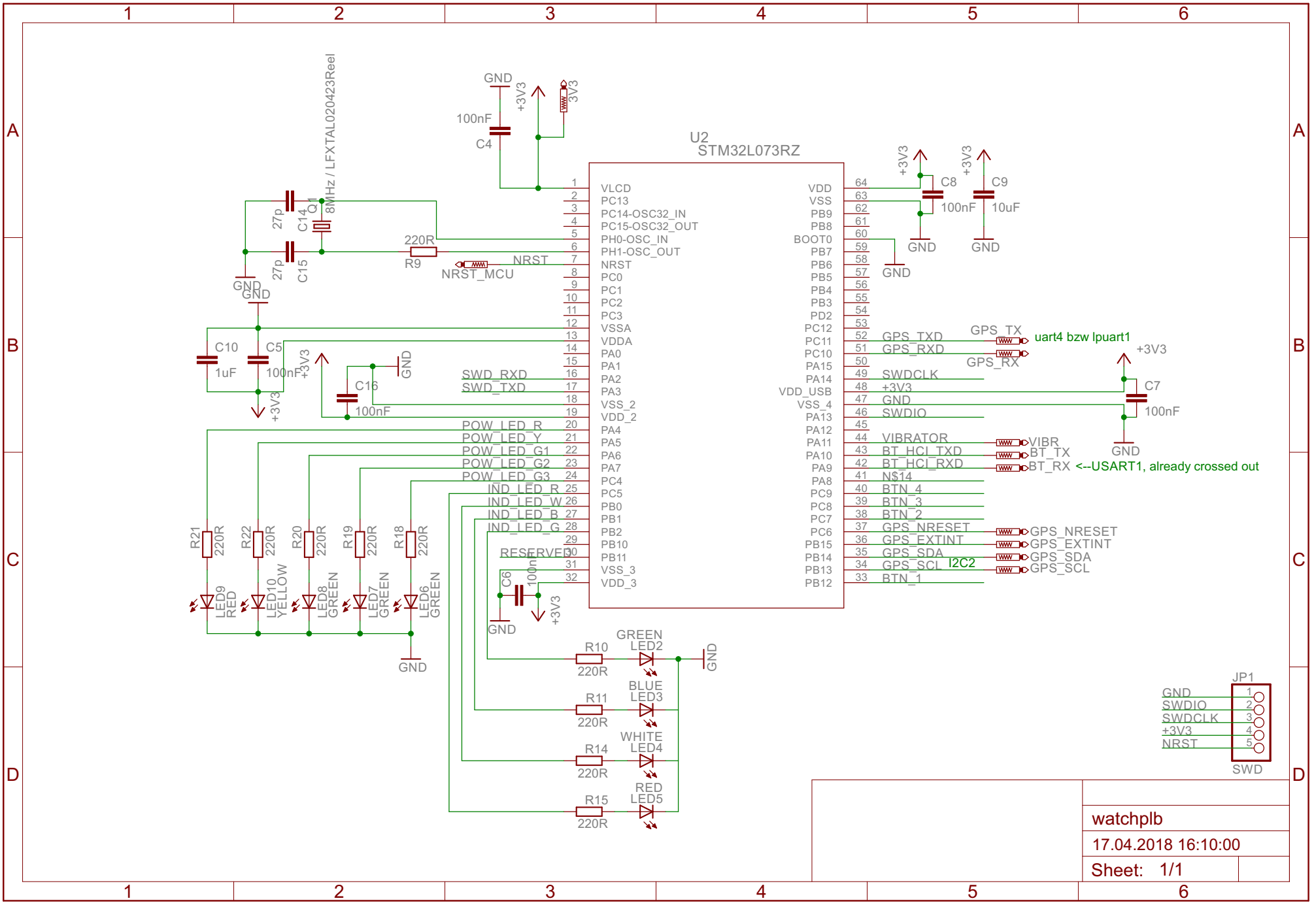
U2
STM32L073RZ
8MHz / LFXTAL020423Reel
Q1
C14
27p
C15
27p
R9
220R
NRST
NRST_MCU
C4
100nF
3V3
C8
C9
100nF
10uF
C10
1uF
C5
100nF
C16
100nF
C7
100nF
SWD_RXD
SWD_TXD
POW_LED_R
POW_LED_Y
POW_LED_G1
POW_LED_G2
POW_LED_G3
IND_LED_R
IND_LED_W
IND_LED_B
IND_LED_G
RESERVED
C6
100nF
R21 220R
R22 220R
R20 220R
R19 220R
R18 220R
LED9 RED
LED10 YELLOW
LED8 GREEN
LED7 GREEN
LED6 GREEN
R10 220R
GREEN LED2
R11 220R
BLUE LED3
R14 220R
WHITE LED4
R15 220R
RED LED5
GPS_TX
GPS_RX
GPS_TXD
GPS_RXD
uart4 bzw lpuart1
SWDCLK
SWDIO
VIBRATOR
VIBR
BT_HCI_TXD
BT_TX
BT_HCI_RXD
BT_RX
<--USART1, already crossed out
N$14
BTN_4
BTN_3
BTN_2
BTN_1
GPS_NRESET
GPS_EXTINT
GPS_SDA
GPS_SCL
I2C2
JP1
SWD
VLCD
PC13
PC14-OSC32_IN
PC15-OSC32_OUT
PH0-OSC_IN
PH1-OSC_OUT
NRST
PC0
PC1
PC2
PC3
VSSA
VDDA
PA0
PA1
PA2
PA3
VSS_2
VDD_2
PA4
PA5
PA6
PA7
PC4
PC5
PB0
PB1
PB2
PB10
PB11
VSS_3
VDD_3
VDD
VSS
PB9
PB8
BOOT0
PB7
PB6
PB5
PB4
PB3
PD2
PC12
PC11
PC10
PA15
PA14
VDD_USB
VSS_4
PA13
PA12
PA11
PA10
PA9
PA8
PC9
PC8
PC7
PC6
PB15
PB14
PB13
PB12
GND
+3V3
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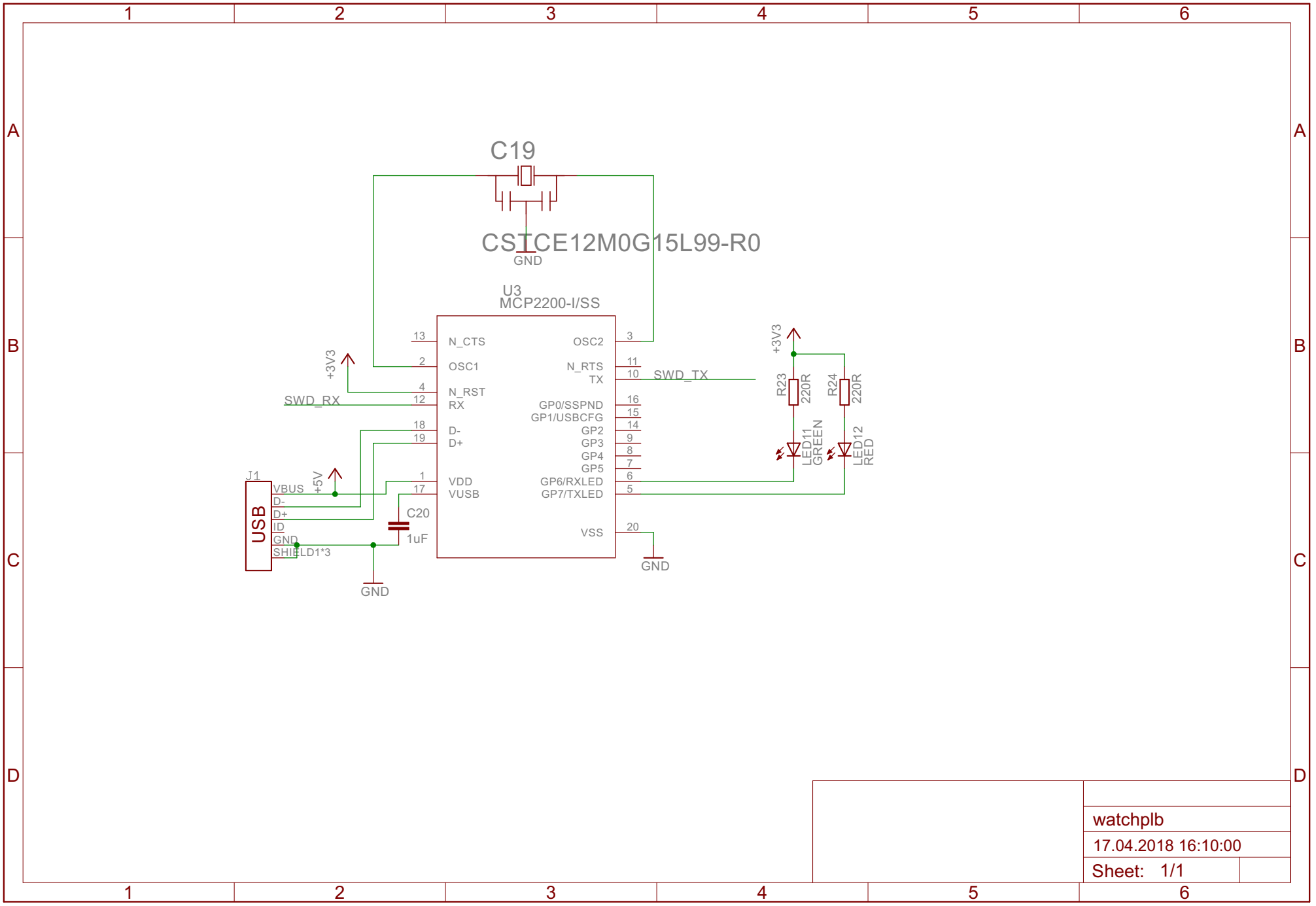

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