BTA16, BTB16 T1610, T1635

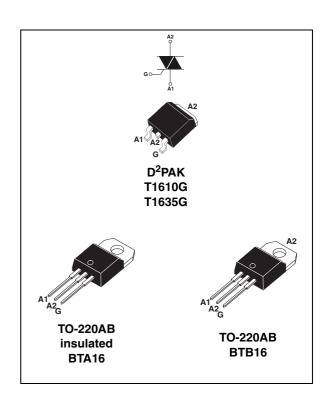
16 A Snubberless™, logic level and standard Triacs

Features

- Medium current Triac
- Low thermal resistance with clip bonding
- Low thermal resistance insulation ceramic for insulated BTA
- High commutation (4Q) or very high commutation (3Q) capability
- BTA series UL1557 certified (File ref: 81734)
- RoHS (2002/95/EC) compliant
- Insulated tab (BTA series, rated at 2500 V_{RMS})

Applications

- Snubberless versions (BTA/BTB...W and T1635) especially recommended for use on inductive loads, because of their high commutation performances
- On/off or phase angle function in applications such as static relays, light dimmers and appliance motor speed controllers



Description

Available either in through-hole or surface-mount packages, the BTA16, BTB16, T1610 and T1635 Triacs series are suitable for general purpose mains power AC switching.

Table 1. Device summary

Symbol	Parameter	BTA16 ⁽¹⁾	BTB16	T1610	T1635
I _{T(RMS)}	On-state rms current	16	16	16	16
V _{DRM} /V _{RRM}	Repetitive peak off-state voltage	600/800	600/800	600/800	600/800
I _{GT} (Snubberless)	Triggering gate current	35/50	35/50	-	35
I _{GT} (logic level)	Triggering gate current	10	10	10	-
I _{GT} (standard)	Triggering gate current	25/50	25/50	-	-

^{1.} Insulated

TM: Snubberless is a trademark of STMicroelectronics

1 Characteristics

Table 2. Absolute maximum ratings

Symbol	Paramete		Value	Unit		
	On-state rms current	D ² PAK / TO-220AB	T _c = 100 °C	16	Α	
IT(RMS)	(full sine wave)	TO-220AB insulated	T _c = 86 °C	10	ζ.	
	Non repetitive surge peak on-state	F = 50 Hz	t = 20 ms	160		
I _{TSM}	I _{TSM} current (full cycle, T _j initial = 25 °C)		t = 16.7 ms	168	Α	
l²t	I ² t value for fusing	t _p = 10 ms		144	A²s	
dI/dt	Critical rate of rise of on-state current $I_G=2~x~I_{GT}$, $t_r \leq 100~ns$	F = 120 Hz T _j = 125 °C		50	A/µs	
V _{DSM} / V _{RSM}	Non repetitive surge peak off-state voltage	t _p = 10 ms	T _j = 25 °C	V _{DRM} /V _{RRM} + 100	V	
I _{GM}	Peak gate current $t_p = 20 \mu s$ T		T _j = 125 °C	4	Α	
P _{G(AV)}	Average gate power dissipation	1	W			
T _{stg}	Storage temperature range	-40 to + 150				
T _j	Maximum operating junction temperat	-40 to + 125				

Table 3. Electrical characteristics (T_j = 25 °C, unless otherwise specified) Snubberless and logic level (3 quadrants)

Symbol	Test conditions	Quadrant		T1610	T1635	BTA16 / BTB16			Unit
Syllibol	rest conditions	Quadrant	11010	11033	sw	cw	BW	Oilit	
I _{GT} ⁽¹⁾	V _D = 12 V	I - II - III	Max.	10	35	10	35	50	mA
V _{GT}	$R_L = 33 \Omega$	1 - 11 - 111	Max.			1.3			V
V _{GD}	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $T_j = 125 \text{ °C}$	1 - 11 - 111	Min.			0.2			V
I _H ⁽²⁾	I _T = 500 mA		Max.	15	35	15	35	50	mA
Iı	I _G = 1.2 I _{GT}	I - III	Max.	25	50	25	50	70	mA
"L	IG = 1.2 IGT	II	iviax.	30	60	30	60	80	IIIA
dV/dt (2)	$V_D = 67 \% V_{DRM}$ gate open	T _j = 125 °C	Min.	40	500	40	500	1000	V/µs
	$(dV/dt)c = 0.1 V/\mu s$	T _j = 125 °C		8.5	-	8.5	-	-	
(dl/dt)c (2)	(dV/dt)c = 10 V/μs	T _j = 125 °C	Min.	3.0	-	3.0	-	-	A/ms
	Without snubber	T _j = 125 °C		-	8.5	-	8.5	14	

^{1.} Minimum IGT is guaranted at 5% of I_{GT} max

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^{2.} For both polarities of A2 referenced to A1

Table 4. Electrical characteristics ($T_j = 25$ °C, unless otherwise specified) standard (4 quadrants)

Symbol	Test conditions	Quadrant		BTA16	BTB16	Unit
Symbol	rest conditions	Quadrant		С	В	O I II
I _{GT} ⁽¹⁾	$V_D = 12 \text{ V}$ $R_L = 33 \Omega$	I - II - III IV	Max.	25 50	50 100	mA
V _{GT}		ALL	Max.	1.3		V
V_{GD}	$V_D = V_{DRM} R_L = 3.3 \; k\Omega T_j = 125 \; ^{\circ}C$	ALL	Min.	0.2		٧
I _H ⁽²⁾	I _T = 500 mA		Max.	25	50	mA
	1 1.21.	I - III - IV	Max.	40	60	mA
'L	I_L $I_G = 1.2 I_{GT}$	II	iviax.	80	120	IIIA
dV/dt (2)	V _D = 67 %V _{DRM} gate open	T _j = 125 °C	Min.	200	400	V/µs
(dV/dt)c (2)	(dl/dt)c = 7 A/ms	T _j = 125 °C	Min.	5	10	V/µs

- 1. Minimum IGT is guaranted at 5% of $\rm I_{GT}$ max
- 2. For both polarities of A2 referenced to A1

Table 5. Static characteristics

Symbol	Test conditio	Value	Unit		
V _T (2)	$I_{TM} = 22.5 \text{ A}$ $t_p = 380 \mu\text{s}$	T _j = 25 °C	Max.	1.55	V
V _{to} (2)	Threshold voltage	T _j = 125 °C	Max.	0.85	V
R _d (2)	Dynamic resistance	T _j = 125 °C	Max.	25	mΩ
I _{DRM}	I _{DRM} V – V		Max.	5	μΑ
$V_{DRM} = V_{RRM}$		T _j = 125 °C	iviax.	2	mA

Table 6. Thermal resistance

Symbol		Value	Unit		
В	lunction to coop (AC)		D ² PAK / TO-220AB	1.2	°C/W
R _{th(j-c)}	Junction to case (AC)	TO-220AB insulated	2.1	C/VV	
	$S^{(1)} = 1 \text{ cm}^2$ Junction to ambient		D ² PAK	45	
R _{th(j-a)}			TO-220AB / TO-220AB insulated	60	°C/W

1. S = Copper surface under tab

Figure 1. Maximum power dissipation versus Figure 2. On-state rms current versus case on-state rms current (full cycle) temperature (full cycle)

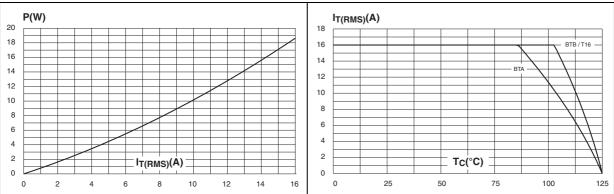


Figure 3. On-state rms current versus ambient temperature (full cycle)

Figure 4. Relative variation of thermal impedance versus pulse duration

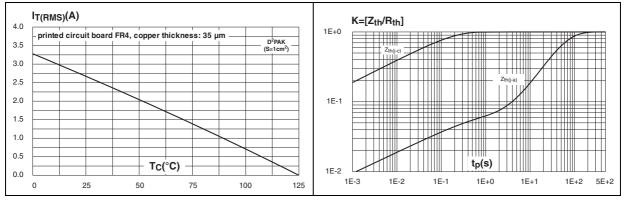
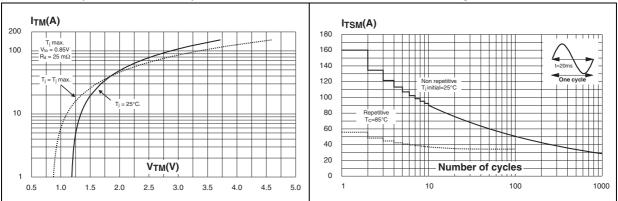


Figure 5. On-state characteristics (maximum values)

Figure 6. Surge peak on-state current versus number of cycles



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Figure 7. Non-repetitive surge peak on-state Figure 8. Relative variation of gate trigger current for a sinusoidal current

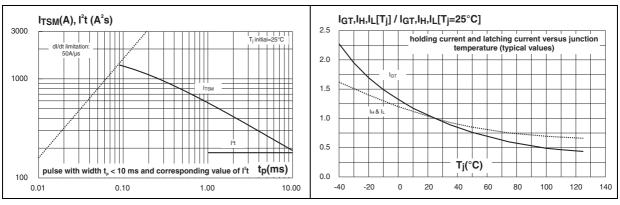


Figure 9. Relative variation of critical rate of decrease of main current versus decrease (dV/dt)c (typical values) (dV/d)

Figure 10. Relative variation of critical rate of decrease of main current versus (dV/dt)c (typical values)

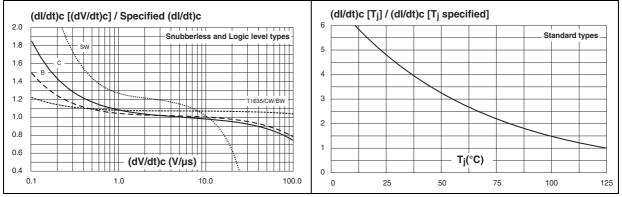
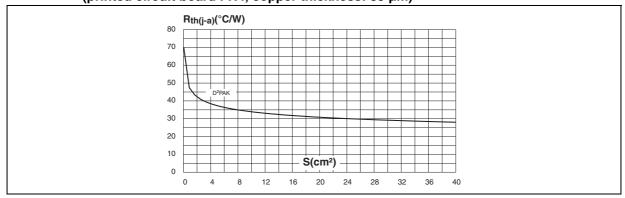


Figure 11. D²PAK thermal resistance junction to ambient versus copper surface under tab (printed circuit board FR4, copper thickness: 35 μm)



2 Ordering information

Figure 12. Ordering information scheme (BTA16 and BTB16 series)

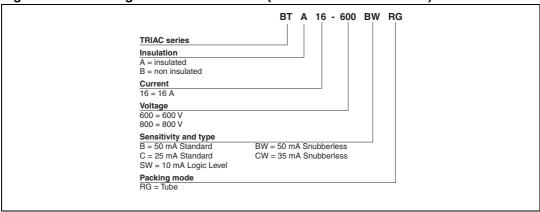


Figure 13. Ordering information scheme (T16 series)

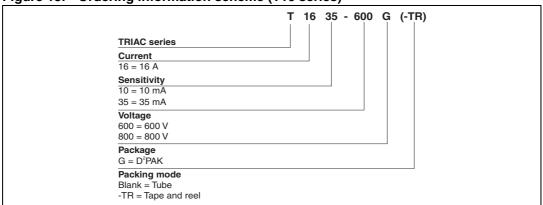


Table 7. Product selector

Device ⁽¹⁾	Voltage (xxx)		Sensitivity	Туре	Package	
Device	600 V	800 V	Sensitivity	Туре	1 ackage	
BTA/BTB16-xxxB	Х	Х	50 mA	Standard	TO-220AB	
BTA/BTB16-xxxBW	Х	Х	50 mA	Snubberless	TO-220AB	
BTA/BTB16-xxxC	Х		25 mA	Standard	TO-220AB	
BTA/BTB16-xxxCW	Х	Х	35 mA	Snubberless	TO-220AB	
BTA/BTB16-xxxSW	Х	Х	10 mA	Logic level	TO-220AB	
T1610-xxxG	Х	Х	10 mA	Logic level	D ² PAK	
T1635-xxxG	Х	Χ	35 mA	Snubberless	D ² PAK	

^{1.} BTB: non insulated TO-220AB package

3 Package information

- Epoxy meets UL94, V0
- Recommended torque value: 0.4 to 0.6 N⋅m

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Table 8. D²PAK dimensions

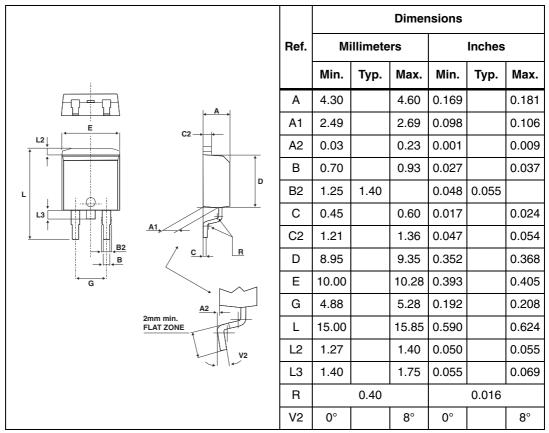
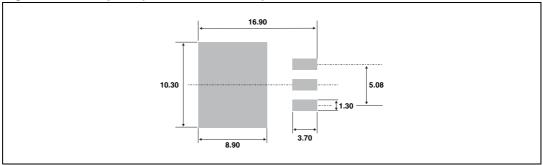


Figure 14. Footprint (dimensions in mm)



Dimensions Ref. Millimeters Inches Min. Typ. Max. Min. Тур. Max. 15.20 15.90 0.598 0.625 0.147 a1 3.75 В С Ø١ 13.00 14.00 0.511 0.551 a2 10.40 В 10.00 0.393 0.409 Ĺ b1 0.61 0.88 0.024 0.034 0.051 b2 1.23 1.32 0.048 С 4.40 4.60 0.173 0.181 13 с1 0.49 0.70 0.019 0.027 c2 c2 2.40 2.72 0.094 0.107 12 a2 2.40 0.094 0.106 2.70 е F 6.20 6.60 0.244 0.259 ØΙ 3.75 3.85 0.147 0.151 0.661 14 15.80 16.40 16.80 0.622 0.646 L 2.65 2.95 0.104 0.116 12 1.14 0.044 0.066 1.70 13 1.14 1.70 0.044 0.066 Μ 2.60 0.102

Table 9. TO-220AB (non-insulated and insulated) dimensions

4 Ordering information

Table 10. Ordering information

	<u> </u>				
Order code ⁽¹⁾	Marking ⁽¹⁾	Package	Weight	Base qty	Delivery mode
BTA16-xxxyzRG	BTA16xxxyz	TO-220AB	2.3 g	50	Tube
BTB16-xxxyzRG	BTB16xxxyz	TO-220AB	2.3 g	50	Tube
T1610-xxxG-TR	T1610xxxG			1000	Tape and reel
T1635-xxxG	T1635xxxG	D ² PAK	1.5 g	50	Tube
T1635-xxxG-TR	T1635xxxG			1000	Tape and reel

^{1.} xxx = voltage, y = sensitivity, z = type

5 Revision history

Table 11. Document revision history

Date	Revision	Changes		
Oct-2002	6A	Last update.		
13-Feb-2006	7	TO-220AB delivery mode changed from bulk to tube. ECOPACK statement added.		
03-Jul-2009	8	Added part number T1610.		
11-Mar-2010	9	9 Updated value for V _{DSM} /V _{RSM} in <i>Table 2</i> . Updated temperature in <i>Table 2</i> from 15 °C to 86 °C.		

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