Encoding Separation Logic in SMT-LIB v2.5

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Abstract. We propose an encoding of Separation Logic using SMT-LIB v2.5. This format is currently supported by SMT solvers (CVC4) and inductive proof-theoretic solvers (SLIDE and SPEN). Moreover, we provide a library of benchmarks written using this format, which stems from the set of benchmarks used in SL-COMP'14 [7].

1 Preliminaries

We consider formulae in multi-sorted first-order logic. A *signature* Σ consists of a set Σ^s of sort symbols and a set Σ^f of *function symbols* $f^{\sigma_1 \cdots \sigma_n \sigma}$, where $n \ge 0$ and $\sigma_1, \ldots, \sigma_n, \sigma \in \Sigma^s$. If n = 0, we call f^{σ} a *constant symbol*. We make the following assumptions:

- 1. all signatures Σ contain the Boolean sort B, where \top and \bot denote the Boolean constants *true* and *false*.
- 2. Σ^f contains a boolean equality function $\approx^{\sigma\sigma B}$ for each sort symbol $\sigma \in \Sigma^s$.

Let Vars be a countable set of first-order variables, each $x^{\sigma} \in \text{Vars}$ having an associated sort σ . First-order terms and formulae over the signature Σ (called Σ -terms and Σ -formulae) are defined as usual. A Σ -interpretation I maps:

- each sort symbol $\sigma \in \Sigma$ to a non-empty set σ^I ,
- each function symbol $f^{\sigma_1,\dots,\sigma_n,\sigma} \in \Sigma$ to a total function $f^I : \sigma_1^I \times \dots \times \sigma_n^I \to \sigma^I$ where n > 0, and to an element of σ^I when n = 0, and
- each variable $x^{\sigma} \in \text{Vars to an element of } \sigma^{I}$.

For an interpretation I a sort symbol σ and a variable x, we denote by $I[\sigma \leftarrow S]$ and, respectively $I[x \leftarrow v]$, the interpretation associating the set S to σ , respectively the value v to x, and which behaves like I in all other cases. By writing $I[\sigma \leftarrow S]$ we ensure that all variables of sort σ are mapped by I to elements of S. For a Σ -term t, we write t^I to denote the interpretation of t in I, defined inductively, as usual. A satisfiability relation between Σ -interpretations and Σ -formulas, written $I \models \varphi$, is also defined inductively, as usual. In this case, we say that I is a *model* of φ .

A (multi-sorted first-order) theory is a pair $T = (\Sigma, \mathbf{I})$ where Σ is a signature and \mathbf{I} is a non-empty set of Σ -interpretations, the models of T. A Σ -formula φ is T-satisfiable if it is satisfied by some interpretation in \mathbf{I} .

2 Ground Separation Logic

Let $T = (\Sigma, \mathbf{I})$ be a theory and let Loc and Data be two sorts from Σ , with no restriction other than the fact that Loc is always interpreted as a countable set. Also, we consider

that Σ has a designated constant symbol nil^{Loc}. We define the *Ground Separation Logic* $SL(T)_{Loc,Data}^g$ to be the set of formulae generated by the following syntax:

$$\varphi := \phi \mid \text{emp} \mid \mathsf{t} \mapsto \mathsf{u} \mid \varphi_1 * \varphi_2 \mid \varphi_1 * \varphi_2 \mid \neg \varphi_1 \mid \varphi_1 \wedge \varphi_2 \mid \exists x^{\sigma} \cdot \varphi_1(x)$$

where ϕ is a Σ -formula, and t, u are Σ -terms of sorts Loc and Data, respectively. As usual, we write $\forall x^{\sigma}$. $\varphi(x)$ for $\neg \exists x^{\sigma}$. $\neg \varphi(x)$. We omit specifying the sorts of variables and functions when they are clear from the context.

Given an interpretation I, a *heap* is a finite partial mapping $h : \mathsf{Loc}^I \to_{\mathsf{fin}} \mathsf{Data}^I$. For a heap h, we denote by $\mathsf{dom}(h)$ its domain. For two heaps h_1 and h_2 , we write $h_1 \# h_2$ for $\mathsf{dom}(h_1) \cap \mathsf{dom}(h_2) = \emptyset$ and $h = h_1 \uplus h_2$ for $h_1 \# h_2$ and $h = h_1 \cup h_2$. We define the *satisfaction relation* I, $h \models_{\mathsf{SL}} \phi$ inductively, as follows:

```
\begin{array}{lll} I,h \models_{\operatorname{SL}} \phi & \iff I \models \phi \text{ if } \phi \text{ is a } \Sigma\text{-formula} \\ I,h \models_{\operatorname{SL}} \operatorname{emp} & \iff h = \emptyset \\ I,h \models_{\operatorname{SL}} \mathsf{t} \mapsto \mathsf{u} & \iff h = \{(\mathsf{t}^I,\mathsf{u}^I)\} \text{ and } \mathsf{t}^I \not\approx \operatorname{nil}^I \\ I,h \models_{\operatorname{SL}} \phi_1 \ast \phi_2 & \iff \operatorname{there \ exist \ heaps} h_1,h_2 \ \text{s.t.} \ h = h_1 \uplus h_2 \ \operatorname{and} I,h_i \models_{\operatorname{SL}} \phi_i,i = 1,2 \\ I,h \models_{\operatorname{SL}} \phi_1 \ast \phi_2 & \iff \operatorname{for \ all \ heaps} h' \ \text{ if } h'\#h \ \operatorname{and} I,h' \models_{\operatorname{SL}} \phi_1 \ \operatorname{then} I,h' \uplus h \models_{\operatorname{SL}} \phi_2 \\ I,h \models_{\operatorname{SL}} \exists x^S.\varphi(x) & \iff I[x \leftarrow s],h \models_{\operatorname{SL}} \varphi(x), \ \text{for some} \ s \in S^I \end{array}
```

The satisfaction relation for Σ -formulae, Boolean connectives \wedge , \neg , and linear arithmetic atoms, are the classical ones from first-order logic. Notice that the range of a quantified variable x^S is the interpretation of its associated sort S^I . A formula φ is said to be *satisfiable* if there exists an interpretation I and a heap h such that $I, h \models_{SL} \varphi$. We say that φ *entails* ψ , written $\varphi \models_{SL} \psi$, when every pair (I, h) which satisfies φ , also satisfies ψ .

2.1 SMT-LIB Encoding

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We write ground SL formulae in SMT-LIB using the following functions:

```
(emp Bool)
  (sep Bool Bool Bool :left-assoc)
  (wand Bool Bool Bool :right-assoc)
  (par (Loc Data) (pto Loc Data Bool))
  (par (Loc) (nil Loc))
```

Observe that pto and nil are polymorphic functions, with sort parameters Loc and Data. There is no restriction on the choice of Loc and Data, as shown below.

Is nil always of sort Loc? Here Loc is just a parameter with no special meaning.

Assume that Loc is an uninterpreted sort U, Data is the integer sort Int, and that x^{U} , y^{U} , a^{Int} and b^{Int} are constants:

```
(declare-sort U 0)
(declare-const x U)
(declare-const y U)
(declare-const a Int)
(declare-const b Int)
```

We write the SL formula emp \land (($x \mapsto a * y \mapsto b$) $\neg * (x \mapsto \mathsf{nil} * \top$)) in SMT-LIB as follows:

```
(and emp (wand (sep (pto x a) (pto y b)) (sep (pto x (as nil Int)) true)))
```

In addition to the classical SMT-LIB typing constraints, the SL theories require that the heap models are well-typed. For instance, a separation constraint of the form:

```
(sep (pto x y) (pto a b))
```

with the above constant declarations results in a typing error, because (pto x y) requires the heap to be of type $U \rightarrow U$, whereas (pto a b) requires the heap to be of type Int \rightarrow Int, and combining heaps of different types is not allowed.

- 1. It is currently unclear what the type of the heap introduced by emp should be. The solution currently adopted in CVC4 is to give emp parameters used only to infer the type, as in (emp x a). Another solution, used in SLIDE, is to infer the type of emp based on the context. For instance the type of the emp heap in (sep (pto x a) emp) is U → Int. We leave this point for discussion.
- 2. It is usually expected that nil be of type Loc, however currently one can force other type that Loc, like in the previous example. Shall we impose a stricter type checking on nil?

This heap typing restriction is not a limitation of the expressive power of the SMT-LIB encoding and can be easily overcome by using datatypes (available in SMT-LIB v2.5). Suppose, for instance that we would like to specify a heap consisting of cells containing both integer and boolean data. The idea is to declare a union type:

```
(declare-datatype BoolInt ((cons_bool (d Bool)) (cons_int (d Int)))) and use it to describe a mixed data heap, as in:
```

```
(sep (pto x (cons_bool false)) (pto y (cons_int 0)))
```

The same workaround can be used to specify heaps with mixed addresses, although this is a much less common situation in practice.

2.2 Separation Logic with Inductive Definitions

Let Pred be a set of second-order variables, each $P^{\sigma_1...\sigma_n} \in \text{Pred}$ having an associated tuple of parameter sorts $\sigma_1, \ldots, \sigma_n \in \Sigma^s$. In addition to the first-order terms built using variables from Vars and function symbols from Σ^f , we enrich the language of SL with the boolean terms $P^{\sigma_1...\sigma_n}(t_1,\ldots,t_n)$, where each t_i is a first-order term of sort σ_i , for $i=1,\ldots,n$. Each second-order variable $P^{\sigma_1...\sigma_n} \in \text{Pred}$ is provided with an inductive definition $P(x_1,\ldots,x_n) \leftarrow \phi_P(x_1,\ldots,x_n)$, where ϕ_P is a formula in the extended language, possibly containing occurrences of P. The satisfaction relation is then extended as follows:

$$I, h \models_{\mathsf{SL}} P^{\sigma_1 \dots \sigma_n}(t_1, \dots, t_n) \iff I, h \models_{\mathsf{SL}} \phi_P(t_1^I, \dots, t_n^I)$$

where ϕ_P is the inductive definition of $P^{\sigma_1...\sigma_n}$. Observe that, given a set of inductive definitions, the set of possible models for each second-order variable is the least fixed point of a monotonic and continuous function mapping tuples of sets of models to a set of models.

2.3 SMT-LIB Encoding

An inductive definition $P(x_1,...,x_n) \leftarrow \phi_P(x_1,...,x_n)$ is written in SMT-LIB using a recursive function definition. For instance, the inductive definition of a doubly-linked list segment:

```
\mathsf{dllseg}(h, p, t, n) \leftarrow (\mathsf{emp} \land h \approx n \land p \approx t) \lor \\ (\exists x^{\mathsf{Loc}} . h \mapsto (x, p) * \mathsf{dllseg}(x, h, t, n))
```

is written into SMT-LIB as follows:

2.4 A Detailed Example

Let us go through an example step by step. First of all, the preamble of and SMT-LIB file describing a SL satisfiability query must contain (at least):

```
(set-logic SEPLOG)
```

Since SL is used in combination with other theories, it is customary to start with:

```
(set-logic ALL_SUPPORTED)
```

We consider the slightly modified version of the dllseg definition above, which describes a doubly-linked list segment with ordered integer data:

```
\begin{aligned} \mathsf{dllseg}_{\mathit{ord}}(h,p,t,n,\mathit{min}) \leftarrow (\mathsf{emp} \wedge h \approx n \wedge p \approx t) \vee \\ (\exists x^{\mathsf{Loc}} \exists d^{\mathsf{Int}} \ . \ h \mapsto (d,x,\mathit{min}) * \mathsf{dllseg}_{\mathit{ord}}(x,h,t,n,d)) \wedge \mathit{min} \leq d \end{aligned}
```

Since we do not perform any pointer arithmetic reasoning, we can declare Loc to be an uninterpreted sort:

```
(declare-sort U 0)
```

We encode the definition of $dllseg_{ord}$ as:

Let us consider the problem of proving that a dllseg_{ord} to which a node is appended is again a dllseg_{ord} , provided that the data of the new node it smaller than the minimal element of the first dllseg_{ord} :

```
x \mapsto (m, u, v) * \mathsf{dllseg}_{ord}(u, x, z, t, n) \land m \le n \models_{\mathsf{SL}} \mathsf{dllseg}_{ord}(x, y, z, t, m)
```

We encode this entailment problem as an assertion asking whether the negated problem is satisfiable:

The entailment holds when the assertion is unsatisfiable, which can be checked in the standard way, using (check-sat). However, the dual problem:

is satisfiable, and the counter-model can be obtained in the standard way, using (get-model). Observe that the model of a satisfiable SL query consists of an interpretation of the constants and a specification of the heap.

3 Abduction and Frame Inference

Abduction and frame inference (or bi-abduction for both) are problems that occur in the context of program verification. In this case, the solver is not only required to give a yes/no answer to a satisfiability query, but to infer SL formulae that ensure the validity of a given entailment. Given SL formulae φ and ϕ , and second-order variables X and Y, we consider the following synthesis problems:

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TO BE REFINED: what are the first-order parameters of X and Y? It seems that X is allowed to all free variables of φ , but how about Y? Is it allowed to all free variables of φ and ψ , or just the ones of ψ ?

- 1. The *abduction problem* asks for a satisfiable definition of a X such that $\varphi * X \models_{\mathsf{SL}} \psi$. Sometimes X is called an *anti-frame*. Observe that $X \leftarrow \bot$ is always a solution, but not a very interesting one.
- 2. The *frame inference problem* asks for a definition of Y such that $\varphi \models_{SL} \psi * Y$.
- 3. The *bi-abduction problem* asks for both a satisfiable definition of *X* and a definition of *Y* such that $\varphi * X \models_{\mathsf{SL}} \psi * Y$.

The capability of solving the above problems is key to using a given SL solver for practical program verification purposes. For this reason, we aim at finding a standard way of specifying these problems in SMT-LIB.

4 Additional Resources

The quest for a suitable format for SL solvers started with SL-COMP'14 [7], which adopted the QF_S format, described in [6]. The current proposal is inspired by QF_S, and relies on the datatypes introduced SMT-LIB v2.5 for an elegant encoding of union and record types. The tools supporting SMT-LIB as a native language are:

CVC4 [3] – a description of the SL format of CVC4 is provided in [2] (a slightly modified version of the current proposal)

- SLIDE (under construction) uses the encoding from the current proposal.
- SPEN [1] a description of the SL format of SPEN (QF_S) is available in [6]. Other tools that participated to SL-COMP'14 have been adapted to QF_S by means of a specialized front-end [5]. It is our goal to convince the developpers of SL solvers to adopt SMT-LIB as the native input language of their tools. For this purpose, we provide a C++ front-end [4] that can be used to parse and type check SL inputs encoded in SMT-LIB using the current specification.

References

- 1. Constantin Enea, Mihaela Sighireanu, and Zhilin Wu. On automated lemma generation for separation logic with inductive definitions. In *ATVA 2015*, *Proceedings*, pages 80–96, 2015.
- Andrew Reynolds. Cvc4 separation logic format, 2016.
 URL: http://church.cims.nyu.edu/wiki/Separation_Logic.
- 3. Andrew Reynolds, Radu Iosif, Cristina Serban, and Tim King. A decision procedure for separation logic in SMT. In *Automated Technology for Verification and Analysis 14th International Symposium*, ATVA 2016, Chiba, Japan, October 17-20, 2016, Proceedings, pages 244–261, 2016.
- 4. Cristina Serban. Smt-lib front end, 2016. URL: https://github.com/cristina-serban/parse-smtlib.
- Mihaela Sighireanu. QF_S front end, 2014.
 URL: https://github.com/mihasighi/smtcomp14-sl/tree/master/smtlib2parser-1.4.
- Mihaela Sighireanu. The qf_s logic, 2014.
 URL: https://github.com/mihasighi/smtcomp14-sl/wiki.
- 7. Mihaela Sighireanu and David Cok. Report on sl-comp 2014. *Journal on Satisfiability, Boolean Modeling and Computation*, 1, 2014.