Semnale control MIPS16 pentru Anexa 5

Instrucțiune	**Opcode** *Instr(15-13)*	**RegDst**	**ExtOp**	**ALUSrc**	**Branch**	**<Br?>** (opțional)	**Jump**	**JmpR** (opțional)	**Mem Write**	**Memto Reg**	**Reg Write**	**ALUOp (1:0)**	**func** *Instr(2-0)*	**ALUCtrl (2:0)**
Add	000	1	0	0	0		0		0	0	1	00 (tip R)	000	000
Sub	000	1	0	0	0		0		0	0	1	00 (tip R)	001	001
Sll	000	1	0	0	0		0		0	0	1	00 (tip R)	010	010
Srl	000	1	0	0	0		0		0	0	1	00 (tip R)	011	011
And	000	1	0	0	0		0		0	0	1	00 (tip R)	100	100
Or	000	1	0	0	0		0		0	0	1	00 (tip R)	101	101
Xor	000	1	0	0	0		0		0	0	1	00 (tip R)	110	110
Srlv	000	1	0	0	0		0		0	0	1	00 (tip R)	111	111
Addi	001	0	1	1	0		0		0	0	1	01 (+)	Xxx	000
Lw	010	0	1	1	0		0		0	1	1	01 (+)	Xxx	000
Sw	011	0	1	1	0		0		1	0	0	01 (+)	Xxx	000
Beq	100	0	1	0	1		0		0	0	0	10 (-)	Xxx	001
Ori	110	0	0	1	0		0		0	0	1	11 (|)	Xxx	101
J	111	0	0	0	0		1		0	0	0	Xx	xxx	xxx

<?> ϵ {_gez, _ne, _gtz}

Tipuri de operații care se pun în paranteză la ALUOp si ALUCtrl: {(+), (-), (&), (|), (^), (<<*l*), (<<*lv*), (>>*l*), (>>*a*), (<)}, & - AND, | - OR, ^ - *XOR, l - logic, a - aritmetic, v - cu variabilă*

URL: https://drive.google.com/file/d/1SI7x2Gp_2m3SEkwnXuGt4ns4voYzpGBH/view?usp=sharing