

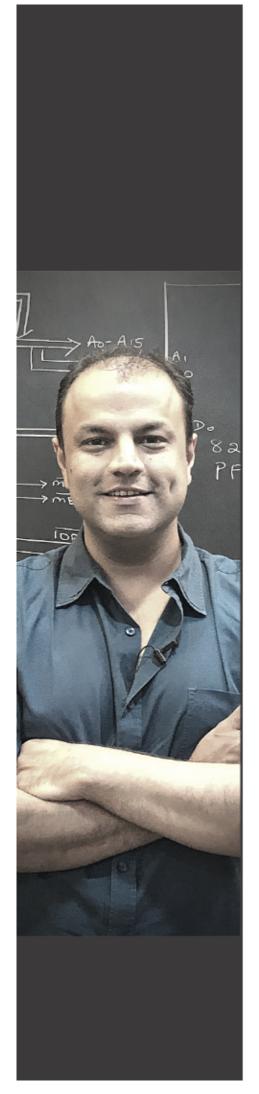


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#### Video Courses for You

- Microprocessor | 8085
- Microprocessor | 8086
- Microprocessor | 80386
- Microprocessor | Pentium
- Microcontroller | 8051
- Microcontroller | ARM
- Computer Organisation & Architecture

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# Microprocessor 8085

## **List of Video topics**

- Introduction
- Architecture
- Flag Register
- Addressing Modes
- Instruction Set (Every Instruction with examples)
- Assembly Programming (Several Videos)
- Timing Diagrams of Machine cycles
- Timing Diagrams of Instructions
- Stacks & Subroutines
- Delays & Delay Routines
- Interrupt Structure
- SIM and RIM Instructions with Sq. Wave Program
- 8259 PIC (Theory, Programs, Interfacing)
- 8255 PPI (Theory, Programs, Interfacing)
- 8254 PIT (Theory, Programs, Interfacing)
- 8237 DMAC (Theory, Programs, Interfacing)
- Full System Memory & I/O Designing
- Memory Mapped I/O and I/O Mapped I/O
- Many more...

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# Microprocessor 8086

## **List of Video topics**

- Introduction
- Pipelining
- Memory Segmentation
- · Memory Banking
- Architecture
- Flag Register
- Addressing Modes
- Instruction Set (Every Instruction with examples)
- Assembler directives
- Assembly Programming (Several Videos)
- DOS Interrupt INT 21H
- Interrupt structure with IVT
- Minimum Mode with Timing Diagrams
- Maximum Mode with Timing Diagrams
- 8086-8087 Interfacing
- 8259 PIC (Theory, Programs, Interfacing)
- 8255 PPI (Theory, Programs, Interfacing)
- 8254 PIT (Theory, Programs, Interfacing)
- 8237 DMAC (Theory, Programs, Interfacing)
- Full System Memory & I/O Designing
- Many more...

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# Microprocessor 80386

## **List of Video topics**

- Introduction
- Real and Protected Mode
- EFlag Register
- Control Registers
- GDTR, LDTR, IDTR and TR
- Segment translation
- Segment Descriptor format
- Page Translation and TLB
- Protection Mechanism
- Many More topics coming soon:
- Architecture\*
- Pin Diagram\*
- Timing Diagram (Pipelined & Non Pipelined cycles)\*
- Multitasking

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# Microprocessor Pentium

List of Video topics

- Introduction
- Superscalar Architecture
- Integer Pipelining
- Floating Point Pipelining
- Branch Prediction Mechanism
- Code Cache Mapping
- Data Cache Mapping

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# Microcontroller 8051

## List of Video topics

- Introduction
- Architecture
- Flag Register PSW
- Memory Organisation
- Internal RAM
- SFRs
- Addressing Modes
- Instruction set (All instructions with Examples)
- Short, Long and Absolute jumps
- Boolean Bit wise operations
- Programmers Model
- Assembly level programs (Several Videos)
- Timer section (Theory and Programs)
- Serial Port (Theory and Programs)
- Interrupt Structure
- Power Saving Features
- Full system designing
- · Matrix Keyboard Interfacing with program
- ADC Interfacing with program
- DAC Interfacing with program
- LCD Interfacing with program
- Many more...

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# Microcontroller ARM7 TDMI

**List of Video topics** 

- Introduction
- RISC Philosophy
- RISC v/s CISC
- ARM Core CPU Architecture
- ARM7 Registers (Programmers model)
- CPSR Flag Register
- Pipelining
- Interrupts and Exceptions
- Operating Modes
- Addressing Modes
- Instruction set (All instruction with examples)
- ARM7 Assembly language programming

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# Computer Organisation & Architecture

## List of Video topics

- Introduction
- Von Neumann Model
- · Number representation
- · Adder Subtractor circuits
- Multiplication including Booth's Algorithm
- · Restoring Division
- Non Restoring Division
- Floating Point Number Representation
- · Floating Point Conversion Numericals
- Floating Point Addition Algorithm
- · Instruction Formats and Types
- Instruction cycle state transition
- Pipelining
- Micro-Operations
- Hardwired Control Units
- Microprogrammed Control Units
- Micro-Instruction Sequencing
- Horizontal v/s Vertical Micro Instruction Formats
- Nano-Programming
- Memory Hierarchy
- Memory characteristics
- Virtual Memory Management Paging & TLB
- · Page Replacement Theory & Numericals
- Cache Architectures
- Cache Mapping Techniques
- Bus Arbitration
- Memory Mapped I/O v/s I/O Mapped I/O
- Interrupt Driven I/O
- DMA based I/O
- Flynn's Classification
- Many more...

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