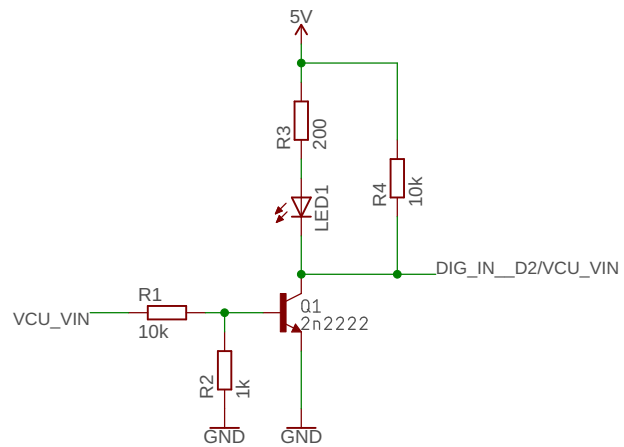
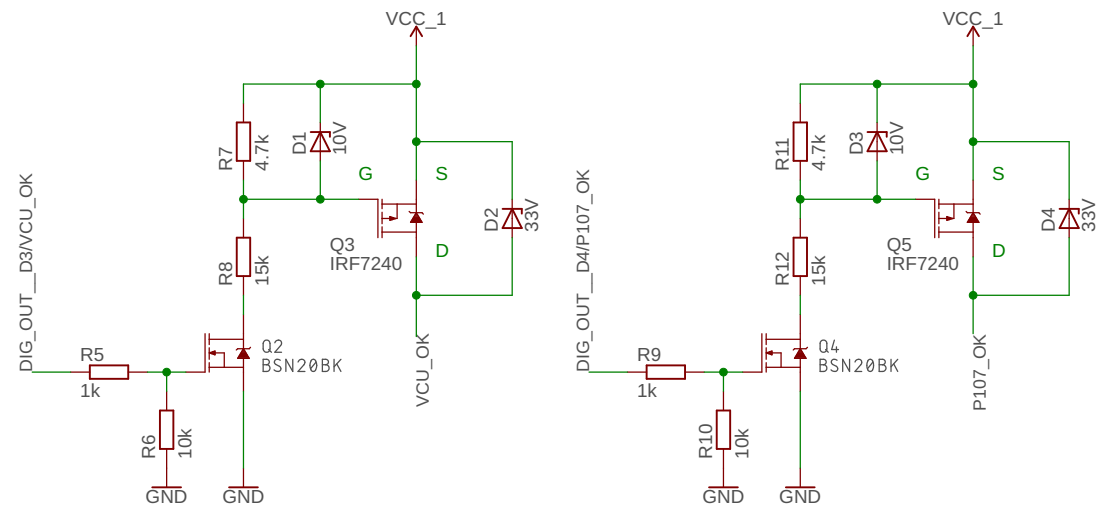


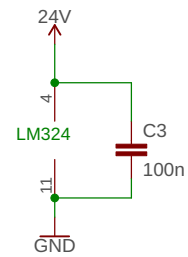
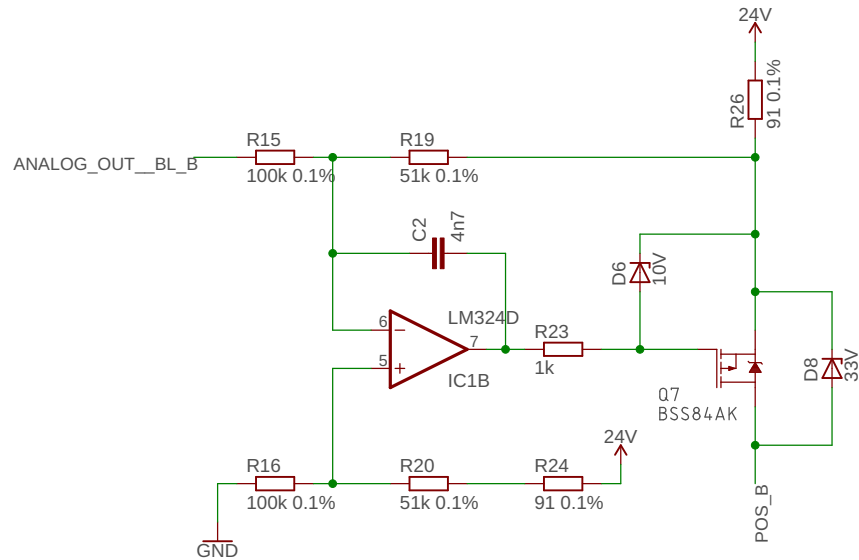
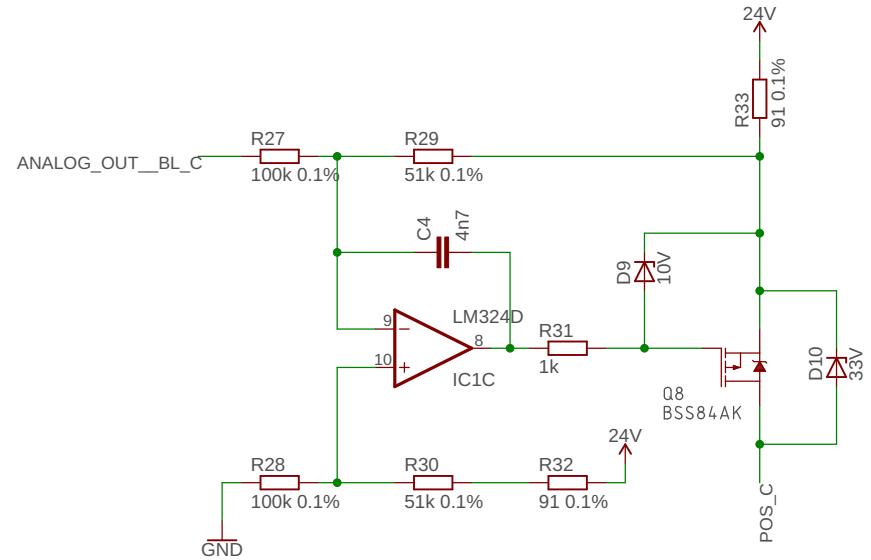
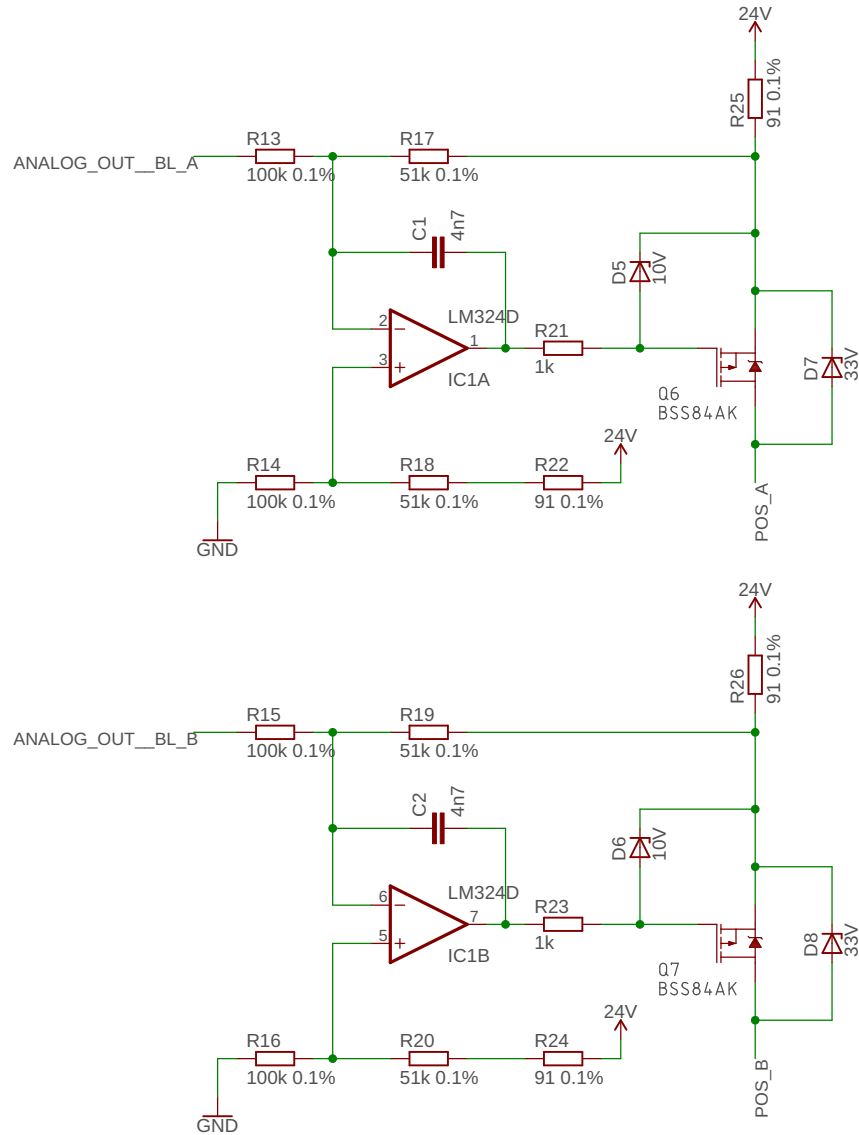
Digital Read



Digital Write

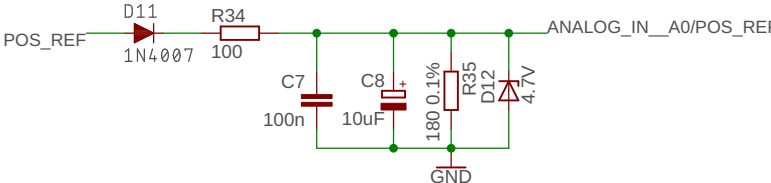


Analog Write

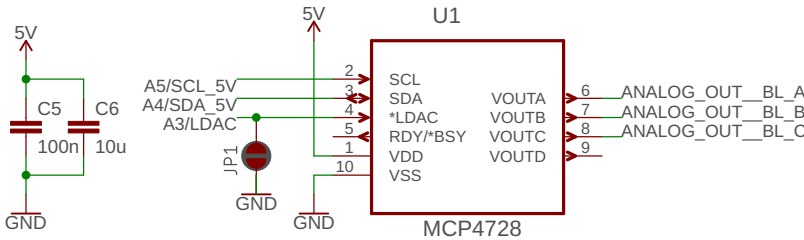


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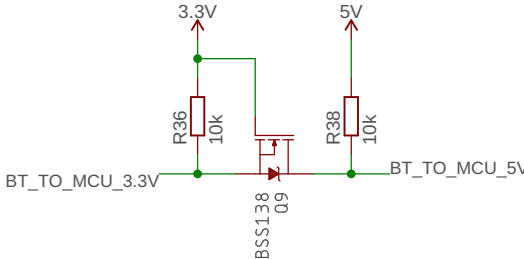
Analog Read



DAC

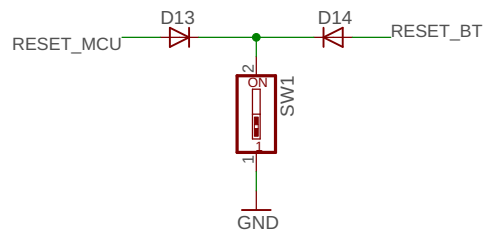
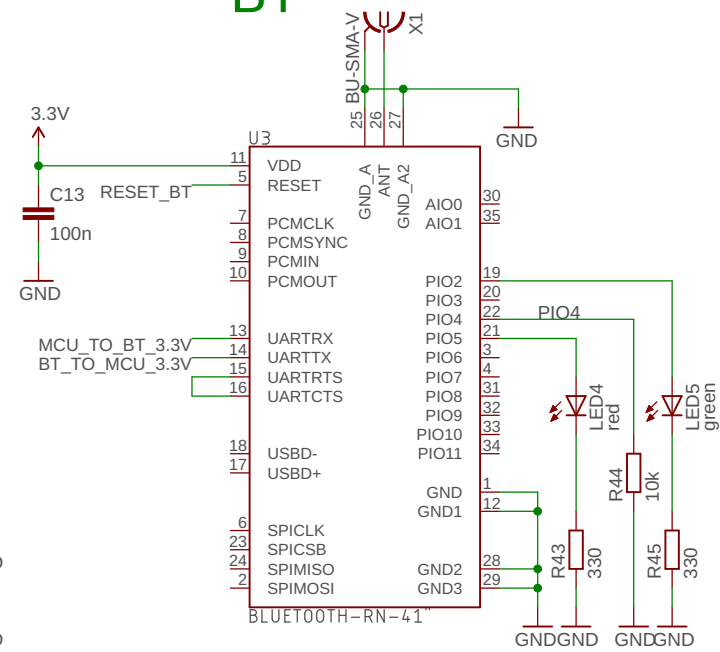
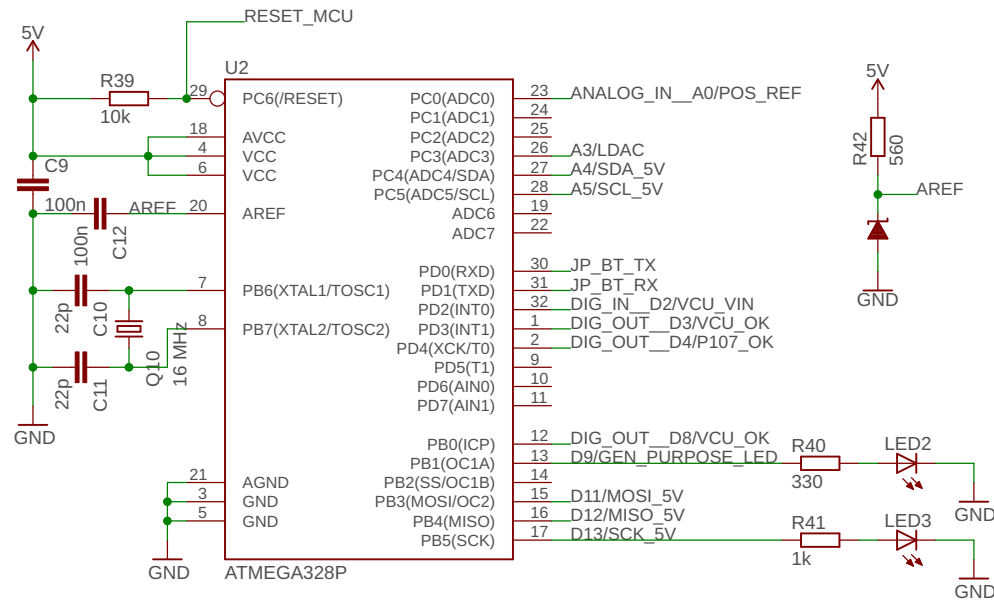


Logic Shifting



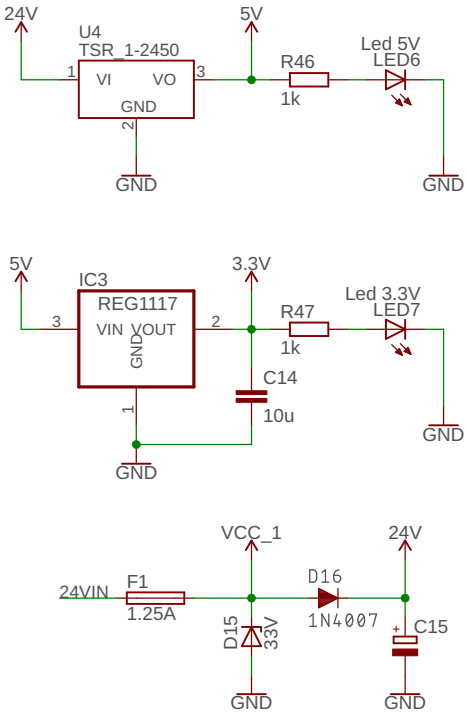
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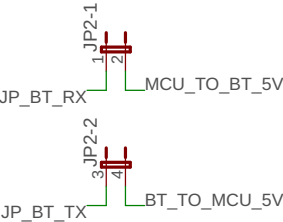
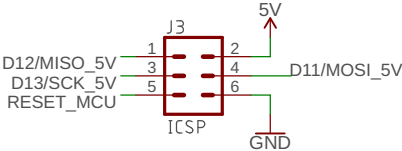
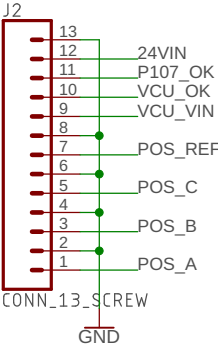


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Power



Headers



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