

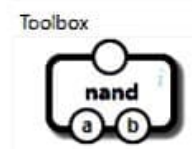
Invert

The next task is to build an inverter (**inv**) component.

An **inv**-component has a single input and a single output.

The output should be the opposite of the input, so 0 for 1 and vice versa:

Input	Output	
0	1	✓
1	0	✓



✓ Level successfully completed!

Input	Output	
0	1	✓
1	0	✓

1 components used. 1 nand gates in total.

This is optimal!

Next level

Stay at this level

Levels in **Logic Gates**:

✗ Nand

✓ Invert

✓ And

✓ Or


✓ Xor

4 / 5 levels completed.

Input:

0

□



The circuit diagram shows a single NAND gate. Both input lines are connected to the same input signal, which is currently 0. The output line is connected to a light blue box showing the value 1. This configuration effectively acts as an inverter.