



MP-IO PIA/ACIA I/O Board Rev. 1

Bill of Material

Qty	Designator	Description
1	R1	10M 1/4W 5%
1	R2	330R 1/4W 5% (Above LED D2)
2	R3, R4	2k7 1/4W 5% (RXA, TXA)
2	R5, R6	100k 1/4W 5% (CTSA, DCDA)
1	R7	470R 1/4W 5%
1	IC1	40 pin Dual Wipe IC socket (optional)
2	IC2, IC3	16 pin Dual Wipe IC socket (optional)
1	IC4	24 pin Dual Wipe IC socket (optional)
5	C1, C3, C4, C7 C9	100nF 50V Monolithic capacitors 0.2" spacing
2	C5, C6	22pF 100V Ceramic capacitor 0.2" spacing
3	C2, C8, C10	10uF RB 16vW Electrolytic Capacitors 0.1" spacing
1	CN1	24 way Straight 0.1" pin header.
1	CN2	26 way (2x13) 0.1" straight box header.
1	CN3	50 Way (1x50) 0.1" spacing pin header (see text for card edge connector details)
1	CN4	6 Way (1x6) R/A 0.1" spacing pin header.
8	'A', 'B', 'C', 'PA', 'PB', 'RSO', 'RSI', 'PWR SEL'	3 Pin 0.1" spacing pin headers.
1	J3	24 way (3x8) 0.1" spacing pin header.
1	J12	(2x9) 18 pin 0.1" spacing pin header.
1	IC1	MC6821 PIA 40 Pin DIP
1	IC2	74HC138 3 to 8 Line Decoder 16 Pin DIP
1	IC3	74HC4060 14 Stage Binary Counter 16 pin DIP
1	IC4	MC6850 ACIA 24 Pin DIP
1	D2	LED 3mm RED
1	X1	2M4576Hz Crystal HC49
1	CN5	Barrel Jack OD5.5 / ID 2.1
11		2 Pin Shunts 0.1"
1	PCB	68retro MP-IO Rev. 1