



**HoGent**

Faculteit Bedrijf en Organisatie

Research of caching strategies in mobile native applications using external data services

Frederik De Smedt

Scriptie voorgedragen tot het bekomen van de graad van  
Bachelor in de toegepaste informatica

Promotor:  
Joeri Van Herreweghe  
Co-promotor:  
Jens Buysse

Instelling: —

Academiejaar: 2015-2016

Tweede examenperiode



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## Abstract

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# Voorwoord

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# Chapter 1

## Introduction

The definition of mobile has changed a lot in the last few years and the expectations of mobile applications continue to rise. Mobile applications should work on a myriad of devices and screen resolutions, and should take battery efficiency into account. Furthermore is an application no longer really considered mobile if it isn't available without a stable internet connection. This makes research about caching in mobile applications really interesting since it can solve multiple problems. Caching of the results of expensive operations, e.g. an internet connection, will allow the system to avoid many of these operations, reducing battery consumption, and can these results be used when a (temporary) connection loss would occur.

Lots of IT companies already implement such a system:

- The Facebook-app still shows several posts and pictures when the user has no internet connection;
- Third-party API's, such as Picasso (Android) and SDWebImage (iOS) retrieve images from the web and have an embedded caching system;
- Twitter allows the user to change account settings offline, which are later synchronized with the backend when a connection is established<sup>1</sup>.

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<sup>1</sup>This is more about persistent data, however it is used to store temporary data that can later be forgotten once it is synchronized with the backend. Just like a cache containing business data.



## 1.1 Problem definition and research questions

However mobile caching is becoming more and more common, there is no real framework on which they all rely. Instead they each have to think about how to implement caching and have to invest in discovering caching strategies and inventing a good caching implementation.

They can decide to ignore the caching problem and focus on the business logic and the actual functional requirements of the project. The chances that the project is able to be completed within the deadline increases, as they have less work to do. However, if there is a non-functional requirement that requires some sort of caching or if the users of the application are complaining about problems that come along (e.g. lots of traffic or battery usage), one would have to implement the caching system afterwards. Which might force the developers to redesign parts of the system (both frontend and backend) or forces them to do this in an incomplete way, introducing lots of bugs that usually come along in caching systems. Every platform already has some infrastructure designed to allow caching, e.g. Guava and a local lightweight database. Yet there is more to caching than simply storing information, developers should think about what data is eligible for caching and how this can improve their application. They should be able to do this with an efficient method, customized to the needs of the application considering all possible events that might occur and how they might be able to handle this. These ideas and strategies should then be implemented in both the backend and frontend, however the backend implementation could be reduced to an almost non-existing implementation, since it is not merely about the native application itself, but about the data flow between the different systems.

Because of these reasons, the following questions will be answered in this article:

- How can a caching strategy efficiently store and fetch data in a native mobile application?
- What life-cycle events should be considered and how can they react to these events?
- How can the cache be synchronized with the external data service (backend)?

# Chapter 2

## Methodology

First, a literature study will take place about caching in general: what concepts and principles are used? Which steps can be differentiated when interacting with a cache? What are some common techniques? Some of these will later be revisited and possibly simplified, to use them on a mobile device.

At the same time, research can be done defining which events are commonly handled by a mobile native application. However the case study will be performed in the Android platform, it is important to consider all platforms (Android, iOS, Windows) and how each of them handles these events.

After these first two steps, we can think about how the cache could respond to these different events and why and when this would be beneficial. Depending on the needs of the cache, the ultimate result should maintain the caches integrity, especially concerning business data. Possibly critical business data could be lost or even worse: wrong or insufficient business data could later be synchronized to and from the underlying data service.

Parallel with step 3, a literature study concerning the synchronization between the frontend cache and the underlying data service will be done. What techniques are commonly used? What happens when there is two-way synchronization and there is conflicting data? Although this is partially out of the scope of the frontend developer, it is important that everyone has a clear view of what they can do to facilitate synchronization. The backend can influence frontend performance and the frontend can influence backend performance!

All these concepts and ideas will then be tested in a case study, which will analyze the performance, from an frontend Android application and a backend Java EE application. When talking about performance, this will be mainly about the performance benefits

on the frontend. Performance benefits of caching in the backend is not in the scope of this thesis.

# Chapter 3

## Basics of caching

A cache can be defined as a (fast) memory structure designed to reduce costs and improve speeds of computer operations. The term caching is widely used in computer science, from the CPU that uses different multi-level caches, to web caches concerning the reuse of already fetched HTTP pages (Fielding, 1999) and all of them are designed to improve performance. Even low-end devices such as microcontrollers can benefit on the usage of a cache (Hruska, 2016). How this memory structure improves the performance is dependent on the use case in which it is used. It could keep the results of long operations or use tokens to check whether data is outdated.

### 3.1 Basic cache performance metrics

The presence of a cache by itself will not cause a performance increase. It is integrated in a software application that uses a cache. The application then checks whether certain data is present in the cache and if not, does everything it needs to do in order to retrieve this data from a third party. In most examples a cache can be represented by a collection of key/value pairs, to which the application requests a value based on a key. When the cache contains the data the application is requesting, it is commonly known as a *cache hit*. Inversely, when the cache does not contain the requested data, it is known as a *cache miss*. The relationship between the total amount of requests and the total amount of hits is the *hit ratio*.  $R = h/n$  where  $R$  is the hit ratio,  $h$  is the total amount of cache hits and  $n$  is the amount of cache requests. These terms are, among others, important factors determining the performance of a cache. A common misconception is that a high hit ratio results in better cache performance, yet this is only true considering hit ratios. If the hit ratio were the only factor, the caches “performance” would be proportional to the size of the cache and a cache of 10Tb always yield better performance than a cache of 20Mb (Wulf and McKee, 1995). This statement can easily be proven wrong when considering other factors such as speed,

as the 20Mb cache will need far less time to produce a result than the cache of 10Tb.

## 3.2 Paging

However caching is of high importance with lots of aspects in IT, caching is what it is today thanks to some specific problems that have received lots of attentions the last decades. Perhaps the most important problem, considering the field of caching, is the problem with *virtual memory* (Denning, 1970). Virtual memory maps virtual memory addresses, provided by an operating system to a program, to actual hardware memory addresses. This way, the operating system can manage its memory resources and decide where to store and retrieve data from programs using virtual memory. This was considered a very elegant way to increase programming productivity, as programmers could use a high-level API to retrieve and store data instead of needing to talk directly to the processor, which would also make them machine dependent.

The operating system can assign priorities to memory addresses and decide whether it should be stored on a fast or slow memory. The memory of an idle process could for example be persisted on slow memory, i.e. hard drive, so that the faster memory, i.e. RAM, can be used for memory of currently active processes. The moment that the idle process wakes up, it can still use the virtual memory pointers, as if the data has never moved. When a process tries to access a piece of data, currently stored on the hard drive, the operating system will produce a *page fault*. This event will cause the *page*, a block of data, containing the desired data on the hard drive to be transferred to somewhere in RAM memory.

The memory transfer due to a page fault is however very expensive as the hard drive is a lot slower than RAM memory and the processor itself. Therefore it is important to have a good paging strategy, an algorithm that decides which pages will be persisted to the hard drive and which pages will remain in RAM memory. The optimal algorithm will only persist the pages that will not be used for the longest period of time.

The paging problem is very similar to finding the optimal caching strategy, RAM memory is the cache, the hard drive is non-cached memory and developers and researchers are looking for an algorithm such that only the least interesting memory is removed from the cache. The principle ultimately solving the paging problem is therefore one of the fundamental principles not only in pages and virtual memory, but all of caching, this principle is called the *locality principle*.

## 3.3 Locality principle

### 3.3.1 What is the locality principle

When people are executing a process, such as preparing dinner, they always tend to gather everything they need before or during the process. If it were a bigger meal the entire meal is divided in a set of subprocesses. When preparing lasagne it could be divided in preparing the tomato sauce, creating each layer using previously prepared ingredients, etc. The principle of dividing a large task in a set of smaller subtasks is called *divide and conquer*. When creating the lasagne layers, one would first collect all supplies needed (tomato sauce, lasagne sheets, etc) and put them somewhere close, e.g. on the counter, once that is done, the layers are assembled. This is more practical and time saving than to continually walk a relatively long distance each time the tomato sauce is needed.

Something similar has been discovered with programs by Denning (2005) when analyzing memory usage of multiprogramming systems using virtual memory in 1967. He formalized this idea through the years and called it the *locality principle* also commonly known as *locality of reference*. He found that programs intrinsically use a very small part of data when performing arbitrary algorithms. In 1980 he formally defined a relationship between the processor, executing the instructions, and the data/object it uses called the *distance* and is denoted as  $D(x, t)$  where  $x$  is the object and  $t$  is the current time. The relevance of this object to the processor at a certain time is based on whether the distance is less than a certain threshold:

$$D(x, t) \leq T$$

where  $T$  is the threshold and  $D(x, t)$  is the distance function. The threshold and the distance are embedded in a, usually 2 dimensional, spatial-temporal coordinate space where the spatial dimension is the memory address of the object and the temporal dimension is the time when it was accessed.

### 3.3.2 Interpreting spatial-temporal distances

Because the distance function  $D$  is operating in a spatial-temporal space, it is based on the definition and interpretation of the two different dimensions. The easiest way to understand how these dimensions can be interpreted is to look at them separately. Once that is understood, there should be a function  $m : S \times T \rightarrow \Theta$  where  $S$  is the set representing the spatial dimension,  $T$  is the set representing the temporal dimension and  $\Theta$  is the set representing the spatial-temporal space.

*Temporal distance*, the distance in time that can be expressed in several ways:

- time since last reference
- time until next reference
- access time to get the object
- a combination of the above or others

The time since the last reference to object  $x$  can be known by storing the references used by the processor and the time when the reference happened in a list. The time until next reference might yield better results when trying to create an effective and efficient cache since it is known when the object will be used again. However this should require a learning agent that can try and calculate when the next reference will happen based on the reference history. The time unit used should be a relative unit such as the execution of an instruction, this is better than an absolute unit such as seconds because it will ignore external factors that shouldn't affect the distance, e.g. other processes running.

*Spatial distance*, the distance to the location of the object can be expressed in several ways as well:

- the amount of hops in a network required to retrieve the contents
- the memory on which the data is stored, e.g. objects stored in RAM have a smaller spatial distance than objects stored on a hard drive
- the number of memory addresses between the object and the addresses currently being used or pointed to
- a combination of the above or others

Intuitively, one could see a relationship between the spatial and temporal locality. When the spatial distance to an object is greater, the temporal distance usually also increases because it will need to cover more ground to retrieve the object. When the temporal distance is greater the spatial distance will usually increase because of pacification of the object: it will be stored on slower memory due to the lack of interaction. When checking the relevance of objects to the processor are checked, with  $D(x, t) \leq T$ , only some objects will pass the inequality, the set of these objects is called the *working set*. It are the objects in the working set that would, among others, be candidates for caching.

## 3.4 CRUD

When interacting with a cache, there are, in general, several operations that each cache is able to do. In general, one would want all operations that allow the developer to read the contents of a cache and to get from an arbitrary cache state, to any other arbitrary cache state through, possibly composite, operations. These operations are known as CRUD operations and are a main part of RESTful web services (Battle and Benson, 2008). This will allow an easier implementation when using Read Through (or Cache Aside) Caches. *CRUD* is an acronym for Create Read Update Delete and as the name suggests, 4 operations can be distinguished:

- *Create* allows some client to add an object to a data set. When adding an object to a cache this will typically invoke an add-function receiving two arguments: a key and a value. SQL implements this with its INSERT-statements and HTTP integrates this using a POST request.
- *Read* is used to get all information or a subset of information from a data set, in a typical cache this will be integrated as a get-function receiving the key of the value one would wish to access. *The key passed to the function is the same key that is passed to the add-function when creating a new cache entry.* SQL implements this with its SELECT-statements, commonly known as queries, and HTTP integrates this using a GET request.
- *Update* will change the contents of an existing object, this is separated from Create as these two might be treated differently, depending on the system in which the data set is embedded. A cache will mostly provide a common API for both Create and Update: a function with a key and a value as its parameters. Yet they might be separated into separate functions to increase readability or to simplify event triggering. SQL implements this with its UPDATE-statement and HTTP implements this using PUT-requests.
- *Delete* is used when an element should no longer be an element of the data set. In a cache this might happen when an object is outdated, or if the object has also been removed in the data service. One would then call a delete-function passing the key of the object that should be removed. SQL implements this with its DELETE-statements and HTTP integrates this with its DELETE request.

(Battle and Benson, 2008) Note how these operations are defined on the database-level, software application level, and networking level. Also note how an interface of a cache should at least contain functions allowing the user to execute all CRUD-operations, yet they are not limited by them.



# Chapter 4

## Cache replacement algorithms

With a concrete list of *what* should be possible with caches, one can discuss *how* this could all be implemented, the algorithms implementing a cache system are called *cache replacement algorithms* or *cache replacement policies*. Yet not just any cache replacement algorithm will do, it is an art to find the most optimal algorithm, the one that will provide the best (performance) results by interacting with it using CRUD-operations. Although most algorithms are only based on CRUD interactions with the cache, some algorithms can be run on separate processes/threads that are then responsible for maintaining the cache even without interactions with the user.

### 4.1 RR

The first cache replacement algorithm that will be discussed is the Random Replacement strategy. Of all the strategies, this one could be considered one of the strategies with the lowest overhead and the easiest implementations (Zhou, 2010). This makes it ideal for certain low-level systems or hardware that cannot afford many processing cycles on cache management.

**Read** When trying to get a certain object by its key, the algorithm can use any sorted or non-sorted collection. When using an non-sorted collection it would check each element in the collection in  $\mathcal{O}(n)$ . Note how this defines the upper bound of the function, therefore it could also be in constant time or  $\mathcal{O}(\log_2 n)$  when performing a binary search on a sorted list.

**Create** When adding an object while the cache is not yet full, it could simply be added to the collection, algorithm-independently. When the cache is already full, the algorithm will randomly remove replace an object from the collection by the new object that should be stored. The operation itself is primitive and easy to program, the only

difficulty is creating a reliable random generator, yet this is not in the scope of this algorithm.

**Update** Updating is similar to *create*, if the key is present it will result in a *read* to get the appropriate object and then changes the object. If the key is not in the cache, it could either *create* object or alarm the user that it is not present.

**Delete** When deleting an object, based on a key, it can be done by first performing a *read* to know whether the key is present and if so removing it. Yet some collections will do this nevertheless, in that scenario the algorithm could simply delegate to the delete function from the backing collection.

## 4.2 FIFO

FIFO, acronym for First-In-First-Out, is a cache replacement policy much like a queue. New objects are inserted at one end of the queue, when the cache is full the first object at the other end will be discarded and the new object will be added at the insertion end (Reineke et al., 2007). FIFO is analogous to FCFS, First-Come First-Served, which is used in OS scheduling algorithms. Note that the hit ratio performance of the FIFO replacement strategy is like the hit ratio performance of the RR replacement strategy (Rao, 1978).

**Read** A read in a FIFO policy results in a queue being scanned in  $\Theta(n)$ , almost just like RR. The difference between RR and FIFO is that a FIFO cannot be a sorted collection, since its performance is based on a “constantly” defined sorting structure, this means that one cannot just swap two elements in the cache and expect it to work just the same: the actual state of the cache has changed. Just like RR, a read does not change the state of the cache.

**Create** When adding a new object to the cache it will, without loss of generality, be placed on the left side of the queue. When the queue is full, the first element on the right will be removed and all other objects will be “moved to the right” (however this is not an actual operation that occurs in an efficient queue implementation), clearing a space on the left, ready to be filled in by the new object.

**Update** When updating an existing object, based on a key, there will be a read that will then change the object at the index  $i$  by the new object. After the object  $x$  is updated,  $x$  should then be removed from the queue, every element left of  $x$  should

then be moved to the right and  $x$  is again added to the left side of the queue. Note that all these operations should be executed in a procedural or atomic way.

**Delete** When deleting an existing object, the algorithm scans the queue until it finds the index with value  $x$  corresponding to the given key. It will then remove  $x$  and move all elements left of  $x$  to the right. The next object added to the queue will therefore not cause the object (completely) at the right to be removed from the cache.

### 4.3 CLOCK

This cache replacement algorithm produces similar results as the FIFO implementation described above, yet it uses a different internal algorithm to accomplish these results. It uses a circular collection, which repeats from the start when trying to retrieve the element after the last element. Using a queue this could be implemented by linking the next pointer of the last element to the first element (Kogman, 2010). Using an array one can always use a modulus operator based on the size of the array to have get an index that is always lower than the size of the cache. At any given time there will be an index pointing to the “current” item, this is used throughout the text as the index pointer or  $R$ , using an array this can be an integer, using a linked list this can be an entry holding a value and references to the next and previous elements.

**Read** Linearly scans the cache starting at  $R$ , scan ends when  $R$  is the same as when the operation started or if the key is found. If the key is found, the key will be registered as being referenced, which could be a boolean or integer.

**Create** If the cache is full, check whether the element at  $R$  is referenced, if not replace the element with the new element and increment  $R$ , otherwise increment  $R$  and repeat this operation. Worst case scenario, every element was initially referenced. In this scenario after having added the new element, every element will not be referenced and the element at the initial  $R$  will be replaced. If the cache is not yet full, add the element before the element at  $R$  (in a linked list this will be in  $\mathcal{O}(1)$ ) and do not change  $R$ . This allows the cache to always stay in a consistent FIFO state, as the cache can, at any point remove or read the cache in the correct order.

**Update** Find the element, update its value, signify it is not being referenced and increment  $R$  to point to the next element. If the element is not yet in the cache, this is the same as a create operation.

**Delete** The element is read in the cache and removed and the size of the cache is decremented. When using a linked list, the algorithm will, indirectly, connect the previous element and next element to each other. When using an array one would need to pull every element after  $R$  to the left, decrementing their indexes. This means that the linked list implementation runs in  $\mathcal{O}(1)$  while the array implementation runs in  $\mathcal{O}(n)$ .

## 4.4 LRU

An *LRU-cache* (Least Recently Used Cache). There are lots of variants on LRU and therefore it can also be seen as a cache replacement algorithm family. The probability of an object being present in a cache, the hit rate  $h(n)$ , can be approximated by  $h(n) \approx 1 - e^{-q(n)t_C}$ , where  $n$  is the object being requested,  $q(n)$  is the popularity of the object (see *Zipf's law*) and  $t_C$  being the (unique) root of  $\sum_{n=1}^N (1 - e^{-q(n)t}) = C$ , with  $N$  being the maximum amount of objects in the cache and  $C$  the cache capacity. This approximation is called the *Che approximation* (Fricker et al., 2012).

The LRU algorithm is all about replacing the least recently used object, in other words, when a new object should be added to a full cache, it will remove the object that hasn't been accessed for the longest period of time. To know when an object has last been accessed, the usual implementation uses a map, let's call it the *key history map* that can get a timestamp (this can be in milliseconds or can be some relative time unit) based on a key. However it could also be implemented by a queue, where the order of the queue determines which objects have, relatively, been accessed most recently.

**Read** Retrieving an object based on a key, first boils down to retrieving a value from any collection, just like RR. When the object is found it will not immediately return this, yet it will firstly access the *key history map* and update the timestamp to its newest value. This way the algorithm will know, in the future, that this key has just been accessed. If the LRU is implemented using a queue, it will, without loss of generality, remove the object  $x$  at index  $i$ , (conceptually) move all objects left of  $i$  to the right (which is equivalent to incrementing the index  $j$  of all objects where  $j < i$ ) and  $x$  will then be inserted at the left side of the queue.

**Create** If the cache is not yet full, the object will be added to the collection and the key history map will be updated with a new timestamp for the specified key. If the cache is full, it will first find the key with the lowest timestamp, which is the object that hasn't been accessed for the longest period of time (considering an increasing time function). It will then remove this key in the collection and remove the corresponding key from

the map, the object will then be added to the collection and the key history map will be updated with a new timestamp for the specified key. When using a queue with a cache that is not yet full, one would add the new object, without loss of generality, to the left side of the queue. When using a queue with a full cache, one will remove the object to the right of the queue, move all remaining objects to the right, and then add the new object to the left of the queue.

**Update** The algorithm will check if the key is already stored in the collection, if not, this boils down to *creating* (adding) a new object to the collection or queue. When the key is stored in the cache, it will update the object in the collection to the new object and then it will update the timestamp of the corresponding key in the key history map. When using a queue, it will remove the object with the specified key from the queue, move all objects to the left of the object to the right, and then add the new object, without loss of generality, to the left side of the queue.

**Delete** To remove an object from the cache, the object is simply removed from both the collection and the key history map. When using a queue, it will remove the object  $x$  from the queue and move all objects left of  $x$  to the right.

(Day, 2012) This is the basic implementation of an LRU cache, yet there are lots of variants. There are algorithms taking a new approach, based on LRU, there are some algorithms that have a highly specialized implementation of LRU, and there are even some implementations that use LRU together with some other cache replacement algorithm to try and get optimal results based on the situation it is in.

## 4.5 MRU

The next algorithm is very similar to LRU, *MRU* is an acronym for *Most Recently Used* and will evict the most recently accessed objects when adding a new object to an already full cache. LRU and MRU should not be seen as “competitors” of each other, but rather a twin where, most of the time, one of the two can be chosen based on the context. If it makes sense that a recently requested object has a high probability of being requested again in the near future, LRU might be a good choice. If it makes more sense that a recently requested object will not be requested any time soon, an MRU cache replacement strategy might be better. Even though one would think LRU would be used most of the time, there are abundant scenarios where MRU would make a lot of sense also. Consider a cache containing Facebook-posts, would it work better under an MRU or an LRU system? To answer this question further research should be done, but MRU makes a very good candidate, considering people tend not to revisit posts they have just visited 10 minutes earlier. If the user were to respond or like the

post, he could however receive notifications of new people posting comments and thus LRU could be more useful.

**Read** Every object in the cache stores an extra bit (or boolean) called the MRU-bit and is used to find out whether the object has recently been visited, let's say that every object in the cache is stored as a tuple  $(b, x)$ , where  $b$  is the MRU-bit and  $x$  is the actual object stored. First the algorithm will check if the object is present in the cache and if so, this will be the index  $i$  and it will set  $b$  to 1 or true. This is used to indicate that it has just been accessed. Next the algorithm will check if there exists another index where the MRU-bit is not 1, if so the algorithm ends, otherwise it will flip the MRU-bit at every index back to 0 or false, this is called a *global-flip*. This is the same as checking if  $\exists i \in I : B(i) = 0$ , where  $I$  is the collection of all indexes and  $B : I \rightarrow \{0, 1\}$  is the function that gets the MRU-bit of the object at an index. This can be refactored as  $\forall i \in I : B(i) = 1$ , so checking for the existence of an index with an MRU-bit at 0 or false is the same as checking whether every MRU-bit is 1 or true.

**Create** When the cache is not yet full, the next object will be added to the end of the cache and its MRU-bit will directly be set to 1 or true (ensuring it will not be replaced before the next *global-flip*). If the cache is full it will look for an index  $j$ , such that  $B(j) = 0$  and  $\forall i \in I : i < j, B(i) = 0$ , so  $j$  is the first index with a 0 or false MRU-bit. Note that there will *always* be at least 1 index with a 0 or false MRU-bit due to global-flipping.

**Update** If the object is not yet present in the cache, it will delegate the object to *create*. If it is in the cache, it will replace the object, while keeping it at the same position, and set its MRU-bit to 1 or true. If the MRU-bit was previously 0 or false, it is important to check if a *global-flip* should be executed or not. Yet it is always safe to check for this, even if the MRU-bit hasn't changed (and already was on 1 or true).

**Delete** To remove an object, the algorithm first checks whether the object is present in the cache, if it is not then it doesn't have to do anything, if it is present at index  $i$ , it moves the object at every index  $j$  such that  $j > i$  to the left which is the same as decrementing  $j$  by one, so  $j \leftarrow j - 1$ .

(Guan et al., 2014) Having discussed the basic MRU and LRU algorithms one can now respond to temporal localities with different characteristics.

## 4.6 LFU

LFU, acronym for Least Frequently Used, is a cache replacement algorithm that looks at how many times an object is accessed. This means that the object that has been accessed the least will be evicted when adding a new object to an already full cache. Just like MRU and LRU, this cache is only beneficial in specific scenario's.

When creating a cache for a web server, static pages, images, css-files, etc. are stored in a cache. This cache will then be accessed with every request to check for its presence, before retrieving the file from some other slower source. One might think LRU is a good idea since the most popular, and therefore the most requested, objects will be present in the cache. This is true to some extent, yet only in some concrete scenario's. Imagine that the "GET /index.html" HTTP request would call  $n$  different objects in the cache. If the LRU-cache were to have a maximum size limit  $N$  such that  $N < n$ , the cache will be adding new elements to the cache to later remove it during the same request. This is where LFU might be more meaningful, even with a maximum size limit  $N < n$ . The LFU algorithm will give a better ranking to common objects that would be accessed over lots of different request, say a logo or common CSS file.

There are several implementations of an LFU cache, yet the algorithm from Shah and Matani (2010) will be discussed, which ensures  $\mathcal{O}(1)$ . This implementation uses two data structures, (1) a hashmap where an element can be accessed in  $\mathcal{O}(1)$ , this is possible if the hash function used is an *injective* function with an upper limit  $u$ . It could then map each hash to a single array index of an array of size  $u$  which can obviously be stored, retrieved, removed and updated in  $\mathcal{O}(1)$ . (2) A frequency list, which is a bidirectional linked list that contains an element having a number marking its frequency and having another bidirectional linked list of all cache objects. These cache objects each hold a reference to the frequency element they are in.

**Read** The hashmap is used to retrieve the cache object based on a key. It will then use its reference to get the frequency element it is in, and use the frequency list to, without loss of generality, traverse to the next element on the right, which should be the next ordered frequency node. If the object were already referenced once, therefore it would be in the 1-node, it would then go to the 2-node. If this node does not exist, it will create it and add it to the right of the current frequency node and to the left of the next frequency node. Now that the cache object is under the next frequency node, the algorithm will check whether the previous frequency node still contains cache objects, if not it will remove the frequency node from the frequency list. Note that however there are particularly more steps than some other algorithms, it is still in  $\mathcal{O}(1)$ .

**Create** When the cache is not yet full, it will add the object to the first node of the frequency list, which is the 1-node signifying the object has been referenced once, and will possibly create this node if it is not yet present. If the cache is already full, the first frequency node will be taken (mostly yet not limited to the 1-node) and from there will take any of the linked cache objects and remove it from the linked list and from the hashmap. After this it can just add the object as the cache is no longer full.

**Update** To update an object, the algorithm will get the cache object, based on its key, from the hashmap and then update the contents of the cache object. After this it will get the frequency node, with frequency number  $n$ , based on its reference, go to the  $(n + 1)$ -node (which is possibly created if it does not yet exist), and adds it to its linked list. If the object does not exist in the cache it could either throw an appropriate exception or delegate to the *create* procedure.

**Delete** To remove an object from the cache, the algorithm will first retrieve the object from the hashmap, based on the key. It will then remove itself from the linked list from the frequency node it is in, possibly removing the frequency node if there are no more elements in it. And last but not least it will remove the object from the hashmap based on the key.

Note how the constant execution time of the algorithm fully depends on the hashmap implementation used. If it is an implementation such as described above, it will provide a constant execution time, yet if it were  $\mathcal{O}(\log n)$  or  $\mathcal{O}(n)$ , the algorithm would no longer have the constant execution time.

## 4.7 LIRS

*LIRS*, *Low Inter-reference Recency Set Replacement Policy* (Jiang and Zhang, 2002), is a cache replacement algorithm based on LRU. The major performance problem with LRU is that it always assumes that the object that has been referenced the least recent is the one that will not be accessed for the longest period of time. Yet in some (even trivial) cases, this is simply not the case: imagine a relational database with a B-tree for efficiently retrieving  $N$  indexes for a set of  $N$  records where there exists a bijective function from an index to a record. In the cache blocks of data are stored with a fixed-size where an index is made up of  $I$  blocks and a record is made up of  $R$  blocks, therefore there are  $I \cdot N$  index blocks and  $R \cdot N$  record blocks, where  $I < R$ . Because every operation on a record will first find the record based on the index B-tree, the probability of an index block being accessed will be higher than the probability of a record block being accessed. Yet in a simple LRU-cache it will most of the time favor record blocks in its cache as those are the last referenced blocks in a typical



database operation. A cache favoring index blocks would however be more efficient as it increases the cache hit ratio. *LIRS* tries to solve such a problem by looking at its *IRR*, *Inter-Reference Recency*, which is the amount of referenced blocks between the last and second-to-last reference of a certain block. Say there exist objects *A*, *B* and *C* the following reference history could exist:

*ABCBABCCBCABC*

The initial *IRR* of *A* is 3 (with *BCB* in between) and the next is 5 (with *BCCBC* in between). When adding a new object to the cache *A* will be the first object to be replaced because it has the highest *IRR*. In a cache implementation there is a set of objects with a low *IRR* (*LIR*) and a set of objects with a high *IRR* (*HIR*), the cache mainly exists of *LIR* objects, which are never considered for replacement. A small subset of the cache's memory is used for *HIR* objects, that are eligible for replacement. When an *HIR* object has a lower *IRR* than some *LIR* object, the two will be swapped and the *LIR* object becomes an *HIR* object and the *HIR* object becomes an *LIR* object.

An *LIRS* cache consists of a data structure called a *LIRS stack*, which contains two queues: one used for *LIR* objects (*LIR stack*), of size  $L_{lirs}$  and one used solely for *HIR* objects (*HIR stack*), of size  $L_{hirs}$ . In general  $L_{lirs} \gg L_{hirs}$ , where  $L_{hirs}$  could even be only 1% of  $L_{lirs}$ . The object on top of each queue is the most recent accessed object and the bottom object is the least recently accessed object, similar to an *LRU stack*. Stack pruning, which is commonly used in the *CRUD* operations, is the removal of *HIR* objects from the bottom of the stack until a *LIR* object is present at the bottom.

Every object in the *LIRS stack* is either a *LIR* or a *HIR* object, if it is a *LIR* object, it will always be fully accessible in the *LIRS stack* and will not be (completely) removed when adding a new object. Every *HIR* object is either a *resident HIR object* or a *non-resident HIR object*. A *HIR* object is *resident* if it is present in the *HIR stack*, otherwise — if it is only present in the *LIR stack* — it will be *non-resident*.

**Read** When referencing an *HIR* object it will be added to the top of the *LIR stack*, removing the bottom element of the corresponding stack and a stack prune will be conducted. If the *HIR* object was already in the *LIR stack* it is promoted to an *LIR* object and its reference in the *HIR stack* is removed. If the *HIR* object wasn't yet present in the *LIR stack* its reference is placed on top of the *HIR stack*, yet there shouldn't be any object removed from the *HIR stack* as there is no new item that was added. If the object is a *non-resident HIR object*, it will still promote to a *LIR* object, yet nothing will need to be done on the *HIR stack* as it wasn't present in that stack, by definition. If the object is not yet in the cache, this results in a create operation.

**Create** When adding a non-existing object to a partially filled LIRS cache, the object will be added to the top of the LIR stack and to the top of the HIR stack. If the LIR stack is already full, the object will be added to the HIR stack, possibly removing the least recent (bottom) object from the stack if it was already full. Note how the removed HIR object could still have a reference in the LIR stack, which would now be a non-resident HIR object.

**Update** When changing an existing object in the cache it will be the same as a read operation where the underlying value is changed. It is imperative that this is not simply a delete-create operation, as the replacement policy would then forget this objects history.

**Delete** When deleting a resident HIR object present in the LIR stack, it is removed from the LIR stack and placed on top of the HIR stack. When deleting a non-resident HIR object, it is simply removed from the LIR stack and will therefore be completely forgotten by the LIRS cache. When deleting a HIR object not present in the LIR stack, the object is removed from the HIR stack and will also be completely forgotten. If the object is a LIR object, it will become a HIR object and will be placed on top of the HIR and LIR stack, possibly removing the bottom element of the HIR stack.

Note how all operations are reduced to operations on queues, therefore the performance of this cache replacement algorithm is directly related to the performance of queues. LIRS is based on IRR and recency, yet neither are explicitly stored or even mentioned in the implementation of the CRUD operations. This is thanks to the carefully defined data structures used in the cache, which have the intrinsic property of implicitly maintaining this information. This not only reduces memory usage, but also allows the algorithm to find the object with the highest IRR (and lowest recency) in  $\mathcal{O}(1)$ .

When removing the bottom element of the LIR stack there will always be a, possibly implicit, stack prune to maintain the integrity of the stack and make sure that the bottom element of the LIR stack is always a LIR object.

## 4.8 ARC

ARC, Adaptive Replacement Cache, is a cache replacement algorithm that stores multiple collections and wraps around an existing replacement algorithm like LRU. It stores two main collections:  $L1$  and  $L2$ , each respectively divided into  $T1$  and  $B1$ , and  $T2$  and  $B2$ .  $L1$  consists of all elements that have “recently” been referenced exactly once,  $L2$  consists of all elements that have “recently” been referenced more

than once. Where  $c$  is the total cache size,  $|L1| = |B1| + |T1| = p$ ,  $|L2| = |B2| + |T2| = c - p$  and  $0 \leq p \leq c$ . Some internal replacement algorithm, like LRU, is responsible for independently maintaining  $T1$ ,  $T2$ ,  $B1$  and  $B2$ . Without loss of generality, when  $|T1| > p$  due to the introduction of a new element in  $T1$  or the decrease of  $p$ , the least recently used (LRU) element in the LRU cache maintaining  $T1$  is passed down to the most recently used (MRU) side of  $B1$ , possibly removing the LRU element in  $B1$  if  $B1$  was already full. When the algorithm refers to updating  $p$ , this means that  $p = \min(c, p + \max(|B2| / |B1|, 1))$  if in context of  $L1$  and  $p = \min(c, p - \max(|B1| / |B2|, 1))$  in context of  $L2$ . The  $p$  replacement is what makes ARC dynamic, without loss of generality,  $p$  is incremented by the ratio between  $B2$  and  $B1$ , yet is at least incremented by 1 if the ratio is less than 1, and will never exceed  $p$ .

**Read** Consider  $x$  to be the referenced element. If  $x \in T1 \cup T2$ , the value is returned and  $x$  is placed on the MRU side of the LRU cache at  $T2$ , possibly updating  $p$  with  $L2$ . If  $x \in B1$ , and therefore  $x \notin T1 \cup T2 \cup B2$ , then  $p$  is updated in context of  $L1$ ,  $x$  is added to  $T2$  and if  $T2$  becomes too large because of it, the LRU element from  $T2$  is placed on the MRU end of  $T1$ . If  $x \in B2$ ,  $p$  is updated in context of  $L2$ ,  $x$  is added to  $T2$  and if  $T2$  becomes too large because of it, the LRU element from  $T2$  is placed on the MRU end of  $T1$ .

**Create** If  $x \notin L1 \cup L2$  and  $|L1| = c$ , remove the LRU element of  $B1$  or remove the LRU element of  $T1$  if  $|B1| = 0$ . If  $x \notin L1 \cup L2$ ,  $|L1| \neq c$  and  $|L1| + |L2| = 2c$ , delete the LRU element of  $B2$  and update  $p$  in context of  $L2$ .  $x$  is added to  $T1$ , possibly moving the LRU element of  $T1$  to  $B1$ , possibly removing the LRU element of  $B1$ . If  $x \in L1 \cup L2$ , the element is updated rather than created.

**Update** When the element is updated, this boils down to the read operation, yet the value of the element is also updated. It is not the same as delete and create, as the element history would then be lost.

**Delete** Delete removes the element from the cache it is in, which is either  $T1$ ,  $T2$ ,  $B1$  or  $B2$  and  $p$  is not updated accordingly. The removal of an element shouldn't increase the size reserved for that cache, if anything it should be reversed.

(Megiddo and Modha, 2004) Note how in the average consistent  $|T1| + |T2| = c$  and  $|B1| + |B2| = c$ . Because of this the total amount of elements is  $2c$ , if a developer wishes to set a maximum amount of elements, he/she should set  $c = n/2$ , where  $n$  is the maximum amount of elements in the entire cache.

## 4.9 Bélády's algorithm

*Bélády's algorithm* is considered the most optimal cache replacement algorithm available, as it discards the objects he knows will not be needed for the longest period of time (Al-Samarraie, nd). Yet there are no claims whether this is the most optimal cache replacement algorithm considering a dynamic cost for retrieving uncached data. Therefore it could be considered the most efficient *page replacement policy* where every page is a fixed size, yet with the dynamic size of objects in non-page caches this is not the case.

# Chapter 5

## Cache benchmarking

In this chapter a detailed explanation of a case study and its results is given. The case study is used to answer the first, most fundamental, research question: *How can a caching strategy efficiently store and fetch data in a native mobile application?*. The case study was done on top of the Android platform, and the actual benchmark was executed on an Android Lollipop 5.0 (API Level 21) XHDPI phone emulator, with 1GB of RAM, a x86 CPU architecture, 2 CPU cores and a screen resolution of 720x1280. The emulator uses the Hyper-V hypervisor and ADB to run and interact with the emulated device (Moth, 2014).

### 5.1 Reproducing results

#### 5.1.1 Run environment

The entire application has been developed in Android Studio 2.1 and therefore it is important to install or update Android Studio 2.1 to make sure there are no issues when trying to build and run the application. The benchmark can be run in every Android environment with an Android version of 21 or higher. Setting up such an environment can be done by using an emulator like Genymotion (which uses VirtualBox) or Visual Studio Android Emulators (which uses Hyper-V), or could be an actual Android device. As long as Android Studio is able to detect and deploy to the device. In case that none of these options work there are several emulators that can be used when installing Android Studio, yet these are unrecommended (Google Inc, 2016). Note how it might be best to connect a physical device to the power net as the app can consume lots of resources.

### 5.1.2 Running the app

One can get the project by cloning the git repository of this thesis by navigating to a directory and issuing the following command in the command prompt:

```
git clone https://github.com/frederikdesmedt/Bachelorproef
```

Now there will be a directory *Bachelorproef* where there will be several directories, the *CacheBenchmarking* directory is the project root. One can import this project in Android Studio by navigating to *File* (upper-left corner), *New*, *Import project* and navigating to the project root. These steps might be slightly different based on the host OS. Android Studio will execute some Gradle tasks, which might take a while. Once all Gradle tasks have been successfully executed one can run the application by pressing the Play button (or pressing *shift-f10*) and selecting the device on which the application should run.

The app will open on the device displaying a single loading indicator, telling the user it is running the benchmarks. The application is constantly logging informative and verbose messages, which can be viewed in *logcat* in Android Studio. The complete benchmark can take up to a few hours, so it is recommended to reduce the amount of benchmarks (see the following section).

When the benchmark is complete, the loading indicator will disappear and the app will tell the user it is done running the benchmarks. In *logcat* the app will have generated an extensive list of tables and runtime logs, which can be used to easily compare different configurations.

## 5.2 Code overview

### 5.2.1 Cache benchmark configuration

A cache benchmark configuration is the main unit used throughout the entire app and will run a predefined amount of iterations or time. The benchmark configuration will only record the bare essentials, meaning that a read-based benchmark will only record the time it takes to read a value from a cache. Everything concerning maintenance is executed outside of the recording which makes the benchmark far more reliable. In the API there is a clear distinction between what code should be benchmarked and what code is created to make the benchmark work. Every non-trivial method will therefore always clearly inform to the reader whether the method will be recorded or not. A typical benchmark run will look like the following:

```
// Create cache and other expensive single-run operations
setup();

// For every iteration in the benchmark
for (...) {
    // Prepare the cache to run a single operation
    prepare();

    // Generate a random input key-value pair
    Pair<K, V> input = generateInput();

    // Start recording
    startRecording();

    // Run the operation a single time
    result = run(input.key, input.value);

    // Stop the recording
    endRecording();

    // Clean cache based on the result and input
    cleanup(input.key, input.value, result);

    // Log the recording
    logRecording();
}

// Release all cache resources
tearDown();
```

In the pseudo-code above, several phases can be distinguished, some of them might be irrelevant based on the operation that is being benchmarked. The *setup()* should create a single cache, which will be used for benchmarking, and other possible objects that should be allocated only once. Usually these instances should release their resources at the end of the benchmark run, allowing efficient garbage collection before starting with the next benchmark.

*generateInput()* generates a random key-value pair that will later be used to pass to the run. Separating the generation of input-values from the actual run allows the assumption that the benchmark run recording results are completely irrelevant of the

input generation code. This is important since different read traces are benchmarked, which each have their own way of generating random values, some of which take much longer than others. An example: several real-life trace results are available as resources in the app, due to its lazy nature, the initial generation of a value is very expensive in time and memory usage as it will start reading the resource. When generating another value of the same trace, the entire trace will already be cached which then results in retrieving a value from an array on a specified index.

*startRecording()* and *endRecording()* will get the system time in nanoseconds. Later the difference will be taken and is considered the time it takes the benchmarked operation, e.g. read or insert, to perform.

*run(input.key, input.value)* is the only timed method and will execute the benchmarked operation. The run is based on a key-value pair, yet in some use cases the value is irrelevant, e.g. a delete benchmark deletes an entry from the cache based on the given key and the value is irrelevant. The *run* method will return a boolean, signifying whether it has succeeded or failed executing the operation. The result will later be used in the intermediate cleanup step and will not be logged or recorded. The semantics of the return value is always irrelevant, yet they are usually used to signify a hit or miss with a read benchmark and a succession or exception with other benchmarks.

Note how hits and misses on a read benchmark are ignored in the global benchmark run. The final hits, misses and hit ratio are calculated using *CacheStats* after the benchmark run. A benchmark implementation should hold the hits and misses by itself, possibly by using the cleanup-phase, which is not timed.

*cleanup(input.key, input.value, result)* allows the benchmark implementation to execute all operations that might be necessary to restore the cache state and internal benchmark state in order to continuously rerun the benchmark for an arbitrary amount of iterations. A delete benchmark might add the entry if it was removed, a read benchmark might increment the hit or miss counter and an insert benchmark might remove the element so that the key is eligible for another insertion.

*tearDown()* is a subtle, yet very important method that should release all resources used by the cache and the benchmark itself. These can then be garbage collected and reused by other benchmarks. Some simple items, like a simple random generator do not cause any dramatic effects when not explicitly removing strong references to it. Yet some cache policies use a *cache manager* singleton that holds every cache that is created, meaning that these caches can never be garbage collected. It is then only a matter of time before the app will crash due to insufficient memory.



Throughout the app there are 4 different types of base benchmarks: read, insert, update and delete benchmarks. They each represent the respective CRUD operation and are meant as a simulation of these. A cache benchmark configuration can however be used for many more, possibly composite, operations.

A benchmark configuration does not hold an amount of elements, rather it holds a ratio called the *cached ratio*, that determines the maximum amount of elements in the cache based on a limited key-space. Say that the key-space has a cardinality of  $n$  and a the benchmark configuration has a *cached ratio* of  $r$ , the maximum amount of elements in the cache will be  $r \cdot n$ .

### 5.2.2 Benchmark runner

The *BenchmarkRunner* will submit benchmarks for every CRUD-operation and cache policy. These are all executed on an *executor service* that run them in a serializable, round-robin fashion. This ensures that there are never multiple benchmarks rivaling for the same resources at the same time. In between every benchmark configuration run the runner will try to explicitly garbage collect non-referenced objects. Because *System.gc()* is only suggestive (Oracle Inc, 2011) the runner will give the garbage collector a maximum of 2 seconds to start collecting objects. If after that time the collector still hasn't collected objects it will timeout and continue with the next benchmark.

When all benchmarks have been run, the runner will collect all statistics of every benchmark, aggregate the results and format them as tables which are printed to log-cat. These tables can then be used as the primary starting point to analyze micro and macro results.

To not run every benchmark configuration used to receive the results used in this thesis, it is possible to comment one or more lines in the code of the *BenchmarkRunner* class.

### 5.2.3 Cache replacement algorithms

Throughout the app there are several cache replacement algorithms that are benchmarked, below is a list of every algorithm benchmarked and contains a simple explanation of the implementation.

**Random Cache** A simple cache replacement algorithm that will replace a random value in the cache when adding a value to an already full cache. No kind of history

is stored and the values can be stored in a sorted fashion, allowing for a theoretical  $\mathcal{O}(\log_2 n)$  time complexity when performing read operations. The implementation uses a queue as backing collection, which allows for  $\mathcal{O}(1)$  when inserting or removing elements.

**FIFO Cache** Replaces cache elements in a FIFO fashion, removing that element which has been in the cache the longest. It internally uses a queue, which allows inserting and deleting in  $\mathcal{O}(1)$ . When a value is added to an already full cache, the value on the “front” side of the queue is removed and the new element is added to the “back” side, also in  $\mathcal{O}(1)$ .

**LRU Cache (Native)** A simple and clean version of an LRU algorithm, which uses recency history to decide the replacement element. The least recent element will be replaced from cache, which corresponds to the first element on top of the backing queue. Due to the intrinsic nature of a queue and the way it is used in the algorithm the least recently used element will always be on top and insert and delete operations can be executed in  $\mathcal{O}(1)$  (The Android Open Source Project, 2011).

**LRU Cache (Guava)** A variation on the traditional LRU cache replacement algorithm, yet some subtle differences are put in place. For example, the algorithm might decide to start removing elements while the cache is not yet full and can remove elements based on timeouts (Decker, 2015). The cache either inherits or uses a *ConcurrentMap*, which is a map supporting fully concurrent/parallel access (Oracle Inc, 2016). The guava cache will create buckets/bins based on hashes of keys and these store a custom hashmap, which hold entries that contain pointers to the next and previous elements (The Guava Authors, 2016).

**LIRS Cache** The LIRS cache implementation is based on the implementation of Jiang and Zhang (2002). Yet it also keeps a queue to maintain some *non-resident HIR blocks*, therefore it can prevent unbound memory usage. Due to all queues hold by the algorithm, it however has the tendency of sometimes surpassing the maximum cache size as confirmed when running the benchmarks. This LIRS implementation is a composite cache which, recursively, exists of more LIRS caches and are defined as segments (Mueller, 2016).

**Clock Cache** A simple implementation of a clock cache. There are some slight differences compared to the traditional clock cache, since the cache uses buckets/bins, based on hashes, to store and retrieve its results (Wilke, 2013b).

**ARC Cache** This *ARC* implementation, by Wilke (2016) just like the clock implementation, uses a linked list to store  $T1$ ,  $B1$ ,  $T2$  and  $B2$ , yet doesn't use an actual collection, rather it holds a reference to an entry that is directly accessed to retrieve the next elements and values. When trying to read the value of an element, based on a key, internally buckets/bins are used to retrieve all results faster than  $\mathcal{O}(n)$  which would otherwise be the execution time.

## 5.2.4 Traces

Throughout the app several traces are used. A trace can be seen as an access pattern used in simulating real-life traffic. Below is a list of every trace used with a short explanation.

**Random** A trace generating random values between a lower and upper bound. The random generation itself is based on *java.util.Random*. Based on the intrinsic nature of a random trace, every cache replacement algorithm should have the same hit ratio as their respective

**NFS** A trace of requests to the Sprite Network File System for a two-day period (Jiang et al., 2005). This trace is file-based, meaning the requests were collected and stored by an organization and are then read during runtime.

**Web12** A trace of requests to a web server on a product detail page in december (Wilke, 2013a). This trace is file-based.

**Search Engine** A trace of search requests to an unnamed "popular" search engine (Ken Bates, 2002). This trace is file-based.

**Zipf-like** A trace in a zipf-like pattern, since common web traffic asymptotically follows such a pattern.  $UPISA = 0.78$  is used in the application as defined by Breslau et al. (1999) as the exponent of the zipf distribution.

## 5.3 Results

### 5.3.1 Insert - Update - Delete

In this section the average execution time of every cache replacement algorithm and operation combination will be analysed, the operations discussed here are: create, update and delete. Read operations will be discussed separately in the following section.

### Insert

The insert benchmarks were generated by timing a million iterations on every cache implementation and cache ratio combination. The graphs at *Appendix A* are based on the cache size = cache ratio \* |key space|, where the key space are all integers between 0 and 499 (included). During an insert benchmark the cache will never be full and there will never be a key collision, meaning there will never be a key that is already present and would be overwritten.

When looking at the graphs in *Appendix A* one can find following order for insert operations based on the average execution time:

$$\text{LIRS} < \text{FIFO} = \text{Native LRU} < \text{Random} < \text{Guava} < \text{CLOCK} < \text{ARC}$$

This means that LIRS has the lowest average execution time and is thus the fastest cache implementation from all implementations benchmarked for inserting new elements. Every implementation seems to have a constant time complexity as was already determined earlier in their corresponding sections. A, relatively, very high average time has been recorded for both CLOCK and ARC, this phenomenon can be explained due to the internal hash buckets that are used in the implementations used. This means that, though we see a high execution time when inserting, we should see a considerably lower execution time when reading.

### Update

As discussed in the previous section, insert benchmarks ensure there is always room in the cache and there are no key collisions, making it a pure insert, rather than a replacement. The update benchmark simply adds random key-value pairs to the cache, allowing key collisions and allowing a full cache. Benchmarking this might not make much sense as it depends on many different operations:

- How long it takes to find out whether a key is already present in the cache.
- How long it takes to find an eviction candidate in case the cache is already full.
- The hit ratio of the cache, determining whether an eviction candidate should even be found.

Yet it is very interesting as this is one of the most common use cases in real world applications. The insert and delete benchmarks are used to test very specific operations of the cache, yet these are usually not executed by themselves. Just like the insert benchmarks, the update benchmarks were generated by timing a million iterations on every cache implementation and cache ratio combination.

When looking at the graphs in *Appendix B* one can find the following order for update operations based on the average execution time:

$$\text{Native LRU} = \text{FIFO} < \text{LIRS} = \text{Guava} < \text{CLOCK} < \text{ARC} < \text{Random}$$

Native LRU, FIFO, ARC and CLOCK all seem to be very reliable, meaning they take about as long on smaller cache sizes as they do on larger cache sizes. This means they are very scalable and could be used in both smartphone and enterprise applications. CLOCK has a theoretical worst case scenario where finding an eviction candidate takes  $\mathcal{O}(n)$ , which occurs if every element is contiguously referenced before updating the cache with a new key-value pair. However this benchmark does not execute intermediate reads, which means that CLOCK performs better than it should in real world applications.

LIRS and Guava seem to have local maxima when reaching a cache size of about 300 elements (with a 60% cache ratio). Yet they seem to stabilize afterwards, which might still make them scale to very high cache sizes. Considering higher cache sizes would however be rather rare in smartphone applications considering their memory availability, which is why no simulations were run to see how they scale with bigger cache sizes.

The random cache replacement algorithm performed slightly worse than LIRS in the smallest cache size simulation, yet when reaching higher values the performance started dropping immensely ending with about double the average execution compared to ARC. The relationship  $R$  between the average execution time and the cache size is of a linear nature. Therefore a random cache policy is very unreliable and should only be used when the developer knows that the cache size  $c$  will always stay very small. Note how  $\frac{dR}{dc}$  seems to be similar to the hiccup that is present in LIRS and Guava.

The reason why the random cache replacement algorithm performs so poorly, is due to the random value that needs to be generated when selecting an eviction candidate. Every other cache replacement algorithm can find an eviction candidate either in  $\mathcal{O}(1)$  or  $\mathcal{O}(n)$  (worst-case scenario in CLOCK), yet generating a random value completely depends on the performance of the generator.

Note how in real world applications the performance of CLOCK will be somewhere between the recorded CLOCK benchmark (lower-bound) and the recorded asymptotic execution time from the random cache update benchmark, in other words the running time of CLOCK is  $\Omega(1)$  and  $\mathcal{O}(n)$ .

**Delete**

**5.3.2 Read**

## Chapter 6

### Conclusion

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