)	0	Task Mode	Task Name	Duration	Start	Finish	% Complete	Predecessors	16	23
1	✓	*	Deliverable #1: Initial Project Proposal	2 days	Fri 1/28/11	Mon 1/31/11	•			
2	✓	A ²	Deliverable #2: Detailed Proposal, Work Schedule, Cost/Time estimate	7 days	Fri 2/11/11	Mon 2/21/11	100%			
3	✓	*	Sample and Test TLC5940	8 days	Mon 2/21/11	Wed 3/2/11	100%			
4	✓	*	Sample and Test Photoresistors	8 days	Mon 2/21/11	Wed 3/2/11	100%			
5	✓	AP .	Sample and Test RGB LEDs	8 days	Mon 2/21/11	Wed 3/2/11	100%			
6	~	*	Deliverable #3: Register set and timing analysis deliverable	25 days	Mon 2/21/11	Fri 3/25/11	100%			
7	✓	₹ [†]	Design Each Cup Unit: Sensor, Lights, Board	8 days	Wed 3/2/11	Fri 3/11/11	100%			
3	√	*	Design Communication Network for Each Cup	8 days	Wed 3/2/11	Fri 3/11/11	100%			
9	√	常	Deliverable #5: Software Requirements Definition, Schematic diagram, Test Plan, BOM, Updated Schedule, and Updated Cost/Time Estimate	16 days	Fri 3/11/11	Fri 4/1/11	100%			
LO	√	*	FPGA Verilog Completion	16 days	Fri 3/11/11	Fri 4/1/11	100%			
1	✓	*	Create Schematic Diagram	11 days		Fri 3/25/11	100%			
2	✓	*	Create Test Plan	11 days		Fri 3/25/11	100%			
L3	✓	*	Update Schedule	3 days	Wed 3/23/11	Fri 3/25/11	100%			
14	✓	*	Update Budget	3 days	Wed 3/23/11	Fri 3/25/11	100%			
.5	✓	*	Build Table	12 days	Mon 3/14/11	Tue 3/29/11	100%			
L6	✓	*	Design Reviews in Class	4 days	Fri 3/25/11	Wed 3/30/11	100%			
17	✓	*	Design PCB	6 days	Mon 3/28/11	Sat 4/2/11	100%			
18	✓	AP.	Assemble PCB	5 days	Mon 4/4/11	Fri 4/8/11	100%			
19	✓	*	Deliverable #6: Initial hardware test	21 days	Mon 3/21/11	Mon 4/18/11	100%			
20	✓	A .	Get one TLC5940 working with 5 cups	5 days	Mon 4/4/11	Fri 4/8/11	100%	10		
			Task		External Milestone	♦		Manual Summary R	ollup	
			Split		Inactive Task			Manual Summary		
Project: ECE453.mpp			Milestone ♦		Inactive Milestone	\Diamond	:	Start-only		
Date: Tue 5/3/11			Summary		Inactive Summary			Finish-only	3	
			Project Summary		Manual Task			Deadline	•	
			External Tasks		Duration-only			Progress		
					Page 1					

ID		Task	Task Name	Duration	Start	Finish	%	Predecessors			
	0	Mode					Complete		16	23	
21	V	*	Complete the 4 TLC5940 and 5 cup modules	5 days	Mon 4/11/11	Fri 4/15/11	100%	20			
22	✓	*	Construct Aluminum Cones for Table	4 days	Wed 3/30/11	Mon 4/4/11	100%				
23	✓	*	Mount Resistors and LEDs	6 days	Tue 4/5/11	Tue 4/12/11	100%	22			
24	~	À	C Software for running LEDs and Photo resistors	5 days	Mon 4/11/11	Fri 4/15/11	100%				
25	√	AP.	Hardware Completion	8 days	Mon 4/11/11	Wed 4/20/11	100%				
26	✓	AP.	Demo Pattern Software Completion	11 days	Mon 4/18/11	Mon 5/2/11	100%				
27	✓	*	Finishing Work on Table	9 days	Wed 4/20/11	Mon 5/2/11	100%				
28	√	A.	GUI Scoreboard and Control Software	22 days	Fri 4/1/11	Mon 5/2/11	100%				
29	~	*	Deliverable #7: Final demonstration of project	11 days	Mon 4/18/11	Mon 5/2/11	100%				
30	✓	A.	Project Open House	1 day	Mon 5/2/11	Mon 5/2/11	100%				
31	√	*	Deliverable #8: Final project report	13 days	Mon 4/18/11	Wed 5/4/11	100%				





