



30V DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVDSS	RDS(ON) Max	Package	I _D T _A = +25°C
-30V	$25m\Omega @V_{GS} = -10V$	SO-8	-6.0A
-307	$38m\Omega @V_{GS} = -4.5V$	30-6	-4.7A

Description

This new generation MOSFET has been designed to minimize the on-state resistance $(R_{DS(ON)})$ yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions
- Load Switch

Features

- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

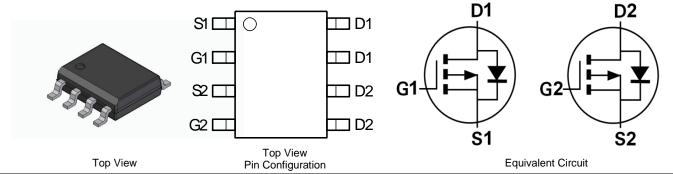
https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Tin Finish Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.074 grams (Approximate)



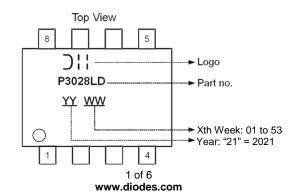
Ordering Information (Note 4)

Part Number	Case	Packaging	
DMP3028LSD-13	SO-8	2,500/Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information





Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	VDSS	-30	V		
Gate-Source Voltage	V_{GSS}	±20	V		
Continuous Prois Current (Note E) V	Steady State	T _A = +25°C T _A = +70°C	lo	-6 -4.7	А
Continuous Drain Current (Note 5) V _{GS} = 10V	t < 10s	T _A = +25°C T _A = +70°C	lo	-7.4 -5.8	А
Maximum Body Diode Forward Current (Note 6)	Is	-2.5	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	-30	Α		

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Total Dawar Dissination (Note 5)	T _A = +25°C	D-	1.3	W
Total Power Dissipation (Note 5)	T _A = +70°C	PD	0.8	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Roja	102	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t < 10s	КӨЈА	61	
Total Power Dissipation (Note 6)	T _A = +25°C	Pp	1.7	W
Total Power Dissipation (Note 6)	T _A = +70°C	PD	1.1	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Roja	75	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t < 10s	КӨЈА	50	
Thermal Resistance, Junction to Case (Note 6)	Rejc	14.5		
Operating and Storage Temperature Range	T_{J} , T_{STG}	-55 to +150	°C	

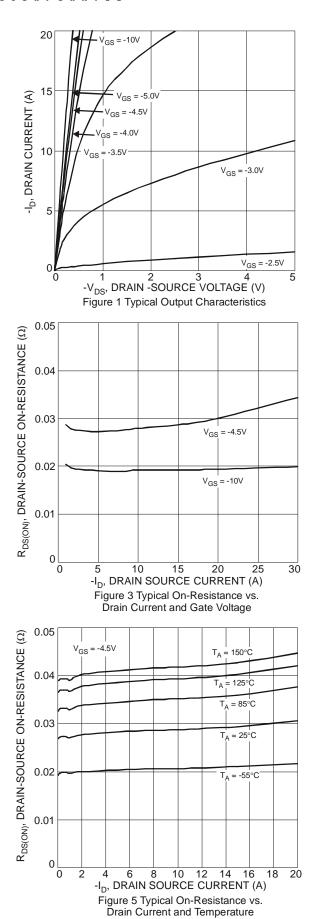
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

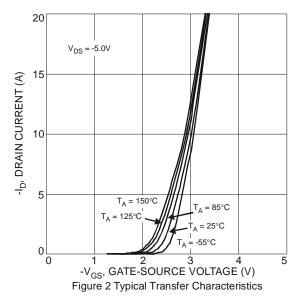
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_		V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current	IDSS	_	_	-1	μΑ	V _{DS} = -30V, V _{GS} = 0V
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)	•					
Gate Threshold Voltage	Vgs(TH)	-1	_	-3	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$
Static Drain-Source On-Resistance			20	25	mΩ	$V_{GS} = -10V, I_D = -7A$
Static Drain-Source On-Resistance	RDS(ON)		29	38	11122	$V_{GS} = -4.5V, I_{D} = -5.5A$
Forward Transfer Admittance	Y _{fs}	_	11	_	S	$V_{DS} = -5V, I_{D} = -7A$
Diode Forward Voltage	VsD	_	0.7	1.2	V	Vgs = 0V, Is = -2.1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	1241	_		V _{DS} = -15V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	Coss	_	147	_	pF	
Reverse Transfer Capacitance	Crss	_	110	_		
Gate Resistance	Rg	_	15		Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = -4.5V)	Qg	_	11	_		V _{DS} = -15V, I _D = -7A
Total Gate Charge (V _{GS} = -10V)	Qg	_	22	_	nC	
Gate-Source Charge	Qgs	_	3.5	_	nc nc	
Gate-Drain Charge	Q_{gd}	_	4.7	_		
Turn-On Delay Time	tD(ON)	_	9.7			
Turn-On Rise Time	t _R	_	17.1		no	$V_{GS} = -10V$, $V_{DD} = -15V$, $R_{GEN} = 6\Omega$,
Turn-Off Delay Time	t _{D(OFF)}	_	60.5	_	ns	I _D = -7A
Turn-Off Fall Time	tF	_	40.4	_		

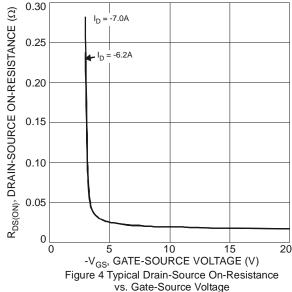
Notes:

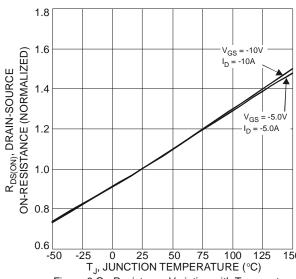
- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.



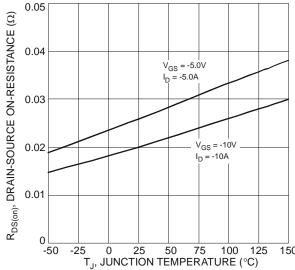




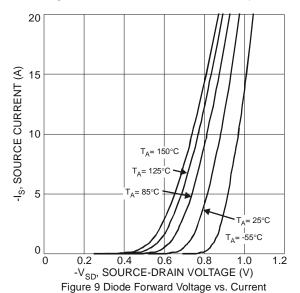












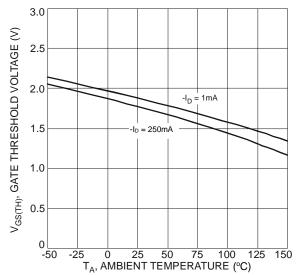
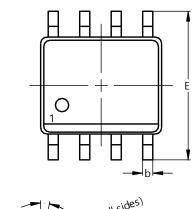


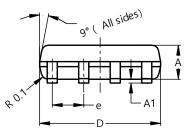
Figure 8 Gate Threshold Variation vs. Ambient Temperature

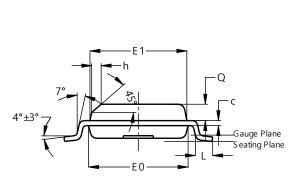


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.







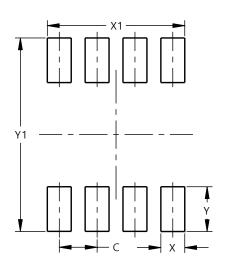
SO-8

SO-8

SO-8				
Dim	Min	Max	Тур	
Α	1.40	1.50	1.45	
A1	0.10	0.20	0.15	
q	0.30	0.50	0.40	
C	0.15	0.25	0.20	
D	4.85	4.95	4.90	
Е	5.90	6.10	6.00	
E1	3.80	3.90	3.85	
E0	3.85	3.95	3.90	
е			1.27	
h			0.35	
٦	0.62	0.82	0.72	
Ø	0.60	0.70	0.65	
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.27
Х	0.802
X1	4.612
Y	1.505
V1	6.50

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