
Reverse engineering of Flutter applications

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This paper explores the challenges associated with reverse engineering Flutter applications and presents effective techniques to overcome them.

Beginning with an introduction to Flutter applications and the Dart programming language, we embark on the task of reversing a basic Dart program and a Flutter application built in release mode. Despite our efforts, we encounter significant difficulties in recovering our original code with current tools.

These challenges arise from key concepts that we delve into, namely the Dart Object Pool, object serialization, and Flutter obfuscation. Armed with this understanding, we proceed to the subsequent sections of the paper where we propose techniques for function identification, string recovery, byte array analysis, and the identification of simple loops and algorithms. With the application of these techniques, we successfully reverse our initial programs and applications.

Short introduction to Flutter

Flutter ([“Flutter,” n.d.](#)) is an open source UI software development kit created by Google. It is particularly attractive for its ability to develop with a *single codebase*, and then compile *natively* to various mobile - or non-mobile - platforms (Android, iOS, Windows, MacOS, Linux, Web... see [list](#)).

The applications are developed in *Dart* ([“Dart,” n.d.](#)), an object-oriented programming language with a C-style syntax and a few features such as sound null safety. They can then be [compiled into various formats](#), depending on desired properties such as portability, performance. Flutter uses *kernel snapshots* for debug builds, and Ahead Of Time (AOT) snapshots for *release* builds. The former are cross platform, the latter initialize quicker.

Dart program formats	Size	Exec time	Description
hello.dart	266 bytes	0m0,320s	Source code
hello.exe	5.8 M	0m0,008s	Self contained executable
hello.aot	863 K	0m0,008s	AOT snapshot
hello.jit	4.7 M	0m0,242s	JIT snapshot
hello.dill	936 bytes	0m0,245s	Kernel snapshot

Anatomy of a Flutter application

Flutter applications consist in at least 2 snapshots: one for the VM, and one or more snapshots for the program. The **VM snapshot** contains the base functionality of the Dart VM and common Dart libraries. The other snapshots are called **isolate snapshots**: one snapshot per isolate. An *isolate* is an independent unit of

execution that runs concurrently with other isolates within the same Dart process. Each isolate has its own memory heap, stack and event loop - contrary to OS threads which share the same memory space.

Dart programs have at least one isolate, to run the main “thread”, and possibly more. For instance, the developer may decide to create more isolate to handle separately UI rendering, or network requests etc. So, Flutter applications at least have *one isolate snapshot* (possibly more). It basically freezes the state of the Dart VM before the main is called.

When bundled as Android applications, a Flutter application typically has the following files:

```
1 ./AndroidManifest.xml
2 ./assets/
3     flutter_assets/
4         AssetManifest.json
5         FontManifest.json
6         fonts/
7     ...
8 ./classes.dex
9 ./kotlin/
10 ...
11 ./lib/
12     ./arm64-v8a/
13         libapp.so
14         libflutter.so
15     ./armeabi-v7a/
16         libapp.so
17         libflutter.so
18     ./x86_64/
19         libapp.so
20         libflutter.so
21 ./META-INF/
22     ...
23 ./res/
24     ...
25 ./resources.arsc
```

The Dalvik code inside `classes.dex` has no interest (apart from loading the Dart runtime). The interesting Dart payload is located in the native libraries `libapp.so`, which are compiled for arm32, arm64 and x86_64. `libapp.so` contains the Flutter snapshots. They are visible from dynamic symbols.

```
1 $ objdump -T libapp.so
2
3 libapp.so:      file format elf64-x86-64
4
5 DYNAMIC SYMBOL TABLE:
6 000000000000170000 g    DO .text      000000000000047a0
   _kDartVmSnapshotInstructions
7 0000000000001747a0 g    DO .text      0000000000002848e0
   _kDartIsolateSnapshotInstructions
8 00000000000001f0 g    DO .rodata    00000000000003890 _kDartVmSnapshotData
9 00000000000003a80 g    DO .rodata    000000000000169bb0
   _kDartIsolateSnapshotData
10 00000000000001c8 g    DO .note.gnu.build-id 00000000000000020
   _kDartSnapshotBuildId
```

We see 2 snapshots (VM and Isolate), and each snapshot has a code section (`.text`) and a data section (`.rodata`). To reverse engineer a Flutter application, we are interested in `_kDartIsolateSnapshotInstructions` and `_kDartIsolateSnapshotData`.

Status for Reversing Dart programs

Basic Dart program

We write a basic Dart program. The syntax of Dart is close to C, with a few [built-in types](#) such as `int`, `double`, `bool`, `List`, `Set`, `String`. Contrary to many programming languages though, Dart does [not have any type to represent bytes](#) nor *single characters*. Bytes are simply represented as arrays of integers: `List<int>`, or a more optimized form as `Uint8List`.

```
1 import 'dart:typed_data';
2
3 void xor_stage2() {
4   String header= 'ph0wn{';
5   String footer = '}';
6   // plain = list('Dart_is_soooo_opaque_isnt_it')
7   // [ ord(i) ^ 0x43 for i in plain ]
8   List <int> core = [7, 34, 49, 55, 28, 42, 48, 28,
9                     48, 44, 44, 44, 44, 28, 44, 51,
10                    34, 50, 54, 38, 28, 42, 48, 45,
11                    55, 28, 42, 55];
12   int i = 0;
13
14   for (i = 0; i < core.length; i++) {
15     core[i] = core[i] ^ 0x43;
16   }
17   print('stage2: ' + header + String.fromCharCode(core) + footer);
18 }
19
20 void xor_stage1() {
21   Uint8List flag = Uint8List.fromList([98, 101, 97, 117, 116,
22   105, 102, 117, 108, 45, 115, 116, 97, 103, 101, 49]);
23   print('stage1: ' + String.fromCharCode(flag));
24 }
25
26 void main(List<String> arguments) {
27   xor_stage1();
28   xor_stage2();
29 }
```

Like in C, the program begins with `main`. Then, we basically print 2 strings: one in `xor_stage1()`, creating a `String` object from a list of bytes, and one in `xor_stage2()` where the string is decrypted from an XOR-encrypted byte array.

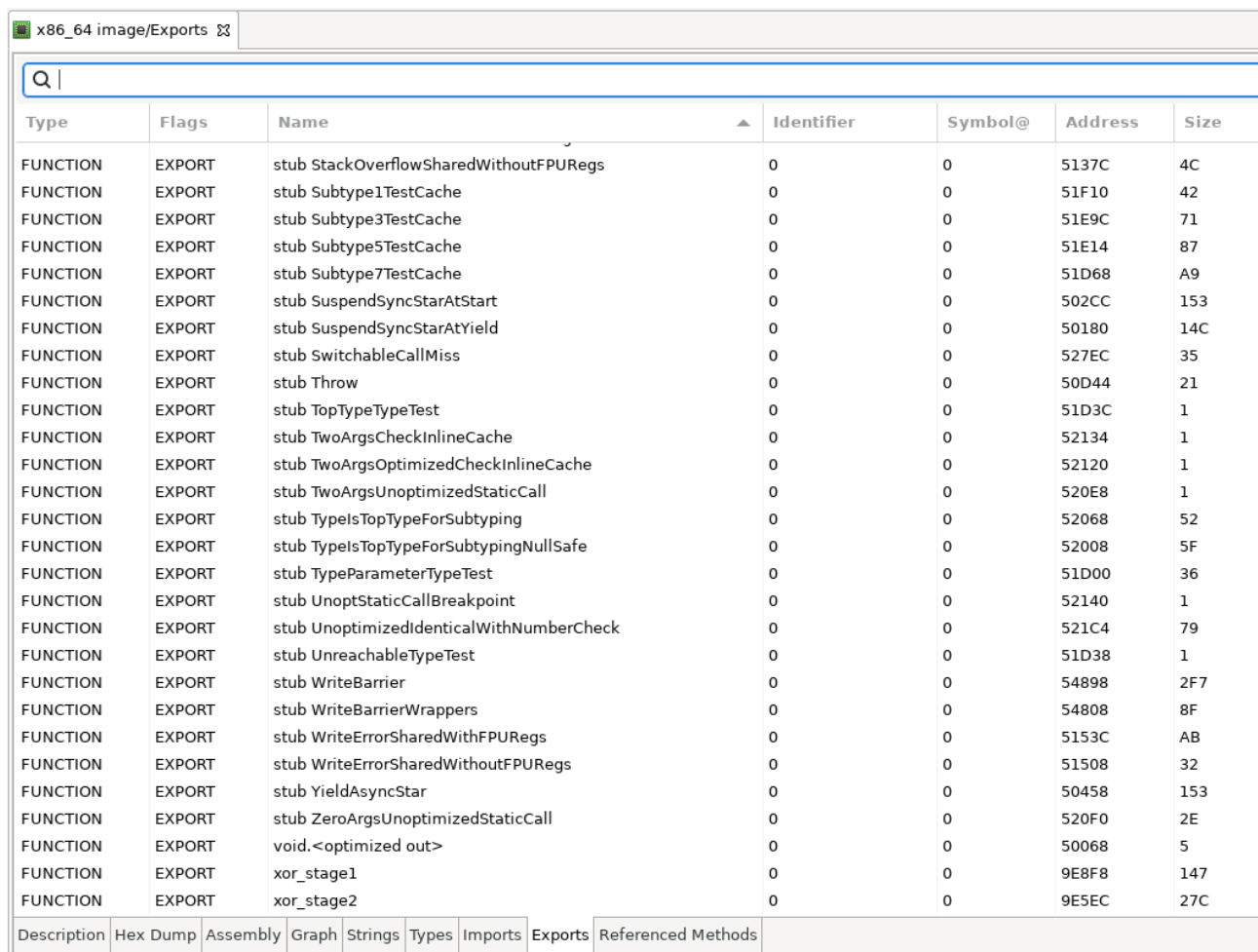
We compile an AOT snapshot (`dart compile aot-snapshot file`). When we run it, the output is the following:

```
1 stage1: beautiful-stage1
2 stage2: ph0wn{Dart_is_soooo_opaque_isnt_it}
```

What disassemblers understand of Dart programs

As we mentioned previously, Dart programs may be compiled for various platforms. Reverse engineering tools generally having better support for x86_64, we maximize our reversing chances by working over a Dart AOT snapshot for x86_64.

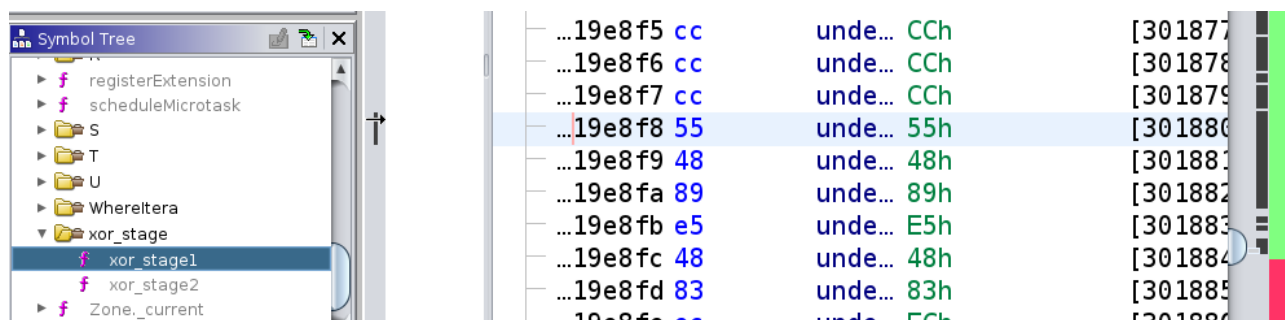
The Unix `strings` command very few interesting strings: `ph0wn{, stage1:, stage2:, xor_stage1, xor_stage2, main`, but **none of the strings built from byte arrays**. Ghidra v10.2.2 is completely lost and only manages to find function names. IDA Pro, Radare and JEB perform better: they find function names and correctly disassemble them.



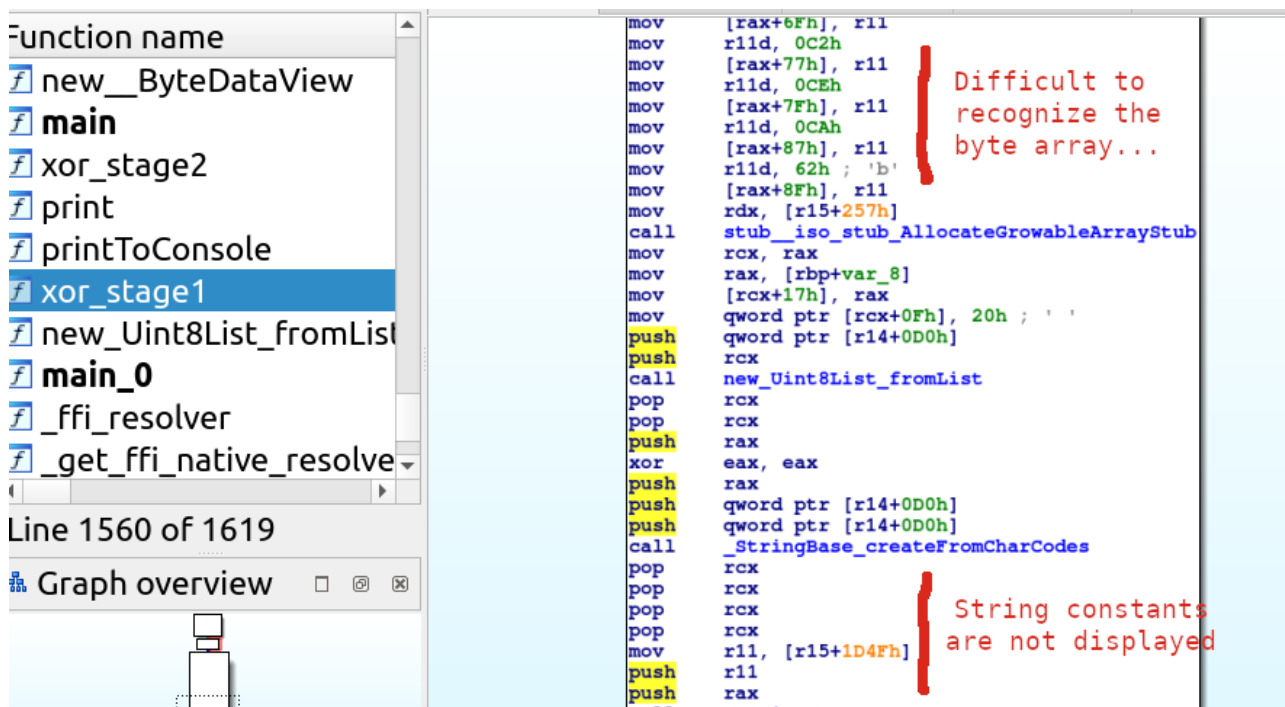
Type	Flags	Name	Identifier	Symbol@	Address	Size
FUNCTION	EXPORT	stub StackOverflowSharedWithoutFPUREgs	0	0	5137C	4C
FUNCTION	EXPORT	stub Subtype1TestCache	0	0	51F10	42
FUNCTION	EXPORT	stub Subtype3TestCache	0	0	51E9C	71
FUNCTION	EXPORT	stub Subtype5TestCache	0	0	51E14	87
FUNCTION	EXPORT	stub Subtype7TestCache	0	0	51D68	A9
FUNCTION	EXPORT	stub SuspendSyncStarAtStart	0	0	502CC	153
FUNCTION	EXPORT	stub SuspendSyncStarAtYield	0	0	50180	14C
FUNCTION	EXPORT	stub SwitchableCallMiss	0	0	527EC	35
FUNCTION	EXPORT	stub Throw	0	0	50D44	21
FUNCTION	EXPORT	stub TopTypeTypeTest	0	0	51D3C	1
FUNCTION	EXPORT	stub TwoArgsCheckInlineCache	0	0	52134	1
FUNCTION	EXPORT	stub TwoArgsOptimizedCheckInlineCache	0	0	52120	1
FUNCTION	EXPORT	stub TwoArgsUnoptimizedStaticCall	0	0	520E8	1
FUNCTION	EXPORT	stub TypeIsTopTypeForSubtyping	0	0	52068	52
FUNCTION	EXPORT	stub TypeIsTopTypeForSubtypingNullSafe	0	0	52008	5F
FUNCTION	EXPORT	stub TypeParameterTypeTest	0	0	51D00	36
FUNCTION	EXPORT	stub UnoptStaticCallBreakpoint	0	0	52140	1
FUNCTION	EXPORT	stub UnoptimizedIdenticalWithNumberCheck	0	0	521C4	79
FUNCTION	EXPORT	stub UnreachableTypeTest	0	0	51D38	1
FUNCTION	EXPORT	stub WriteBarrier	0	0	54898	2F7
FUNCTION	EXPORT	stub WriteBarrierWrappers	0	0	54808	8F
FUNCTION	EXPORT	stub WriteErrorSharedWithFPUREgs	0	0	5153C	AB
FUNCTION	EXPORT	stub WriteErrorSharedWithoutFPUREgs	0	0	51508	32
FUNCTION	EXPORT	stub YieldAsyncStar	0	0	50458	153
FUNCTION	EXPORT	stub ZeroArgsUnoptimizedStaticCall	0	0	520F0	2E
FUNCTION	EXPORT	void.<optimized out>	0	0	50068	5
FUNCTION	EXPORT	xor_stage1	0	0	9E8F8	147
FUNCTION	EXPORT	xor_stage2	0	0	9E5EC	27C

JEB lists Dart functions in the Exports tab. Radare 2 lists function names with `afl`

They however remain unable to recover strings and byte arrays. For example, in the screenshots below, no disassembler recognizes use of the string `stage1:`. The byte array is not recognizable either: each individual byte do not match the bytes of the source code... As for decompilation, only JEB manages to produce a decent output - yet, it remains mostly useless.



Ghidra is completely lost when disassembling a Dart snapshot



Disassembly of xor_stage1 by IDA Pro. Note the byte array is difficult to recognize: the bytes do not match.


```

; arg int64_t arg10 @ xmm3
; var int64_t var_8h @ rbp-0x8
0x0009e8f8 55          push rbp                      ; xorpi.dart:0
0x0009e8f9 4889e5      mov rbp, rsp
0x0009e8fc 4883ec08    sub rsp, 8
0x0009e900 493b6638    cmp rsp, qword [r14 + 0x38]
0x0009e904 0f8629010000 jbe 0x9ea33
; CODE XREF from sym.xor_stage1 @ 0x9ea3a(x)
0x0009e90a 498b9f570200. mov rbx, qword [r15 + 0x257]
0x0009e911 41ba20000000 mov r10d, 0x20                ; "@"
0x0009e917 e8f06dffff  call sym.stub__iso_stub_AllocateArrayStub
0x0009e91c 488945f8    mov qword [var_8h], rax
0x0009e920 41bbc4000000 mov r11d, 0xc4
0x0009e926 4c895817    mov qword [rax + 0x17], r11
0x0009e92a 41bbca000000 mov r11d, 0xca
0x0009e930 4c89581f    mov qword [rax + 0x1f], r11
0x0009e934 41bbc2000000 mov r11d, 0xc2
0x0009e93a 4c895827    mov qword [rax + 0x27], r11
0x0009e93e 41bbea000000 mov r11d, 0xea
0x0009e944 4c89582f    mov qword [rax + 0x2f], r11
0x0009e948 41bbe8000000 mov r11d, 0xe8
0x0009e94e 4c895837    mov qword [rax + 0x37], r11
0x0009e952 41bbd2000000 mov r11d, 0xd2

```

Disassembly of the `xor_stage1` function by Radare 2. The assembly is correct, but raw.

```

xor_stage1/Source
long xor_stage1(long param0, long param1, long param2, long param3, long param4, long p
long* ptr0, ptr1, ptr2;

if(*(ptr1 + 7) >= (unsigned long)&ptr0) {
    *(ptr1 + 81)(param0, param1, param2, param3, param4, param5);
}

long* ptr3 = (long*)stub __iso_stub_AllocateArrayStub(param0, param1, param2, param3
ptr0 = ptr3;
*((long*)((char*)ptr3 + 23L) = 196L;
*((long*)((char*)ptr3 + 31L) = 202L;
*((long*)((char*)ptr3 + 39L) = 194L;

```

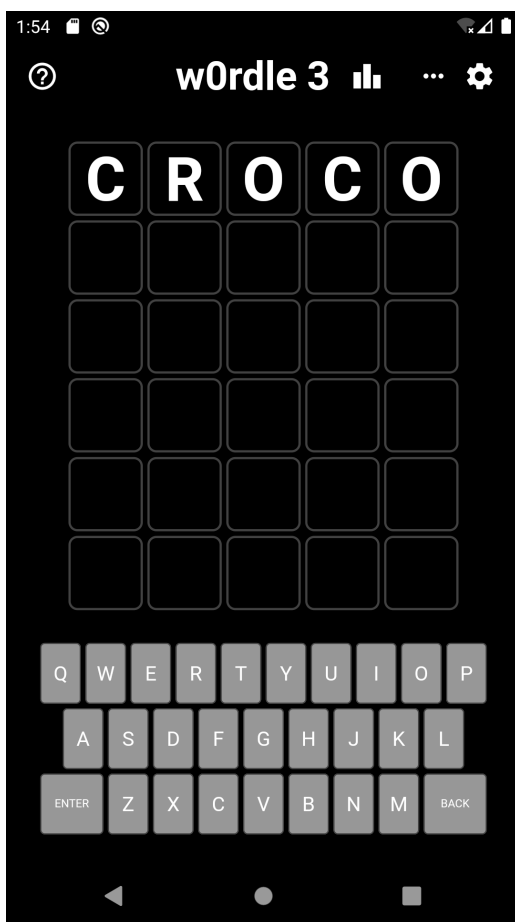
Decompiled code for `xor_stage1`, by JEB. Though far from excellent, it's the best decompiled output we can hope for.

Disassembler	Function names	Function disassembly	User strings	Byte array recovery	Decompiler output
Ghidra v10.2.2	Yes	No	No	No	Not usable
IDA Pro 8.2	Yes	Yes	No	No	-
Radare 5.8.7	Yes	Yes	No	No	Not usable
JEB 4.31.0	Yes	Yes	No	No	Basic

Status for reversing Flutter application

w0rdle stage 3

In a second attempt, we try to reverse a Flutter application. For that, we use a challenge I released in January 2023 and which remains, up to now, unsolved. The Flutter application is downloadable from ([Apvrille 2023e](#)). Its SHA256 hash is `99068666d845bdf2513bca731ad95e4b21f6d0460925beaf881a649ce16a9c2e`. The application features a customized wordle game: this is a game where [you have 6 chances to guess a 5-letter word](#). It is implemented using Flutter and offers 3 reverse engineering stages: [stage 1 and stage 2 were solved during Ph0wn CTF 2022](#) and they can be solved with no to little Flutter knowledge. Stage 3, however, is meant to require Flutter reverse engineering skills.



The wordle game, stage 3, on Android.

In stage 3, the word to guess consists of characters which are not present in the game's keyboard. Contrary to stage 2, it is difficult to modify the word to guess (because it is encrypted in the code). Thus, there is no way to win by playing the game. We will investigate a solution where we *reverse engineer* the function that displays the flag. It is (intentionally) extremely similar to the Dart program we built previously.

```
1 import 'package:flutterdle/domain.dart';
2 ...
3
4 class Flutterdle {
5   String _winningMessage() {
```

```
6     if (stage == 2) {
7         // return flag for stage 2
8         ...
9     }
10
11     if (stage == 3) {
12         String header = 'ph0wn{';
13         String footer = '}';
14         // encrypted body of the flag
15         List<int> core = [81, 94, 89, 83, 104, ...];
16         int i = 0;
17
18         for (i = 0; i < core.length; i++) {
19             core[i] = core[i] ^ 0x37;
20         }
21         print(header + String.fromCharCode(core) + footer);
22         return header + String.fromCharCode(core) + footer;
23     }
24
25     // else, stage 1
26     String flag = 'FLAG for STAGE1';
27     print(flag); // we want this to print on logcat
28     return flag;
29 }
```

The flag is decrypted from a very basic XOR algorithm. **This would typically represent no issue to reverse engineer.** However, we'll see in the next paragraph Flutter makes things complicated.

Disassemblers understand less from Flutter apps

As we mentioned in Flutter application's anatomy, the library to reverse is `libapp.so`. Again, to maximize chances of successful disassembly, we attempt to reverse the `x86_64` version.

For stage 3, the `strings` command finds `ph0wn{` and `_winningMessage@739388852`. There are no other interesting strings for stage 3. We load the library in a disassembler and analyze. It's worse than for a Dart program: disassemblers do not find *any* function name [^1].

[^1] JEB manages to recover function names in some cases, but it did not recover them for `w0rdle 3`.

In the example below, we ask Radare 2 to list functions (`afl`). All have a dummy name `fcn.address`.

```
1 > afl
2 ...
3 0x0036f024 14 232 fcn.0036f024
4 0x00332678 14 232 fcn.00332678
5 0x00332760 9 105 fcn.00332760
6 0x003328b4 9 105 fcn.003328b4
7 0x00178f1c 1 67 fcn.00178f1c
```

There are many functions. It's unlikely we'll pick one randomly and by chance disassemble `_winningMessage`. If ever we did, we'd notice disassemblers have difficulties finding the correct boundaries. The assembly

below is produced by Radare 2, who failed to understand the beginning of `_winningMessage` is at `0x003ce09c`.

```
1 0x003ce08b e9d7fdffff jmp 0x3cde67
2 ; CODE XREF from fcn.003cde3c @ 0x3cde82(x)
3 0x003ce090 498bb72f9d00. mov rsi, qword [r15 + 0x9d2f]
4 0x003ce097 e838500200 call fcn.003f30d4
5 ; CALL XREF from fcn.003cde3c @ 0x3cdf5(x)
6 0x003ce09c 55 push rbp
7 0x003ce09d 4889e5 mov rbp, rsp
8 0x003ce0a0 4883ec10 sub rsp, 0x10
9 0x003ce0a4 493b6638 cmp rsp, qword [r14 + 0x38]
10 0f8697040000 jbe 0x3ce545
11 ; CODE XREF from fcn.003cde3c @ 0x3ce54c(x)
12 0x003ce0ae 488b4510 mov rax, qword [var_10h_2]
13 0x003ce0b2 488b482b mov rcx, qword [rax + 0x2b]
14 0x003ce0b6 4883f902 cmp rcx, 2
```

This is due to [Flutter's obfuscation](#). By default, it *renames* all Dart module names, class names and function names for release builds on Android and iOS. While this is not strong obfuscation (no encryption, no modification of instruction), it hardens reverse engineering.

Improving Reverse Engineering

In the rest of this paper, we use **Radare 5.8.7** and **JEB 4.31.0**, which are the current versions at the time of writing this article. I am in contact with both authors / contributors, both disassemblers are likely to improve in the next few months.

The Object Pool and its impact

In our initial attempts, we are able to find string constants (`strings`, or `iz` command in Radare) but disassemblers are unable to annotate code when they are used. This is **because Dart assembly accesses strings indirectly through an Object Pool**.

The Object Pool is a table-like structure which stores and references frequently used objects, immediates and constants within a Dart program. ([Batteux 2022](#)) explains how the Object Pool is serialized in a AOT snapshot, and how to access objects at runtime. Basically, all objects are serialized in a snapshot. The Object Pool itself is an object and is serialized too. At runtime, each object - including the object pool - is de-serialized. Then, to access a given object, the code provides its pool index. The Object Pool looks up for the corresponding object and returns it.

To illustrate the mechanism, let's take an example from our Dart program.

```
1 void xor_stage2() {
2     String header= 'ph0wn{'; // access to constant "ph0wn{"
3     ...
4 }
```

When we affect constant `ph0wn{`, the assembly actually says “please get me index XXX”. The object pool looks up this particular entry and returns the corresponding object `ph0wn{`. **Dart uses a dedicated register for requests to the Object Pool:** it’s `r15` for `x86_64`, `r5` for `Aarch32`, and `x27` for `Aarch64` (see Appendix [register layout](#))

```
1 ; get pool object index 936
2 0x0009e7f0      4d8b9f471d00.  mov r11, qword [r15 + 0x1d47]
3 0x0009e7f7      4153                push r11
```

([Software 2022](#)) explains the pool index is computed from `address // 8`. So, the assembly above asks for pool index 936. If the pool index is high, computing its address may be split on several instructions. For example, the following instructions compute address `0x8000 + 0xbb8 = 0x8bb8`. It fetches pool index 4471 (`0x8bb8 // 8`).

```
1 ; x17 = x27 + (8 << 12)
2 add x17, x27, 8, lsl 12
3 ; x17 = x17 + 0xbb8
4 ldr x17, [x17, 0xbb8]
```

This explains our reverse engineering difficulties: the strings are visible in the ELF executable because the string constant is serialized at snapshot creation, but disassemblers are unable to detect their access and properly annotate code with a message like `/*accessing pool string: ph0wn{ */`¹ because they are not aware of the Object Pool, the use of a specific register and the computation of the pool index.

Architecture	Load Pool Object	Opcode bytes
x86_64	<code>mov rbx, qword ptr ds:[r15+847h]</code> ²	498B9F470800
arm7eabi	<code>ldr r1, [r5, #433h]</code>	331495E5
arm64	<code>ldr x16, [x27, #7D50h]</code>	70AB7EF9

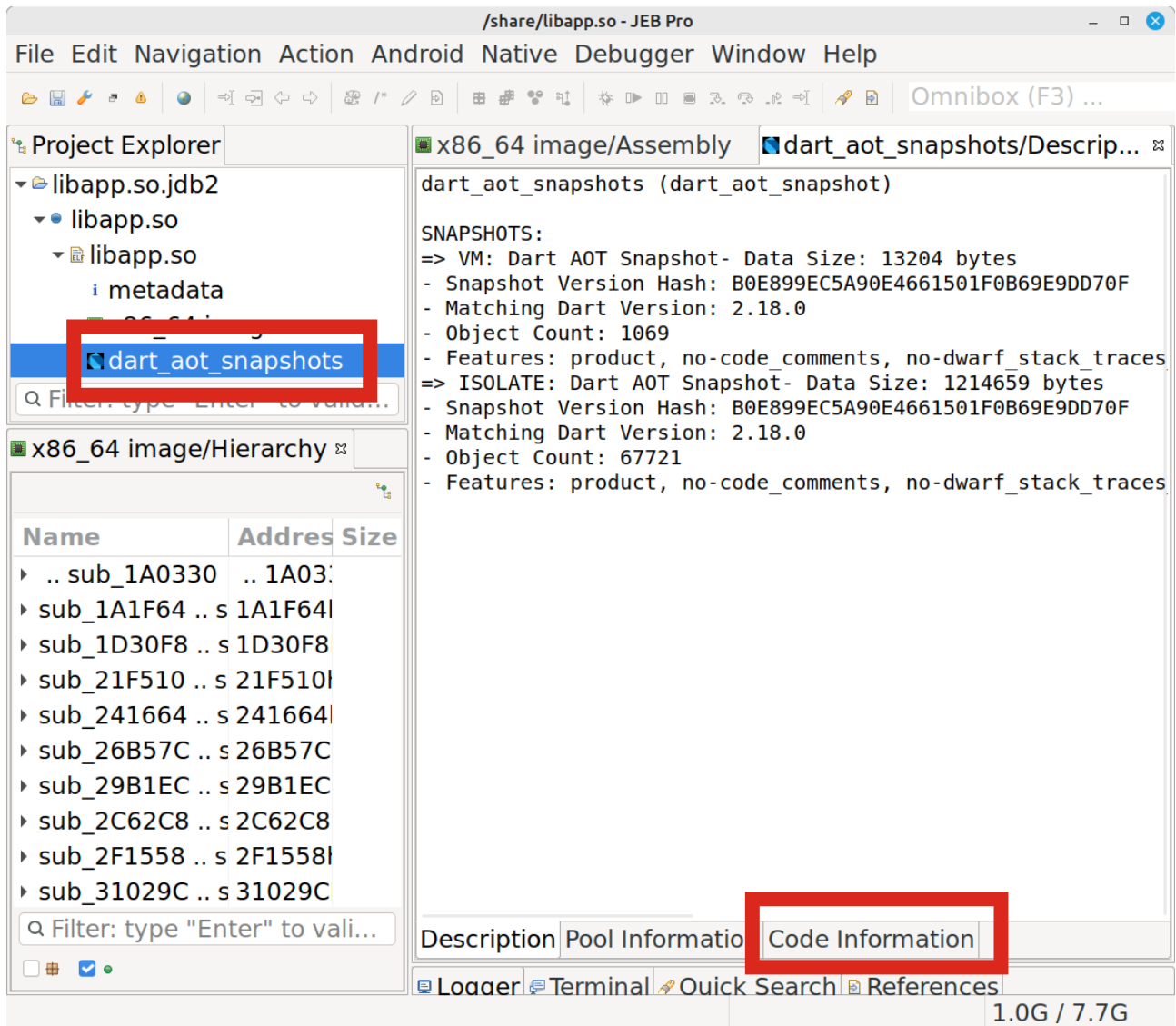
How to locate Flutter functions with JEB

JEB manages to recover Flutter application function names and addresses in a few cases, and provides the necessary elements to retrieve names manually in other cases. This features stems from JEB’s ability to parse the Object Pool³. Click on the Dart AOT snapshot, then on the “Code Information”.

¹ JEB does it for the `x86_64` platform. However, there are bugs: it doesn’t recognize all strings, and the feature does not work for ARM platforms.

² With Radare2, the produced instructions differ slightly: `mov rbx, qword [r15 + 0x847]`

³ Currently, there are bugs, especially for ARM platforms.

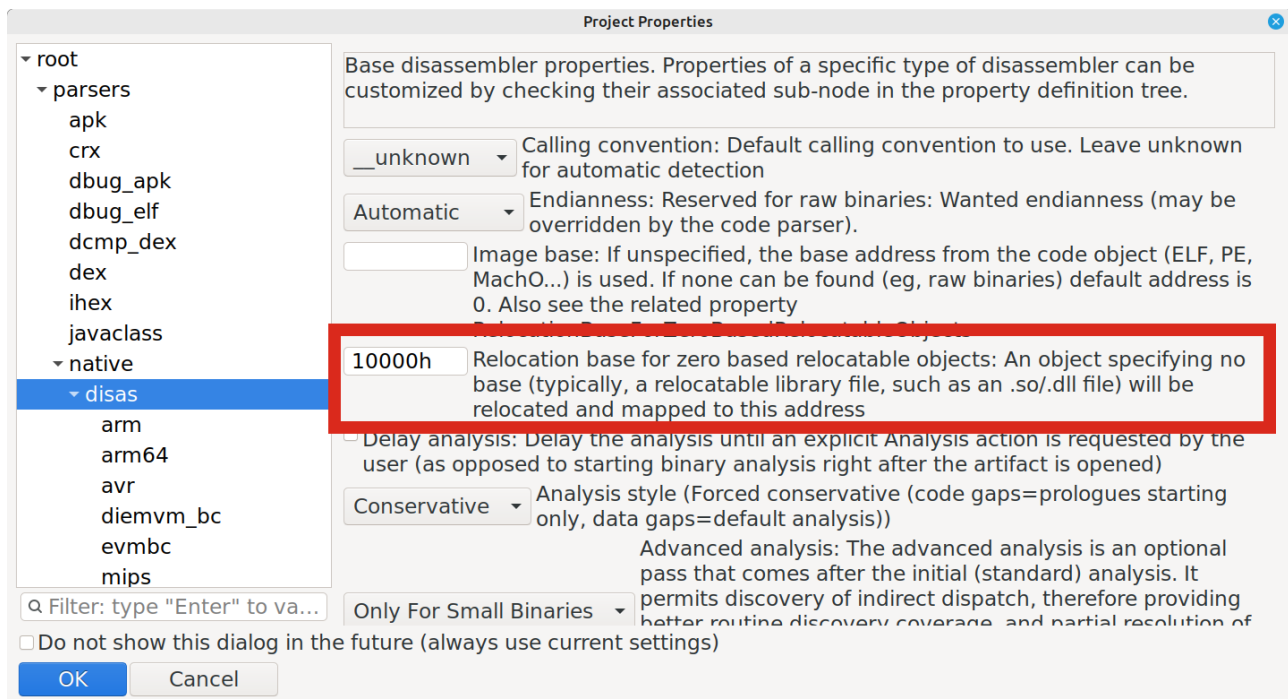


Accessing Flutter function addresses with JEB: head to Code Information

Then, search for `_winningMessage`. The list displays functions and their relative virtual offset.

```
1 updateAfterSuccessfulGuess @ 0x3CDE3C
2 _winningMessage@739388852 @ 0x3CE09C
3 _updateStats@739388852 @ 0x3CE56C
```

So, `_winningMessage` should be located at `0x3CE09C`. Except JEB uses a default setting where all objects with unknown base address are automatically re-located at `0x10000`. Consequently, `_winningMessage` is not at `0x3CE09C` but at `0x3DE09C`.



Default relocation address in JEB. This is customizable in Options/Backend properties: root/parsers/native/disas/

sub_3DE09C

proc

```

push    rbp                                ; xref: sub
mov     rbp, rsp
sub     rsp, 10h
cmp     rsp, qword ptr ds:[r14+38h]
jbe     loc_3DE545

loc_3DE0AE:
mov     rax, qword ptr ss:[rbp+10h]        ; xref: sub
mov     rcx, qword ptr ds:[rax+2Bh]
cmp     rcx, 2
jnz     loc_3DE2FC
mov     rbx, qword ptr ds:[r15+A5Fh]
mov     r10d, 38h
call    sub_40277C
mov     qword ptr ss:[rbp-8], rax
mov     r11d, Eh
mov     dword ptr ds:[rax+Fh], r11d
mov     r11d, 44h
mov     dword ptr ds:[rax+13h], r11d

```

Disassembly of `_winningMessage()`. The address of the function is computed from the snapshot code information added to the base relocation address.

The encoding of Small Integers, and its impact to byte arrays

The assembly for a byte array consists in the following steps:

1. Allocate an array of the necessary size.
2. Load each byte.
3. Create the String. This consists in a call to `createFromCharCodes` which is called by `String.fromCharCodes`

There is an unexpected challenge: **Dart uses a special representation for bytes**. It represents differently:

- Small Integers (SMI). Those are integers which can fit on 31 bits (for 32-bit architectures) or 63 bits (for 64-bit architecture). They are represented with their least significant bit set to 0. The value is encoded on the remaining bits. Example: to push a SMI value of 5 (0x5) to a register, we do not move `mov ip, 0x05` but `mov ip, 0x0a`.
- Medium Integers (Mint). Those which need more bits than 31/63.

For example, let's suppose we have this array ("Pico was there") in Dart:

```
1 List<int> message = [ 0x50, 0x69, 0x63, 0x6f, 0x20, 0x77, 0x61, 0x73,
2   0x20, 0x74, 0x68, 0x65, 0x72, 0x65];
```

We allocate an array of 14 bytes. To represent SMI 14 (1110 in binary), we set the least significant bit to 0 and obtain 11100, which consists in doubling the value. The result equals 28 in decimal, or 0x1c in hexadecimal. Below, the assembly pushes 0x1c to the lower 32 bits of general purpose register r10.

```
1 ; size of array = 0x1c / 2 = 14
2 mov     r10d, 1Ch
3 call    stub_iso_stub_AllocateArrayStub
```

The same occurs for each byte of the array. We are not pushing 0x50, 0x69, 0x63, 0x6f as expected, but their double, 0xa0, 0xd2, 0xc6, 0xde, to the data segment `rax+x`. Rax is a general purpose register.

1	000AE650	mov	r11d, A0h		; P
2	000AE656	mov	qword ptr ds:[rax+17h], r11		
3	000AE65A	mov	r11d, D2h		; i
4	000AE660	mov	qword ptr ds:[rax+1Fh], r11		
5	000AE664	mov	r11d, C6h		; c
6	000AE66A	mov	qword ptr ds:[rax+27h], r11		
7	000AE66E	mov	r11d, DEh		; o

and finally, create the String:

```
1 000AE70D call _StringBase.createFromCharCodes
```

If we need to concatenate strings (e.g. `'stage2: ' + header`), the assembly is straight forward: get the parts to concatenate and call the String concatenation function. In the code below, we are concatenating 2 strings from the object pool, respectively indexes 938 and 939.

```
1 mov     r11, qword ptr ds:[r15+1D57h]
```



```

2  push    r11
3  mov     r11, qword ptr ds:[r15+1D5Fh]
4  push    r11
5  call    _StringBase.+

```

Knowing about SMI representation, the byte array of our Dart program or our Flutter application are easier to locate. Our Dart program loads [98, 101, 97, ...], so we search for `mov something, C4`.

Search in... ☐ Entire Project ☐ Current Unit ☒ Current Document ☐ Case Sensitive Cap results to: 100 Refresh

Search string (*/? accepted): `mov*, c4`

Index	Text	Unit	Document	Location
0	LOAD: xorpi.x86.aot > xorpi.x86.aot > x86_64			xor_stage1+28h

```

LOAD.text:00000000'000AE917      call     stub_iso_stub_AllocateArrayStub
LOAD.text:00000000'000AE91C      mov     qword ptr ss:[rbp-8], rax
LOAD.text:00000000'000AE920      mov     r11d, C4h
LOAD.text:00000000'000AE926      mov     qword ptr ds:[rax+17h], r11
LOAD.text:00000000'000AE92A      mov     r11d, CAh
LOAD.text:00000000'000AE930      mov     qword ptr ds:[rax+1Fh], r11
LOAD.text:00000000'000AE934      mov     r11d, C2h
LOAD.text:00000000'000AE93A      mov     qword ptr ds:[rax+27h], r11
LOAD.text:00000000'000AE93E      mov     r11d, EAh
LOAD.text:00000000'000AE944      mov     qword ptr ds:[rax+2Fh], r11
LOAD.text:00000000'000AE948      mov     r11d, E8h
LOAD.text:00000000'000AE94E      mov     qword ptr ds:[rax+37h], r11
LOAD.text:00000000'000AE952      mov     r11d, D2h
LOAD.text:00000000'000AE958      mov     qword ptr ds:[rax+3Fh], r11
LOAD.text:00000000'000AE95C      mov     r11d, CCh
LOAD.text:00000000'000AE962      mov     qword ptr ds:[rax+47h], r11
LOAD.text:00000000'000AE966      mov     r11d, EAh
LOAD.text:00000000'000AE96C      mov     qword ptr ds:[rax+4Fh], r11
LOAD.text:00000000'000AE970      mov     r11d, D8h
LOAD.text:00000000'000AE976      mov     qword ptr ds:[rax+57h], r11
LOAD.text:00000000'000AE97A      mov     r11d, 5Ah

```

1 match (completed)

Close Help Legacy Dialog ...

JEB understands joker characters in search patterns. We can search for the first SMI of our byte array, represented as 0xC4

Same, in stage 3 of our Flutter application, we create a byte array [81, 94, 89...]. This will be encoded as 0xa2, 0xbc, 0xb2. We can ask the disassembler to search for `mov r11d, 0xa2`. This is how to do it with Radare 2, then we can confirm which hit is correct by disassembling the next few instructions:

```

1  [0x003ce09c]> /ad mov r11d, 0xa2
2  0x003ce31e      41bba2000000    mov r11d, 0xa2
3  0x003ce382      41bba2000000    mov r11d, 0xa2
4  0x003ce38c      41bba2000000    mov r11d, 0xa2
5  0x0006f329      41bb800225a2    mov r11d, 0xa2250280
6  [0x00170000]> pd 4 @ 0x003ce31e
7  ;-- hit0_1:
8  0x003ce31e      41bba2000000    mov r11d, 0xa2
9  0x003ce324      4489580f        mov dword [rax + 0xf], r11d
10 0x003ce328      41bbbc000000    mov r11d, 0xbc

```

Custom register for Dart VM thread

In some cases - particularly for Aarch32 and Aarch64 - your favorite disassembler **fails to correctly recognizes the boundaries of a function**. In that situation, we need to manually define the beginning of the function. To

do so, it helps to know what the beginning of a Dart function looks like (see [Appendix Prologue Snippet](#)).

```

LOAD.text:00446A08 sub_446A08      proc
LOAD.text:00446A08
LOAD.text:00446A08      PUSH      {R11, LR}                ; xref: sub_44692C+3Ch (call)
LOAD.text:00446A0C      ADD       R11, SP, #0
LOAD.text:00446A10      SUB       SP, SP, #8
LOAD.text:00446A14      LDR       R12, [R10, #1Ch]
LOAD.text:00446A18      CMP       SP, R12
LOAD.text:00446A1C      BLLS     sub_481338
LOAD.text:00446A20      LDR       R0, [R11, #8]
LOAD.text:00446A24      LDR       R2, [R0, #27h]
LOAD.text:00446A28      LDR       R1, [R0, #2Bh]
LOAD.text:00446A2C      CMP       R2, #2
LOAD.text:00446A30      CMPEQ    R1, #0
LOAD.text:00446A34      BNE      loc_446D18
LOAD.text:00446A38      LDR       R1, [R5, #533h]
LOAD.text:00446A3C      MOV       R2, #38h
LOAD.text:00446A40      BL       sub_4811D0
;
LOAD.text:00446A44      db 4, 0, 0Bh, E5h, 0Bh, ' ', 80h, E2h, 0Eh, C0h, A0h, E3h
LOAD.text:00446A50      db 0, C0h, 82h, E5h, 0Fh, ' ', 80h, E2h, 'D', C0h, A0h, E3h, 0, C0h, 82h, E5h
LOAD.text:00446A60      db 13h, ' ', 80h, E2h, 'b', C0h, A0h, E3h, 0, C0h, 82h, E5h, 17h, ' ', 80h, E2h
LOAD.text:00446A70      db 'n', C0h, A0h, E3h, 0, C0h, 82h, E5h, 1Bh, ' ', 80h, E2h, '8', C0h, A0h, E3h
LOAD.text:00446A80      db 0, C0h, 82h, E5h, 1Fh, ' ', 80h, E2h, 'T', C0h, A0h, E3h, 0, C0h, 82h, E5h
LOAD.text:00446A90      db "# ", 80h, E2h, ' ', C0h, A0h, E3h, 0, C0h, 82h, E5h, '"', 80h, E2h
LOAD.text:00446AA0      db '8', C0h, A0h, E3h, 0, C0h, 82h, E5h, "+ ", 80h, E2h, ' ', C0h, A0h, E3h
LOAD.text:00446AB0      db 0, C0h, 82h, E5h, "/", 80h, E2h, 'X', C0h, A0h, E3h, 0, C0h, 82h, E5h
LOAD.text:00446AC0      db "3 ", 80h, E2h, 'X', C0h, A0h, E3h, 0, C0h, 82h, E5h, "7 ", 80h, E2h

```

JEB 4.31.0 does not detect 0x446A44 and further as code. This needs to be fixed manually by telling it is code (C).

As usual, a function begins by (1) saving various registers on the stack, (2) moving the frame pointer and (3) allocating necessary space on the stack.

In addition, in Dart assembly code, there is a **stack overflow check**. The assembly **compares the stack pointer with a register Dart dedicates to the current VM thread pointer**. This register is different for each platform, see [Appendix register layout](#). If an overflow is detected, the function branches to an error exit.

```

1 ; push base pointer on the stack
2 push rbp
3 ; the new value for the base pointer is the stack pointer
4 mov rbp, rsp
5 ; allocate 16 bytes
6 sub rsp, 10h
7 ; r14 holds the current Dart VM thread pointer
8 cmp rsp, qword [r14 + 0x38]
9 ; if stack pointer is <= [r14 + 0x38]: jump stack overflow error
10 jbe 0x9e850

```

Dart's custom calling convention (ABI)

The assembly generated for a Dart program demonstrates an unconventional method of passing parameters to functions, which poses a challenge for disassemblers attempting to analyze the code.

For example, this is the assembly of a Dart program concatenating 2 object pool constants ("stage 2:" and "ph0wn{").

```

1 mov r11, qword [r15 + 0x1d3f]
2 push r11
3 mov r11, qword [r15 + 0x1d47]

```

```
4 push r11
5 call fcn.string_concat
```

Usually, on x86_64, the first few arguments of a function are passed in specific registers, and only subsequent arguments are pushed on the stack. For example, [see here](#): the first four arguments on Microsoft Windows are passed using registers, the first six arguments on Unix (System V AMD64 ABI).

Dart does it differently and **passes all arguments on the stack** (`push r11` in both cases).

The same behavior occurs for AAarch32: the first string is loaded in LR register, the second in SB. **Both registers are pushed on the stack** (`stm sp, {sb, lr}`) and provided to the concatenation function.

```
1 ldr lr, [r5, 0xe9f] ; "stage2: "
2 ldr sb, [r5, 0xea3] ; "ph0wn{"
3 stm sp, {sb, lr}    ; push them on the stack
4 bl fcn.concat       ; concatenate strings
```

For Aarch64, the difference is even more striking because **Dart uses a custom stack register**, X15. This is because program counter (PC) and stack pointer (SP) aren't indexed registers on Aarch64, unlike AAarch32. Then, same as other platforms, all arguments are pushed on the stack, whereas, normally, Aarch64 uses X0-X7 for the first 8 arguments, and only uses the stack for the additional arguments.

```
1 LDR      X16, [X27, #1C90h] ; "stage2: "
2 LDR      X30, [X27, #1C98h] ; ph0wn{
3 STP      X30, X16, [X15]    ; we push them on the stack
4 BL       _StringBase.+      ; concatenate both strings: '
    stage2: ph0wn{'
```

XOR encryption loop

The assembly of a Dart (or Flutter) XOR encryption loop is not difficult to identify, but interesting to read in detail because it shows the differences the compiler needs to perform between *small* integers and *medium* integers.

This is our program's `xor_stage2` loop Dart source code, and after, its corresponding assembly for x86_64.

```
1 for (i = 0; i < core.length; i++) {
2   core[i] = core[i] ^ 0x43;
3 }
```

The counter is represented by register `rdi`. It is initialized to 0 (XOR-ing with itself is a common way to null a value). The next 2 lines are specific to Dart: it checks for stack overflow, r14 register being dedicated to the pointer to running VM thread.

```
1 ; initialize edi=0
2 xor     edi, edi
3 loop:
4 cmp     rsp, qword ptr ds:[r14+38h]
5 jbe     overflow_check2
```

Then, it checks for the loop's end condition. The byte array has 28 bytes (0x1c).

```
1 cmp     rdi, 1Ch
2 jnl     loop_finished
```

The following is specific to retrieving a SMI. Remember we stored in the array double its value. When we want to XOR it, the assembly pays attention to XOR it with the real value, i.e it divides it back by 2.

```
1 mov     rax, qword ptr ds:[rcx+8*rdi+17h] ; load core[i]
2 sar     rax, 1                          ; core[i] / 2
3 jnb     not_negative                    ; jump if not negative
4 mov     rax, qword ptr ds:[rax+rax+8]    ; we won't get here
5
6 not_negative:
7 mov     rdx, rax
8 xor     rdx, 43h                        ; core[i] ^ 0x43
```

The compiler has some extra work: it does not know if the XOR result fits in a small or a medium integer. Consequently, it writes code for both cases. It tests if the result fits in a SMI by doubling it and checking if there's an overflow. If there's no overflow, this is a SMI. If it overflow, it must be stored in a Mint.

```
1 ; rdx contains XOR result: core[i] ^ 0x43
2 mov     rax, rdx
3 ; compute rax * 2
4 add     rax, rax
5 ; no overflow: SMI case, overflow: Mint case.
6 jno     no_overflow
7 ; Mint case: create Mint containing XOR result value
8 call    stub_iso_stub_AllocateMintSharedWithoutFPUREgsStub
9 mov     qword ptr ds:[rax+7], rdx
```

Then, it stores the XOR result in `core[i]`:

```
1 no_overflow:
2 mov     rdx, rcx
3 ; get address of core[i]
4 lea     r13, qword ptr ds:[rdx+8*rdi+17h]
5 ; store XOR result in core[i]
6 mov     qword ptr ds:[r13], rax
```

The lines above are run in SMI and Mint case. In the case of a Mint, there is again some extra steps that we can skip as we have SMI. To differentiate between SMI and Mint, the assembly tests the least significant bit of `rax` (al): it should be 0 for a SMI, and 1 for a Mint. Finally, we increment the counter and loop back to the beginning.

```
1 test     al, 1
2 jz      loop_increment
3 ; Mint case
4 ...
5 loop_increment:
6 ; increment counter
7 add     rdi, 1
8 ; loop
9 jmp     loop
```

Dealing with Aarch32 and Aarch64

We have mostly dealt with x86_64 so far, because disassemblers are usually better for that platform. In theory, reversing Aarch32 and Aarch64 is not more difficult: it is just a matter of knowing the different instructions. The issue mostly comes from badly disassembled code.

```

LOAD.text:00446CF8 sub_446CF8      proc
LOAD.text:00446CF8
LOAD.text:00446CF8      ADD      SP, SP, #8
LOAD.text:00446CFC      PUSH     {R0}
LOAD.text:00446D00      LDR      LR, [R5, #943h]
LOAD.text:00446D04      PUSH     {LR}
LOAD.text:00446D08      BL       sub_1DAEE0
LOAD.text:00446D08 sub_446CF8      endp
LOAD.text:00446D0C      db 8, D0h, 8Dh, E2h
LOAD.text:00446D10      db 0, D0h, 'K', E2h, 0, 88h, BDh, E8h
LOAD.text:00446D18 loc_446D18:
LOAD.text:00446D18      MOV      R0, #0
LOAD.text:00446D1C      MOV      R3, #0
LOAD.text:00446D20      CMP      R2, #3
LOAD.text:00446D24      CMPEQ    R1, #0
LOAD.text:00446D28      BNE      sub_446FEC
LOAD.text:00446D2C      LDR      R1, [R5, #533h]
LOAD.text:00446D30      MOV      R2, #2Ch
LOAD.text:00446D34      BL       sub_4811D0
LOAD.text:00446D34 sub_446A08      endp
LOAD.text:00446D38      db 4, 0, 0Bh, E5h, 0Bh, ' ', 80h, E2h
LOAD.text:00446D40      db A2h, C0h, A0h, E3h, 0, C0h, 82h, E5h, 0Fh, ' ', 80h, E2h, BCh, C0h, A0h, E3h
LOAD.text:00446D50      db 0, C0h, 82h, E5h, 13h, ' ', 80h, E2h, B2h, C0h, A0h, E3h, 0, C0h, 82h, E5h
LOAD.text:00446D60      db 17h, ' ', 80h, E2h, A6h, C0h, A0h, E3h, 0, C0h, 82h, E5h, 1Bh, ' ', 80h, E2h
LOAD.text:00446D70      db D0h, C0h, A0h, E3h, 0, C0h, 82h, E5h, 1Fh, ' ', 80h, E2h, BCh, C0h, A0h, E3h
LOAD.text:00446D80      db 0, C0h, 82h, E5h, "# ", 80h, E2h, 86h, C0h, A0h, E3h, 0, C0h, 82h, E5h
LOAD.text:00446D90      db "' ", 80h, E2h, D0h, C0h, A0h, E3h, 0, C0h, 82h, E5h, "+ ", 80h, E2h
LOAD.text:00446DA0      db E6h, C0h, A0h, E3h, 0, C0h, 82h, E5h, "/ ", 80h, E2h, BCh, C0h, A0h, E3h
LOAD.text:00446DB0      db 0, C0h, 82h, E5h, "3 ", 80h, E2h, A2h, C0h, A0h, E3h, 0, C0h, 82h, E5h
LOAD.text:00446DC0      db "7 ", 80h, E2h, A2h, C0h, A0h, E3h, 0, C0h, 82h, E5h, "; ", 80h, E2h
LOAD.text:00446DD0      db BCh, C0h, A0h, E3h, 0, C0h, 82h, E5h, "? ", 80h, E2h, A8h, C0h, A0h, E3h
LOAD.text:00446DE0      db 0, C0h, 82h, E5h, "C ", 80h, E2h, 84h, C0h, A0h, E3h, 0, C0h, 82h, E5h
LOAD.text:00446DF0      db "G ", 80h, E2h, B6h, C0h, A0h, E3h, 0, C0h, 82h, E5h, "K ", 80h, E2h
LOAD.text:00446E00      db 86h, C0h, A0h, E3h, 0, C0h, 82h, E5h, "O ", 80h, E2h, D0h, C0h, A0h, E3h
LOAD.text:00446E10      db 0, C0h, 82h, E5h, "S ", 80h, E2h, B2h, C0h, A0h, E3h, 0, C0h, 82h, E5h
LOAD.text:00446E20      db "W ", 80h, E2h, 0Eh, C0h, A0h, E3h, 0, C0h, 82h, E5h, "[ ", 80h, E2h
LOAD.text:00446E30      db 80h, C0h, A0h, E3h, 0, C0h, 82h, E5h, " ", 80h, E2h, 10h, C0h, A0h, E3h
LOAD.text:00446E40      db 0, C0h, 82h, E5h, "35", 95h, E5h, FCh, E4h, 0, EBh, 0, '0', A0h, E1h
LOAD.text:00446E50      db 4, ' ', 1Bh, E5h, 8, '0', 0Bh, E5h, 0Bh, ' ', 83h, E5h, 0, C0h, A0h, E3h
LOAD.text:00446E60      db 7, C0h, 83h, E5h, ' ', C0h, A0h, E3h, 7, C0h, 83h, E5h

```

Addresses 0x00446D38 are incorrectly disassembled. Define them as Code (C) to recover the instructions. In that case, it is storing SMIs in the byte array.

To identify the XOR loop, do not forget the instruction is named *EOR* (not XOR). Moreover, for Aarch64, pay attention that the XOR is done on a register not an immediate value.

```

1 MOVZ      X16, #37h      ; load XOR Key 0x43 in register X16
2 EOR       X5, X1, X16    ; XOR byte with register X16

```

Consequently, if you wish to search for the XOR loop for Aarch64 platform, you need to adjust search instructions. Searching for *EOR*37h* won't find anything, you need to search for *MOVZ*37h* instead (and check there is an EOR after).

Finally, as we mentioned earlier, pay attention to Dart's specific calling convention on AArch64: X15 is used as custom stack register + all arguments are pushed on the stack.

Flutter reverse examples

Wordle stage 3

This paragraph is a quick write-up to solve w0rdle 3, using knowledge acquired through this research.

We quickly identify that `w0rdle.apk` uses Flutter, with the presence of `lib/<platform>/libapp.so` and `lib/<platform>/libflutter.so`. The strings show the stage 1 flag “ph0wn{you_w1n_darts_stage1}” and the beginning of another flag “ph0wn{”.

```
1 $ strings libapp.so | grep ph0wn
2 ph0wn
3 file:///home/axelle/prog/challenges/ph0wn2022/challenges/w0rdle/.dart_tool/
  flutter_build/dart_plugin_registrant.dart
4 ph0wn{
5 ph0wn w0rdle
6 ph0wn CTF is awesome
7 ph0wn{you_w1n_darts_stage1}
8 I love ph0wn CTF. I guessed w0rdle
```

We load the application in JEB and disassemble the `x86_64` library. We search for the stage 1 flag with the Dart AOT snapshot element.

The screenshot shows the JEB IDE interface. On the left, the Project Explorer displays the file structure of `w0rdle.apk`, including `com.ph0wnctf.wordle`, `Manifest`, `Certificate`, `Bytecode`, `Resources`, `Assets`, `Libraries`, `arm64-v8a`, `armeabi-v7a`, `x86_64`, `libapp.so`, `metadata`, `x86_64 image`, `dart_aot_snapshots`, and `libflutter.so`. The main window shows the disassembly of the `dart_aot_snapshots.Pool` class. A search dialog is open, displaying the search string `ph0wn{` and the results of the search. The search results table shows two matches: index 5810 and index 5811. Index 5810 contains the string `ph0wn{` and index 5811 contains the string `ph0wn{you_w1n_darts_stage1}`. The search dialog also shows the search string `ph0wn{` and the results of the search.

Stage 1 flag is in the Object Pool at index 5811

We search for code using this pool index (index 5810 with the beginning of a flag is interesting too). We know the instruction is going to be something like `mov *ds: [r15+XXh]`, where pool index = `XX // 8`. So `XX` can be `0xb598-0xb59f`. We disassemble the `x86_64` code (wait till analysis has finished) and search for that.

Search in... ☐ Entire Project ☐ Current Unit ☒ Current Document ☐ Case Sensitive Cap results to: 100 Refresh

Search string (*/? accepted): `mov*[r15+b59*h]`

Index	Text	Unit	Document	Location
1	LOAD: wordle			sub_3DE09C+421h
2	LOAD: wordle			sub_3DE09C+467h
3	LOAD: wordle			sub_3DE09C+48Eh

text:00000000'003DE529 ret
 text:00000000'003DE52A loc_3DE52A: mov r11, qword ptr ds:[r15+B59Fh] ; xref: sub_3DE09C+467h
 text:00000000'003DE52A
 text:00000000'003DE531 push r11
 text:00000000'003DE533 call sub_1C70E8

10 matches (completed)

Close Help Legacy Dialog ...

This piece of code uses the stage 1 flag.

```

1 ; load ph0wn{you_w1n_darts_stage1}
2 003DE52A mov r11, qword ptr ds:[r15+B59Fh] ; xref:
   sub_3DE09C+266h (cbr)
3 003DE531 push r11
4 003DE533 call sub_1C70E8

```

We notice the same function uses constant `ph0wn{` with index 5810.

```

1 ; load "ph0wn{" from pool objects
2 003DE503 mov r11, qword ptr ds:[r15+B597h]
3 003DE50A push r11
4 003DE50C push rax
5 003DE50D call sub_196B98

```

We quickly identify 2 arrays in the function:

```

1 003DE0B6 cmp rcx, 2
2 003DE0BA jnz loc_3DE2FC
3 003DE0C0 mov rbx, qword ptr ds:[r15+A5Fh]
4 ; array size
5 003DE0C7 mov r10d, 38h
6 003DE0CD call sub_40277C
7 003DE0D2 mov qword ptr ss:[rbp-8], rax
8 ; load array bytes
9 003DE0D6 mov r11d, Eh
10 003DE0DC mov dword ptr ds:[rax+Fh], r11d
11 003DE0E0 mov r11d, 44h
12 003DE0E6 mov dword ptr ds:[rax+13h], r11d
13 003DE0EA mov r11d, 62h
14 ...
15 003DE2FE cmp rcx, 3
16 003DE302 jnz loc_3DE52A
17 003DE308 mov rbx, qword ptr ds:[r15+A5Fh]
18 ; array 2 size
19 003DE30F mov r10d, 2Ch
20 003DE315 call sub_40277C
21 003DE31A mov qword ptr ss:[rbp-8], rax
22 ; load bytes of array2

```



```

23 003DE31E      mov     r11d, A2h
24 003DE324      mov     dword ptr ds:[rax+Fh], r11d
25 003DE328      mov     r11d, BCh
26 003DE32E      mov     dword ptr ds:[rax+13h], r11d
27 003DE332      mov     r11d, B2h

```

The first array is XORed with 0x43:

```

1 003DE243      xor     rdx, 43h
2 003DE247      lea     rax, qword ptr ds:[rdx+rdx]

```

The second array is XORed with 0x37:

```

1 003DE44F      xor     rdx, 37h

```

We notice the first array is preceded by a comparison with 2, while the second array is compared to 3. We guess this is the stage indicator. So, stage 3 is the second array, and it is XORed with 0x37. We recover the SMLs: [0xa2, 0xbc, 0xb2, ...], convert them to integers, apply the XOR and read the flag:

```

1 length = 0x2c // 2
2 table = [0xa2, 0xbc, 0xb2, 0xa6, 0xd0, 0xbc, 0x86, 0xd0, 0xe6, 0xbc, 0xa2, 0
    xa2, 0xbc, 0xa8, 0x84, 0xb6, 0x86, 0xd0, 0xb2, 0x0e, 0x80, 0x10]
3
4 assert len(table) == length, f"The table does not have the expected length:
    {length}"
5
6 ciphertext = [i // 2 for i in table]
7 print("Ciphertext: ", ciphertext)
8
9 plaintext = [chr(i ^ 0x37) for i in ciphertext]
10 print("Flag: ", ''.join(plaintext))

```

The plaintext is `find_it_Difficult_n0w?`. As we have another pool object which is `ph0wn{` (and that the official flag format for Ph0wn CTF is `ph0wn{xxxxx}`), we assume the beginning and trailing part will be concatenated to the main plaintext to create the full flag `ph0wn{find_it_Difficult_n0w?}`

There are several other ways to solve the challenge. For instance, a participant might spot the method name `_winningMessage` in Code Information.

	x86_64	armeabi-v7a	arm64-v8a
pool index for <code>ph0wn{</code>	5810	5901	5797
pool index for <code>ph0wn{</code> <code>you_w1n_darts_stage1}</code>	5811	5902	5798
<code>_winningMessage</code> address	0x003CE09C	0x00436A08	0x003C71A0

Android/MoneyMonger malware

The MoneyMonger family was discovered in May 2022 and consists in loan scams. The cybercriminals attract victims with easy money, collect personal data and uses the information to blackmail them later. Victims have reported being harrassed by phone calls, SMS to re-pay their loan even before the due date. Some even reported being harrassed without concluding any contract. The criminals go as far as threatening victims to leak sensitive information.

In December 2022, a Flutter version of MoneyMonger was discovered and detailed in (Ortega 2022). The researcher used static analysis to reverser malicious parts within the Dalvik layer, and fell back to dynamic analysis for the difficult parts, in particular parts implemented in Dart. With our learned knowledge, we are able to go further into the reversing of Dart.

We study malicious sample `e7fdecbac151325ad88054fb2287d7a5b48234f63d81fc35e2425b57ebb559e3`. It has only been compiled for AArch64, which makes the task more difficult with regards to the current status of tools.

The manifest lists an entry point `com.fastrupee.fastrupeepeefa.MainActivity` which inherits from `FlutterApplication` and initiates the initialization of the Flutter framework.

Platform Channels

Communication between the Dalvik layer and Flutter is perform through *Platform Channels*. *Platform channels are implemented by Flutter* and are the standard way to communication between a Flutter client application and its host (Android in our case). In particular, Flutter provides a class named `MethodChannel` to help *Dart* code call *Java* or *Kotlin* code.

MoneyMonger's `MainActivity.kp()` method defines the mapping between both worlds:

- `getGaid()` calls `getAdvertisingIdInfo().getId()`
- `setConfig()` adds an entry named `smsCount` to a shared preferences file
- `removeCrashLog()` removes appropriate entries from the shared preferences file
- `getCrashLog()` reads the `crash_message` and `crash_stacktrace` entries from the shared preferences file
- `aesEncrypt()` calls `b2.dp()` with appropriate arguments provided by the Dart code
- same for several other functions like `getLocation()`, `toHex()`, `fromHex()`, `aesDecrypt()`, `getAllDeviceInfo()`, `reportBranchEvent()`

```
1     private final Object kp(MethodCall methodCall0, Result
2         methodChannel$Result0) {
3         SharedPreferences.Editor sharedPreferences$Editor0;
4         if(l8.a6(methodCall0.method, "getGaid")) {
5             try {
6                 return AdvertisingIdClient.getAdvertisingIdInfo(this).getId
7                     ();
8             }
9             catch(Exception exception0) {
```

```

8         exception0.printStackTrace();
9         return null;
10    }
11 }
12
13 int v = 0;
14 if(l8.a6(methodCall0.method, "setConfig")) {
15     String s = (String)methodCall0.argument("smsCount");
16     if(s == null) {
17         return null;
18     }
19
20     sharedPreferences$Editor0 = this.getSharedPreferences("data", 0)
21         .edit().putString("smsCount", s);
22     sharedPreferences$Editor0.commit();
23     return null;
24 }

```

Dart AOT Snapshot

In the Dart AOT snapshot, the Code Information displayed by JEB (or searching for strings in `libapp.so`) shows the application mostly consists in classes to handle the UI: `Widgets`, `UserPageConfig`, `WebViewController`, `StartupPageConfig`... Class `MyUtilsPlugin` references several methods mentioned by [11]: `aesDecrypt`, `setConfig`, `getAllDeviceInfo`.

Search in...
☐ Entire Project
☐ Current Unit
☒ Current Document
☐ Case Sensitive
Cap results to: 100
Refresh

Search string (*/? accepted): aesEncrypt

Index	Text	Unit	Document	Location
0	aesEncrypt @ 0x1F8854	8db66		14250,2

getCrashLog @ 0x1F858C
removeCrashLog @ 0x1F8E04
getGaid @ 0x1F9324
runLiveness @ 0x1F8420
getFirebaseMessageToken @ 0x1F91C4
[aesEncrypt](#) @ 0x1F8854
aesDecrypt @ 0x1F89C8
setConfig @ 0x1F8C98
getPackageName @ 0x1F8B38
initByContext @ 0x1F7E68

1 match (completed)

Close Help Legacy Dialog ...

The following methods are part of a class named `MyUtilsPlugin`

When the Flutter method `getGaid` is called, method channel glue between Flutter and Dalvik gives the information to the Java code, and this triggers the call of `getAdvertisingIdInfo()` on Java side.

We are able to locate the (Dart) implementation of `getGaid`, `setConfig`, `aesEncrypt`, `aesDecrypt` etc.

method name	address
aesDecrypt	0x1F89C8
aesEncrypt	0x1F8854
setConfig	0x1F8C98

setConfig

For example, we located `setConfig()` and recognize the typical ARM prologue:

```

1 com_fastrupée_fastrupéepeefaMyUtilsPlugin__setConfig proc
2
3             STP        X29, X30, [X15, #FFFFFFF0h]!    ; xref:
               sub_4196E8+1D0h (call) / 45CDDCh (call)
4             MOV        X29, X15
5             SUB        X15, X15, #18h
6             LDR        X16, [X26, #40h]
7             CMP        X15, X16
8             B.LS       loc_208DF8

```

Actually, this function is a wrapper function to communicate with the Dalvik layer. We set it fetches the string `setConfig` at index 10185 from the Object Pool. The access to the index is performed in 2 instructions: (1) compute 0x13000, then (2) add 0xE48. The resulting offset divided by 8 is 10185.

```

1             BL         sub_20EBC
2             ADD        X17, X27, #13h, LSL #12          ; setConfig
3             LDR        X17, [X17, #E48h]
4             STUR       X17, [X0, #1Fh]
5             LDUR       X1, [X29, #FFFFFFF0h]
6             STUR       X1, [X0, #27h]
7             STUR       X0, [X1, #5Fh]
8             STR        X0, [X15, #FFFFFFF8h]!
9             BL         ::__asyncThenWrapperHelper@4048458

```

The `setConfig` method is called by the code below which (1) gets the current SMS count and (2) provides this count as argument to the `setConfig` method. As a matter of fact, from the Dalvik glue, we saw that `setConfig` was taking one argument named `smsCount`.

```

1 BL         com_fastrupée_fastrupéepeefaPermissionConfig__get:getSmsCount
2 STR        X0, [X15, #FFFFFFF8h]!
3 BL         com_fastrupée_fastrupéepeefaMyUtilsPlugin__setConfig
4 ADD        X15, X15, #8

```

The function `getSmsCount` calls `getConfigValue`:

```
1 LDR      X4, [X4, #6D8h]
2 BL      com_fastrup_eepefaLtConfig___com_fastrup_eepefagetConfigValue@
```

AES encryption/decryption

As for the Flutter `aesEncrypt` and `aesDecrypt` functions, we see the actual AES encryption/decryption is performed from the Dalvik layer, and that it takes two arguments: a password (secret key) and a buffer.

The password can be recovered by different methods:

1. Search for 16-byte strings in `libapp.so`. We notice `D4JcGjcw489iiEq1` (actually, `strings` does not output the string boundary correctly and adds a trailing 8, but keys are 16 bytes long for AES, so we can remove it)
2. Search for 16-byte strings in the Object Pool: the key is at index `4102`.
3. Use a Frida hook on the Java layer to display the password. The same hook can also be used to print decrypted values.

```

1  r.parseInt("\\1022137011\\");\\n      if (Math.random() > x) {\\n\\n
      } else {\\n\\n      }\\n      }\\n{\\n      ret = (
      int)(Double.MAX_VALUE * Math.random());\\n      }\\n{\\n      int a
      = (int)(Integer.MAX_VALUE * Math.random());\\n      int b = (int)(
      Integer.MAX_VALUE * Math.random());\\n      ret = a + b;\\n
    }\\n{\\n      ret = (WindowManager) this.getApplicationContext().
      getSystemService(Context.WINDOW_SERVICE);\\n      }\\n{\\n      ret
      = (WindowManager) this.getApplicationContext().getSystemService(Context
      .WINDOW_SERVICE);\\n      }\\n}\\n@Override\\n      protected void onCreate(
      Bundle savedInstanceState) {\\n      super.onCreate(savedInstanceState)
      ;\\ntry {com_fastrupee_fastrupeepeeefaActivity40();} catch(Exception e) {
      \\n      }\\n}\\n}}}}
2  dp: password=D4JcGjcw489iiEq1
3  dp: plaintext={}
4  dp: password=D4JcGjcw489iiEq1
5  dp: plaintext={"AID":"419325ce0c5f52d2","UUID":"cd4842a0-b89b-11ed-8d26-87
      e38aff63fe","GAID":"f1db4ccc-af6a-42db-a428-a7f6155068cb","VERSION":"
      1.0.18","SE":"true"}

```

- The password is `D4JcGjcw489iiEq1`.
- The text above `dp: password=D4JcGjcw489iiEq1` is the *decrypted* configuration. It is dumped in `FlutterSharedPreferences.xml`
- The last plaintext is a JSON value containing device information. This plaintext is *encrypted* on request by the Dart code by the Dalvik layer. And indeed, if we look at the code of the routine which calls `aesEncrypt` in Dart, we notice it operates on JSONs:

```
1 STP    X1, X16, [X15, #FFFFFF0h]!  
2 LDR    X4, [X27, #4A8h]
```

```
3 BL      JsonCodec__encode
4 ADD     X15, X15, #10h
5 ...
6 BL      com_fastruppee_fastruppeeefaMyUtilsPlugin__aesEncrypt
```

With MoneyMonger, the malicious code is located on Dalvik's side, which makes it easier to reverse than we could have expected. Malware such as **FluHorse** implement the malicious parts in Dart and are more tricky to reverse. See (Apvrille 2023c).

State of the Art

Research on reversing Flutter consists in at most a handful of interesting articles and tools.

[Doldrum](#) and [Darter](#) dump Dart AOT snapshots. However, due to frequent format changes, these projects became outdated and could not keep up with the modifications, rendering them obsolete. For example, Doldrum works until Dart version 2.12. It throws an exception for any more recent version (here 2.19.1). The fix is far more complex than just adding the new snapshot hash ⁴.

```
1      raise Exception('Unsupported Dart SDK, snapshot hash: ' + snapshotHash)
2 Exception: Unsupported Dart SDK, snapshot hash:
      adb4292f3ec25074ca70abcd2d5c7251
```

An alternative approach is employed by [reFlutter](#), which instruments code to dynamically dump memory addresses of objects. By design, it only dump code it *visits*. Even so, it was unable to dump the address of any useful method of the w0rdle 3 challenge.

```
1 Library:'package:flutterdle/game.dart' Class: Flutterdle extends Object {
2     // missing dump
3 }
4
5 // successful dump of address if Stats.fromJson in domain.dart
6 Library:'package:flutterdle/domain.dart' Class: Stats extends Object {
7     Function 'toJson':. (Stats) => Map<String, dynamic> {
8         Code Offset: _kDartIsolateSnapshotInstructions + 0
9         x00000000000109648
10    }
```

Among the articles, (Lipke 2020) stands out as one of the first to address in detail Dart reverse engineering. [4] does an excellent job at outlining the core challenges and providing a viable solution with IDA Pro. However, the solution is complex: memory dump of the sample, use of several scripts, and IDA Pro plugins to automate function name recovery.

Other articles, such as (Loura 2020), focus on specific aspects, while (Software 2022), (Alexander 2023), (“Reverse Engineering a Flutter App by Recompiling Flutter Engine” 2021) and my own (Apvrille 2022a) explore practical cases and show how to use *reFlutter*.

⁴For Darter too: changing the `EXPECTED_VERSION` constant is in `darter/constants.py` only superficially fixes the error message.

One of the key *novelties* of this paper are the **disassembly of byte arrays in Dart**. This hasn't ever been covered. *It is particularly useful to find secrets in CTF challenges, CrackMes or malicious code.** The other contributions are:

1. **Dart's calling convention specificities.** ([Batteux 2022](#)) had noticed it for Aarch64, but we see it is also true for Aarch32 and x86_64 to some extent.
2. **A comprehensive approach to reversing Dart or Flutter** with Radare or JEB. There are fragments of information in various articles, none provide specific details on the task. This paper details how to spot the beginning and end of functions when disassemblers fail to identify them, highlight the stack overflow instructions which are specifically inserted in Dart assembly, or tricks to search for XOR loops over x86_64, arm32 and arm64 platforms.

Conclusion and Future Work

The current support for assembly produced by Flutter applications is limited. Besides Flutter's intentional and default obfuscation, this is due to several concepts of Dart: the Object Pool with indirect access to constant strings, the special representation of Small Integers, the use of custom registers (object pool, current Dart VM thread, and even custom stack register for AAarch64) and an unusual calling convention where all parameters are pushed on the stack whatever their number.

With research from this paper, we are able to tweak disassemblers manually to assist reverse engineering. We are able to reverse a basic Dart release program, solve a Flutter CTF challenge and dig in Flutter parts of a malware of the MoneyMonger family.

The next step consists in enhancing disassemblers. For instance, the following would particularly interest:

- Recognize encoding of Small Integers and automatically annotate code with the corresponding integer value
- Link strings of the Object Pool to their use in assembly code, instead of manually computing the pool index and looking it up
- Organize Flutter methods in the classes they belong to. This would help recover the object oriented aspect of the source code.
- Detect Dart's calling convention, where arguments are pushed on the stack. On Aarch64, detect Dart's custom stack register. Understanding the calling convention will help produce better decompiled code.
- Automatically detect and annotate Dart's stack overflow check lines.

Appendix

Dart commands

Create a standalone Dart project: `dart create -t console projectname`

Dart program formats

Dart formats	Command to create it		Run it...
Source code			<code>dart run hello.dart</code>
Self contained executable	<code>dart compile exe hello.dart</code>		<code>./hello.exe</code>
AOT snapshot	<code>dart compile aot-snapshot hello.dart</code>		<code>FLUTTER_DIR/flutter/bin/cache/dart-sdk/bin/dartaotruntime hello.aot</code>
JIT snapshot	<code>dart compile jit-snapshot hello.dart</code>		<code>dart run hello.jit</code>
Kernel snapshot	<code>dart compile kernel hello.dart</code>		<code>dart run hello.dill</code>

Dart formats	Portable	Contains a Dart Runtime	Description
Source code	Yes	No	<code>dart run sourcecode</code> uses Dart VM's JIT compiler to run the code
Self contained executable	No	Yes	
AOT snapshot	No	No	Code is <i>natively</i> compiled for the platform. Used for Flutter <i>release</i> builds. The command <code>dartaotruntime</code> contains the runtime.
JIT snapshot	No	No?	Usually slower than AOT snapshots.
Kernel snapshot	Yes	No	Portable representation of code. Used for Flutter <i>debug</i> builds.

AOT snapshot format

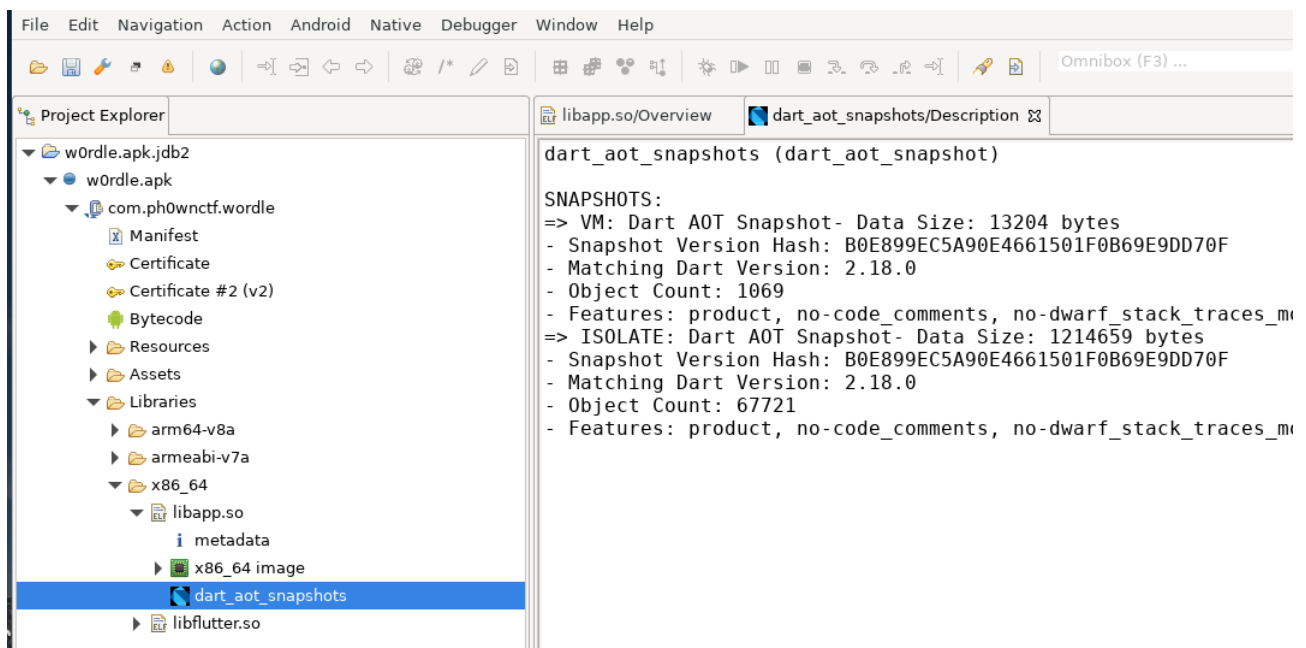
Dart AOT snapshot formats have evolved through time. Therefore, one of the first steps is to read the *version*, which is present in the snapshot header. It can be read with JEB, my Python script ([Apvrille 2022b](#)) or using

my [ImHex](#) pattern ([Apvrille 2023a](#))

```

1 $ python3 flutter-header.py -i ./hello.aot
2 ===== Flutter Snapshot Header Parser =====
3 Snapshot
4     offset  = 512 (0x200)
5     size    = 3519
6     kind    = SnapshotKindEnum.kMessage
7     version = 2.19.1
8     features= product no-code_comments no-dwarf_stack_traces_mode no-
          lazy_dispatchers dedup_instructions no-asserts x64 linux no-
          compressed-pointers null-safety
9 -----
10 Snapshot
11     offset  = 34240 (0x85c0)
12     size    = 98694
13     kind    = SnapshotKindEnum.kMessage
14     version = 2.19.1
15     features= product no-code_comments no-dwarf_stack_traces_mode no-
          lazy_dispatchers dedup_instructions no-asserts x64 linux no-
          compressed-pointers null-safety
16 -----

```



JEB reading a Dart AOT snapshot header

Project Explorer

- xorpi.aot
 - xorpi.aot
 - metadata
 - x86_64 image
 - dart_aot_snapshots

Filter: type "Enter" t...

x86_64 image/Hierar...

Name	Address
xor_stage2	AE5ECh
xor_stage1	AE8F8h

x86_64 image/Assembly dart_aot_s

- 922: "value"
- 923: ?
- 924: ?
- 925: ?
- 926: ?
- 927: ?
- 928: ?
- 929: ""
- 930: ?
- 931: ?
- 932: ?
- 933: ?
- 934: "function result"
- 935: "stage2: "
- 936: "ph0wn{"
- 937: "stage1: "
- 938: ": "

JEB shows the Object Pool of a Dart program and display readable strings for each index. There are a few bugs though, and this doesn't work all the time

Page: 0x01 / 0x01 Region: 0x00000000 - 0x003FC397 (0 - 4178839)

Selection: 0x00003A80 - 0x00003A83 (0x4 | 4 Bytes) Data Size: 0x003FC398 (0x3FC398 | 3.99 MiB)

Data visualizer: Little Hexadecimal (8 b) 16

Pattern Data

Address	Value	Size	Comments
0x00003A80	0x000001F0	0x000001F4	0x000001F3
0x00003A84	0x000001F0	0x000001FB	0x000001F3
0x00003A88	0x000001FC	0x00000203	0x000001F3
0x00003A8C	0x00000204	0x00000223	0x000001F3
0x00003A90	0x00000224	0x000002B2	0x000001F3

ImHex showing 2 Dart AOT snapshots in libapp.so

Description	Value	Size	Comments
Magic number	f5f5dcdc	4 bytes	kMagicValue
Size		8 bytes	
Snapshot kind		8 bytes	SnapshotKindEnum
Version hash		32 bytes	

Features

Variable:
Null termi-
nated
string

For example, this is a Dart AOT Snapshot at 0x140:

```

1 00000140 f5 f5 dc dc c2 0d 00 00 00 00 00 00 00 03 00 00 00
   |.....|
2 00000150 00 00 00 00 61 64 62 34 32 39 32 66 33 65 63 32 |....
   adb4292f3ec2|

```

- `f5 f5 dc dc c2`: Magic number
- size: 4 bytes (`0d 00 00 00`)
- kind: `SnapshotKindEnum.kMessage` (`00 00 00 03`)
- version_hash: `adb4292f3ec25074ca70abcd2d5c7251`
- features: null terminated string

After the Snapshot header, there is *Cluster Info*:

Description	Size	Comments
Base Object Count	ULEB128	
Object Count	ULEB128	
Cluster Count	ULEB128	
Code order length	ULEB128	

- ClassId: U32

Dart strings

Dart strings are created one of following ways:

1. Using `String`, e.g. `String flag = 'flag{congrats}'`
2. `String` are immutable, so if they need to be manipulated, use the `StringBuffer` class, and, if needed, convert to a `String` with `toString()`.

```

1 void main() {
2     final buffer = StringBuffer('Pico le Croco');
3     buffer.write(' has big teeth');
4     print(buffer);
5     print(buffer.toString());
6 }

```

3. A string is also a sequence of Unicode UTF-16 code units, which are represented as integers. So, they can also be created from list of integers, or types derived from integers (e.g Uint8List). Note that if UTF-8 conversion is needed, there are `encode()` and `decode()` methods from the `dart:convert` library.

```
1 // String to bytes
2 String foo = 'Hello world';
3 List<int> bytes = foo.codeUnits;
4 // Bytes to String
5 String bar = String.fromCharCode(bytes);
```

Object Pool serialization

When you compile a Flutter app,

1. Each object is serialized
2. A reference to each Dart object is stored in the *Dart object pool*
3. The object pool itself is *serialized* too
4. The Dart code is then modified, and each access to an object is replaced with an indirect access via the pool index.

At runtime, to access objects:

1. Retrieve the serialized objects from the Dart AOT snapshot format
2. De-serialize all objects in Flutter
3. Whenever the code asks to access a given pool index, the object pool looks up for the corresponding object and returns it.

Register layout

Architecture	Register	Use
arm7eabi	r5	Object Pool
	r10	Pointer to running VM thread
	r11	Frame Pointer
	r13	Stack Pointer
	r14	Link Register
	r15	Program Counter
arm64	X15	Custom Stack Pointer

Architecture	Register	Use
	X26	Pointer to running VM thread
	X27	Object Pool
	X29	Frame Pointer
	X30	Link Register
x86_64	r10	Arguments descriptor register
	r12	Code register
	r14	Pointer to running VM thread
	r15	Object Pool

Where is it implemented?

	URL
ClassId enumeration	sdk/runtime/vm/class_id.h
ObjectPool class	runtime/vm/object.h
Register enumeration	runtime/vm/constants_arm.h , runtime/vm/constants_arm64.h , runtime/vm/constants_x64.h
Snapshot class	runtime/vm/snapshot.h
Snapshot serialization	sdk/runtime/vm/app_snapshot.cc
Serialization of integers	runtime/vm/app_snapshot.cc
Class Smi	runtime/vm/object.h
Cluster Info serialization	runtime/vm/app_snapshot.cc
Read/Write Uint	runtime/vm/kernel_binary.h
Read/Write LEB128	runtime/vm/datastream.h L173

Code snippets for various platforms

Storing *representations* of SMIs

On Aarch32,

```
1 MOV      R12, #A2h
2 STR      R12, [R2]
```

On Aarch64,

```
1 ; Assign value A2 to X17
2 MOVZ     X17, #A2h
3 ; Store Unsigned Register
4 ; We are writing W17 (least significant 32 bits of X17) at
5 ; the address of X0 + Fh
6 STUR     W17, [X0, #Fh]
```

On x86_64,

```
1 mov r11d, A2h
2 mov dword ptr ds:[rax+Fh], r11d
```

Storing a SMI in a byte array

We need (1) to double the SMI to represent it, and (2) store the representation.

On Aarch32, with:

- the value to store is R3
- the byte array is R2
- the counter is R6

```
1 LSL      R0, R6, #1
2 ...
3 MOV      R8, R0
4 ...
5 ; R0 = R1
6 EOR      R0, R1, #0
7 MOV      R9, R0
8 ; Logical Shift Left: multiply result by 2
9 LSL      R0, R3, #1
10 ; Check negative value
11 CMP      R3, R0, ASR #1
12 CMPEQ    R9, R0, ASR #31
13 BEQ      not_negative2
14 ; we should not get here
15 BL       sub_481430
16 STR      R3, [R0, #7]
17 STR      R9, [R0, #Bh]
18 not_negative2:
19 MOV      R1, R2
```

```

20 ; compute the offset in the array: R9 = R2 + R6*4
21 ; because R8 = 2*R6
22 ADD     R9, R1, R8, LSL #1
23 ADD     R9, R9, #Bh
24 ; store the representation of the SMI in the array
25 STR     R0, [R9]
26 ; R0 is a SMI representation, so R0 & 1 = 0
27 TST     R0, #1 ; tests R0 & 1
28 ; Branch if previous test was 0
29 BEQ     increment

```

On Aarch64 with:

- the value to store is X5
- the byte array is X1
- the counter is X4

```

1 ; Signed BitField Insert Zeroes
2 ; copies the 31 lower bits of X5 at position 1 in X0
3 ; this doubles X5!
4 SBFIZ    X0, X5, #1, #1Fh
5 ; Shift right X0 and compare with X5
6 CMP      X5, X0, ASR #1
7 ; branch if equal
8 B.EQ     equal_case
9 ; we should not get here
10 BL      sub_402188
11 STUR     X5, [X0, #7]
12 equal_case:
13 MOV      X1, X2
14 ; offset = array+(X4*4)
15 ADD      X25, X1, X4, LSL #2
16 ADD      X25, X25, #Fh
17 ; store the X0Red value (W0) at the address of X25
18 STR      W0, [X25]
19 ; bit 0 of W0 will be 0 because it's an SMI, so we will branch to increment.
20 TBZ      W0, #0, increment

```

On x86_64 with:

- the value to store is eax
- the byte array is rdx
- the counter is rdi

```

1 ; Load Effective Address works on addresses (no access to memory)
2 ; So, rax = 2 * rdx
3 lea      rax, qword ptr ds:[rdx+rdx]
4 mov      rbx, rdx
5 ; Right shift 31 bits: we only keep the sign bit
6 sar      rbx, 1Eh
7 add      rbx, 1
8 ; at this point if rdx was negative, then rbx = 2
9 ; if rdx was positive, then rbx = 1
10 cmp     rbx, 2

```

```
11 ; branch if below 2
12 jb      is_below
13 ; we should not get here
14 call    sub_402B00
15 mov     qword ptr ds:[rax+7], rdx
16 is_below:
17 ; compute the offset where to store the value
18 lea     r13, qword ptr ds:[rdx+4*rdi+Fh]
19 ; store value
20 mov     dword ptr ds:[r13], eax
```

Getting SMI from a byte array

This consists in

1. Reading the value from the byte array
2. Dividing it by 2 for a SMI

For Aarch32:

```
1 ; compute offset address R12 = R2 + R6 * 4
2 ; because R8 = R6 * 2 previously
3 ADD     R12, R2, R8, LSL #1
4 ; load byte from that position
5 LDR     R0, [R12, #Bh]
6 ; Arithmetic Shift Right: store sign bit of R0 in R1
7 ASR     R1, R0, #31
8 ; perform division by 2
9 ASRS    R9, R0, #1
```

For Aarch64:

```
1 ; X2 holds the array
2 ; X4 is the counter
3 ; we compute offset address X16 = X2 + X4*4
4 ADD     X16, X2, X4, LSL #2
5 ; Load Unsigned Register W0 = &(X16 + Fh)
6 LDUR    W0, [X16, #Fh]
7 ;
8 ADD     X0, X0, X28, LSL #32
9 ; Sign-Extended BitField Extract
10 ; Extract bits 1 to 31 (with sign extension) and copies to X1
11 ; This actually performs a division by 2
12 SBFX    X1, X0, #1, #31
```

For x86_64:

```
1 ; rcx holds the array
2 ; rdi is the counter
3 ; we compute offset address rcx+4*rdi+Fh
4 ; the content of this address will be in eax
5 mov     eax, dword ptr ds:[rcx+4*rdi+Fh]
6 ; don't care
```



```
7 add      rax, qword ptr ds:[r14+48h]
8 ; the content is now in rdx
9 movsxd   rdx, eax
10 ; shift arithmetic right
11 ; this divides by 2
12 sar      rdx, 1
```

Prologue snippet + stack check

For Aarch32:

```
1 ; push frame pointer (r11) and link register on the stack
2 PUSH     {R11, LR}
3 ; move frame pointer to the bottom of the stack
4 ADD      R11, SP, #0
5 SUB      SP, SP, #8
6 MOV      R0, #2Ch
7 ; check stack overflow
8 ; r10 holds the current VM thread pointer
9 LDR      R12, [R10, #1Ch]
10 CMP     SP, R12
11 BLLS    sub_32FCF4
```

For Aarch64:

```
1 STP      X29, X30, [X15, #FFFFFFF0h]!
2 MOV      X29, X15
3 SUB      X15, X15, #10h
4 LDR      X16, [X26, #38h]
5 CMP      X15, X16
6 B.LS     loc_3D75DC
```

For x86_64:

```
1 ; push base pointer on the stack
2 push rbp
3 ; the new value for the base pointer is the stack pointer
4 mov rbp, rsp
5 ; allocate 16 bytes
6 sub rsp, 10h
7 ; r14 holds the current Dart VM thread pointer
8 cmp rsp, qword [r14 + 0x38]
9 ; if stack pointer is <= [r14 + 0x38]: jump stack overflow error
10 jbe 0x9e850
```

XOR

For Aarch32: `EOR R3, R9, #37h`

For Aarch64:

```
1 MOVZ     X16, #37h
```

```
2 EOR      X4, X3, X16
```

For x86_64: `xor rdx, 0x37`

Load a Pool Object

- For Aarch32: `LDR R1, [R5, #433h]`
- For Aarch64: `LDR X16, [X27, #433h]`
- For x86_64: `mov rbx, qword ptr ds:[r15+433h]`

String concatenation

For Aarch32:

```
1 ldr lr, [r5, 0xe9f] ; "stage2: "  
2 ldr sb, [r5, 0xea3] ; "ph0wn{"  
3 stm sp, {sb, lr}    ; push them on the stack  
4 bl fcn.concat       ; concatenate strings
```

For Aarch64:

```
1 LDR      X16, [X27, #1C90h] ; "stage2: "  
2 LDR      X30, [X27, #1C98h] ; ph0wn{  
3 STP      X30, X16, [X15]    ; we push them on the stack  
4 BL       _StringBase.+     ; concatenate both strings: '  
    stage2: ph0wn{'
```

For x86_64:

```
1 mov r11, qword [r15 + 0x1d3f]  
2 push r11  
3 mov r11, qword [r15 + 0x1d47]  
4 push r11  
5 call fcn.string_concat
```

Useful commands

Unix

Task	How
Get the base address of snapshots	<code>objdump -T libapp.so</code>

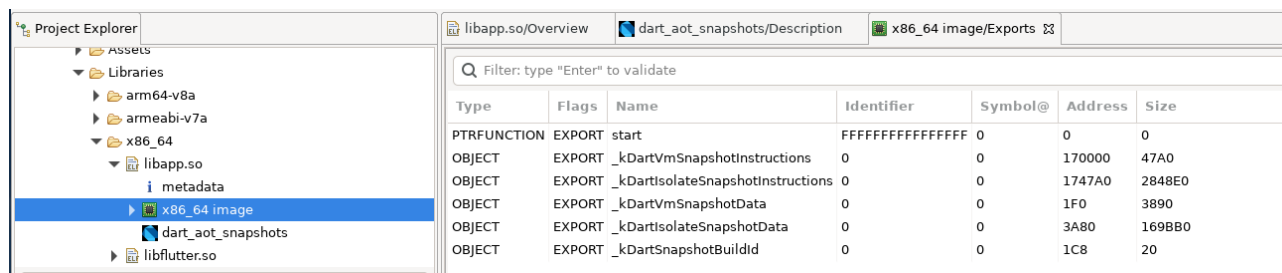
Task	How
List packages from a reFlutter dump	<code>grep -Eo "package:[a-z_A-Z]*/"dump. dart grep -v " flutter/src" sort uniq</code>
Searching for strings constants in a Flutter app or Dart program	<code>strings or bgrep -t hex 'deadbeef' file bgrep or binwalk -R '\xde\ xad\xbe\xef' file</code>

Radare 2

Task	Example
Search for instructions	<code>/ad mov~0xa0</code>
Modify instruction delimiter for search	<code>e asm.cmt.token=#</code>
Search for opcodes	<code>/x 379021e2</code>
Get address of the beginning of a function	<code>afi</code>
Define a function	<code>af</code>
Show only the instructions, no opcode, no arrows	<code>pif</code>

JEB

Task	How
Get Dart version	Go to Dart AOT snapshot > Description
Get the base address of snapshots	Platform image > Exports
Get address of Flutter functions	Search for the function name in "Code Information". Add base relocation address to the virtual address
Search for initialization of a byte array	Search for the instruction that moves double of the byte value. Wild card * is allowed in the search
View opcodes	Edit > Rendering Options > Show bytes count (6)



JEB displays the addresses of snapshots

MoneyMonger Frida hook

```

1  Java.perform(function(){
2      var aes = Java.use("mpdi.v4.b2");
3      // aesDecrypt
4      aes.b8.implementation = function(password, ciphertext) {
5          console.log("b8: password="+password);
6          var decrypted = this.b8(password, ciphertext);
7          console.log("decrypted="+decrypted);
8          return decrypted;
9      }
10     // aesEncrypt
11     aes.dp.implementation = function(password, plaintext) {
12         console.log("dp: password="+password);
13         console.log("dp: plaintext="+plaintext);
14
15         return this.dp(password, plaintext);
16     }
17
18 });

```

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