#### ART OF EMBEDDED SYSTEM DESIGN

Bobby

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In these book, I will summerize all I know about the embedded system, and give some practical electronic designs what I have done in my career. Also, I want to improve the ability of using LATEX by myself. This book is in the public domain. All the copyrights are under GPL 2.0. Thanks all for your attentions! All the materials are from books, internet, etc. I will list as much references as possible, but I am not sure that I can list all. For any improper or defects, your suggurestions are warmly welcomed.

Best Regards!

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### Part I

### Physics

### Solid state physics

In this chapter, will discuss the solid state physics, mainly about the basic fundamentles.

$$a+b+c=b+c+a$$

#### Semiconductor physics

Table 2.1:				
11	1	12	4	6
				-

Table 2.1 indicate that

The universe is immense and it seems to be homogeneous, in a large scale, everywhere we look at.

furnished his toy with a romantic legend about a much larger "Tower of Brahma", which supposedly has 64 disks of pure gold Gold —wow.

Are our disks made of concrete? resting on three diamond needles. At the beginning of time, he said, "God" placed these golden disks on the first needle and ordained that a group of priests should transfer them to the third, according to the rules above. The priests reportedly work day and night at their task. When they finish, the Tower will crumble and the world will end.

resting on three diamond needles. At the beginning of time, he said, "God" placed these golden disks on the first needle and ordained that a group of priests should transfer them to the third, according to the rules above. The priests reportedly work day and night at their task. When they finish, the Tower will crumble and the world will end.

## Part II Circuit basics

## Part III Analog circuit

## $\begin{array}{c} {\rm Part~IV} \\ {\bf Analog~VLSI~design} \end{array}$

#### Current source

$$\frac{d}{dx}\left(int_0^x f(u)\ du\right) = f(x)$$



Figure 3.1:

### Current mirror

Differential amplifier pair

### OP AMP design

# Part V Digital circuit

Finite state machine

## Part VI Analog digital converter

# $egin{array}{c} { m Part\ VII} \\ { m Verilog\ HDL} \end{array}$

# $\begin{array}{c} {\rm Part\ VIII} \\ {\rm \bf Digital\ VLSI} \end{array}$

CPU design

## $\begin{array}{c} {\rm Part~IX} \\ {\rm PCB~design} \end{array}$

#### Part X PCB SI&PI

## $\begin{array}{c} {\rm Part~XI} \\ {\rm C~and~C++~programming} \end{array}$

# Part XII Data structure and algorithms

# Part XIII Computer orgnization

## Part XIV Computer architecture

## Part XV Operating system

#### Part XVI Linux administration

#### Part XVII Linux kernel

## Part XVIII Linux driver development

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Buildroot

# Part XX Computer Network

TCP/IP protocols

Socket programming

# $\begin{array}{c} {\rm Part~XXI} \\ {\bf Signal~and~system} \end{array}$

# Part XXII Ditial signal processing

# Part XXIII Digtal image processing

# Part XXIV Audio signal processing

# Part XXV Modal analysis

# Part XXVI Project Development

Part XXVII

Appendix

#### Chapter 12

#### Basic mathimatics

#### 12.1 Formulas

a+b=b+a

appendix test

Part XXVIII

References

#### Chapter 13

### References

www.baidu.com www.github.com www.google.com Bing