ART OF EMBEDDED SYSTEM DESIGN

Bobby

February 8, 2018

In these book, I will summerize all I know about the embedded system, and give some practical electronic designs what I have done in my career. Also, I want to improve the ability of using LaTeXby myself. This book is in the public domain. All the copyrights are under GPL 2.0. Thanks all for your attentions! All the materials are from books, internet, etc. I will list as much references as possible, but I am not sure that I can list all. For any improper or defects, your suggurestions are warmly welcomed.

Best Regards!

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Part I

Physics

Chapter 1

Solid state physics

In this chapter, will discuss the solid state physics, mainly about the basic fundamentles.

$$a+b+c=b+c+a$$

Chapter 2

Semiconductor physics

Table 2.1:

11	1	12	4	6

Table2.1 indicate that

The universe is immense and it seems to be homogeneous, in a large scale, everywhere we look at.

furnished his toy with a romantic legend about a much larger "Tower of Brahma", which supposedly has 64 disks of pure gold Gold —wow.

Are our disks made of concrete? resting on three diamond needles. At the beginning of time, he said, "God" placed these golden disks on the first needle and ordained that a group of priests should transfer them to the third, according to the rules above. The priests reportedly work day and night at their task. When they finish, the Tower will crumble and the world will end.

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Table2.1 is stupid and in the coming

This is my key2. This is my second palace that has a key.

bibliography
greenwade93 [1]
greenwade93 [2]
carbon nanotube [3]
carbon [4]
transistors [5]

Part II

Circuit basics

Chapter 3

Circuit

A balanced circuit is circuitry for use with a balanced line or the balanced line itself. Balanced lines are a common method of transmitting many types of electrical communication signals between two points on two wires. In a balanced line the two signal lines are of a matched impedance to help ensure that interference induced in the line is common-mode and can be removed at the receiving end by circuitry with good common-mode rejection. To maintain the balance, circuit blocks which interface to the line, or are connected in the line, must also be balanced

Part III

Analog circuit

Part IV

Analog VLSI design

Chapter 4

Current source

$$\frac{d}{dx}\left(int_0^x f(u)\,du\right) = f(x)$$

Figure 4.1:

Chapter 5

Current mirror

Chapter 6

Differential amplifier pair

Chapter 7

OP AMP design

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References

www.baidu.com
   www.github.com
   www.google.com
   Bing

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By default, where appropriate, citations are abbreviated automatically after the first reference when bibliographies are produced by BibT E X. Provision is also made for this feature to be accessed during manual coding

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