

# Development and implementation of an OpenCL driven neural network for use in Python

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## ABSTRACT

This paper describes the development and implementation process of a neural network which is driven by OpenCL using GPU acceleration. The neural network should be usable within Python using a wrapper for the shared object file of the C++ library.

## Categories and Subject Descriptors

H.4 [Computer Science]: Neural Networks

## General Terms

Implementation

## Keywords

Neural Network, OpenCL, Python, C++11, Back-propagation, Stochastic Gradient Descent

## 1. INTRODUCTION

Artificial neural networks (ANNs) are processing devices (algorithms or actual hardware) that are loosely modeled after the neuronal structure of the human cerebral cortex but on much smaller scales. A large ANN might have hundreds or thousands of processor units, whereas a mammalian brain has billions of neurons with a corresponding increase in magnitude of their overall interaction and emergent behavior. Although ANN researchers are generally not concerned with whether their networks accurately resemble biological systems, some have. For example, researchers have accurately simulated the function of the retina and modeled the eye rather well.

Neural networks are typically organized in layers. Layers are made up of a number of interconnected **nodes** which contain an **activation function**. Patterns are presented to the network via the input layer, which communicates to one or more **hidden layers** where the actual processing is done via a system of weighted **connections**. The hidden layers then link to an **output layer**.

Most ANNs contain some form of **learning rule** which modifies the weights of the connections according to the input patterns that it is presented with. In a sense, ANNs learn by example as do their biological counterparts; a child learns to recognize dogs from examples of dogs.

Although there are many different kinds of learning rules used by neural networks, this paper is concerned only with one; the delta rule. The delta rule is often utilized by the most common class of ANNs called **backpropagational neural networks** (BPNNs). Backpropagation is an abbreviation for the backwards propagation of error.

With the delta rule, as with other types of backpropagation, learning is a supervised process that occurs with each cycle or **epoch** (i.e. each time the network has processed the full dataset once) through a forward activation flow of outputs, and the backwards error propagation of weight adjustments. More simply, when a neural network is initially presented with a pattern it makes a random guess as to what it might be. It then sees how far its answer was from the actual one and makes an appropriate adjustment to its connection weights.

## 2. PRELIMINARIES

We use the following notation:

- Matrices are written in uppercase bold e.g. **X**.
- Vectors are written in lowercase bold e.g. **x**.
- scalars are written in lowercase or uppercase. Lowercase indicates that it is a counting variable and uppercase that it is one of the limits in a finite set.
- For all functions, e.g.  $f(x)$ , where  $x = \mathbf{X}$ , we apply the function element wise.
- Dot product is indicated by simple concatenation of two matrices/vectors e.g.  $\mathbf{X}\mathbf{y}$ .
- Element wise multiplication is indicated as  $\mathbf{X} \otimes \mathbf{Z}$ .

Neural networks are a statistical model for regression or classification. Even though naturally they do not produce any classification output, we can model the regressed output to be used as a classifier.

Overall we have two different modes: Training and evaluation. In the training phase we use the back propagation algorithm to train the network and adjust it so that it produces output, which is close to our target output.

Furthermore a neural network contains  $L$  hidden layers, which are called hidden since their output is not directly observable.

The weights are in our simple case randomly initialized and then updated using the back propagation rule.

A basic neural network has therefore the following parameters:

- The weights from an neuron  $k$  in layer  $a$  to a neuron  $j$  in layer  $b$
- The input to the network, usually a vector  $\mathbf{x}$  with  $k$  values
- The target which should be estimated, usually a vector  $\mathbf{t}$  with  $q$  values.
- The learning rate  $\eta$ , which is the indicator how fast the network learns.

Moreover we can add some advanced techniques as the momentum to speed up the training the momentum

## 2.1 Gradient Descent

Gradient descent is a first-order optimization algorithm. To find a local minimum of a function using gradient descent, it takes steps proportional to the negative of the gradient (or of the approximate gradient) of the function at the current point. If instead one takes steps proportional to the positive of the gradient, one approaches a local maximum of that function; the procedure is then known as gradient ascent [2, 3].

Gradient descent is based on the observation that if the multivariable function  $F(\mathbf{x})$  is defined and differentiable in a neighborhood of a point  $\mathbf{a}$ , then  $F(\mathbf{x})$  decreases fastest if one goes from  $\mathbf{a}$  in the direction of the negative gradient of  $F$  at  $\mathbf{a}$ ,  $-\nabla F(\mathbf{a})$  [4]. It follows that, if

$$\mathbf{b} = \mathbf{a} - \gamma \nabla F(\mathbf{a}) \quad (1)$$

for  $\gamma$  small enough, then  $F(\mathbf{a}) \geq F(\mathbf{b})$ . With this observation in mind, one starts with a guess  $\mathbf{x}_0$  for a local minimum of  $F$ , and considers the sequence  $\mathbf{x}_0, \mathbf{x}_1, \mathbf{x}_2, \dots$  such that

$$\mathbf{x}_{n+1} = \mathbf{x}_n - \gamma_n \nabla F(\mathbf{x}_n), \quad n \geq 0. \quad (2)$$

Finally we have [1]:

$$F(\mathbf{x}_0) \geq F(\mathbf{x}_1) \geq F(\mathbf{x}_2) \geq \dots, \quad (3)$$

## 2.2 Minibatch Stochastic Gradient Descent

Both statistical estimation and machine learning consider the problem of minimizing an objective function that has the form of a sum:

$$Q(w) = \sum_{i=1}^n Q_i(w) \quad (4)$$

where the parameter  $w^*$  which minimizes  $Q(w)$  is to be estimated. Each summand function  $Q_i$  is typically associated with the  $i$ -th observation in the data set (used for training).

When used to minimize the above function, a standard (or "batch") gradient descent method would perform the following iterations :

$$w := w - \eta \nabla Q(w) = w - \alpha \sum_{i=1}^n \nabla Q_i(w) \quad (5)$$

where  $\eta$  is the learning rate.

But since in many cases, the update of one gradient would take to estimate the gradient over the whole dataset, the computation would take too much time. Often times we only need a rough estimate of the gradient, not a precise one.

What stochastic gradient descent does is to remove the sum and only uses one sample to estimate the gradient.

$$w := w - \eta \nabla Q(w) = w - \alpha \nabla Q(w) \quad (6)$$

In practice this turns out to be less effective than batch gradient descent, since we only get a very rough estimate of the gradient. Therefore the minibatch gradient descent technique is used. The difference here is that while batch gradient descent uses  $n$  samples and stochastic gradient descent uses only 1, minibatch gradient uses  $b$  training samples, where  $b \ll n$ . Typical sizes for  $b$  are in the range if  $b \in 2, \dots, 100$ .

Therefore in minibatch stochastic Gradient descent we randomly subset the training set and only take some samples out of it and estimate the gradient out of the seen sample set.

The important thing to do is to randomize the training set, since otherwise only a small portion of the training set will be seen and leads to wrong gradients.

## 2.3 The Forward Backward procedure in detail

To train the network we use the standard back-propagation algorithm, which essentially does a forward and a backward pass of the network in a row. Firstly we initialize the weights in a matrix.

$$\mathbf{W}_{j \times k} = \begin{bmatrix} w_{11} & w_{12} & \dots & w_{1k} \\ w_{21} & & \ddots & \vdots \\ \vdots & & \ddots & \vdots \\ w_{j1} & \dots & \dots & w_{jk} \end{bmatrix}, \quad \mathbf{x}_{1 \times k} = \begin{bmatrix} a_1 \\ \vdots \\ \vdots \\ a_k \end{bmatrix} \quad (7)$$

*Forward propagation.* is one of the two passes the network needs to do ( hence it's name ). In forward propagation the network calculates the predicted output of the network.

The output of the hidden layer  $j$  of the network can be calculated as the weighted sum of the inputs

$$\mathbf{nnet}_j = \sum_{k=1}^n w_{kj} x_k = \mathbf{W} \mathbf{x} \quad (8)$$

Even though this method should already work, we add a bias to every node to increase the learning speed and void that the network stops learning.

$$\mathbf{nnet}_j = \sum_{k=1}^n w_{kj}x_k + b_k = \mathbf{W}\mathbf{x} + \mathbf{b} \quad (9)$$

r

Later we see that back-propagation needs some variables, which can be already precomputed during the forward step. These two variables are the output  $\mathbf{o}_j$  of layer  $j$  and the derivative of the output w.r.t to the weighted sum  $\mathbf{nnet}_j$ . We represent the derivatives in a matrix  $\mathbf{D}$  and the output of the current layer

$$\mathbf{D}_j = \frac{\mathbf{o}_j}{\mathbf{nnet}_j} \quad (10)$$

In case of sigmoid activation function, we obtain:

$$\mathbf{D}_j = \varphi(\mathbf{nnet}_j)(1 - \varphi(\mathbf{nnet}_j)) \quad (11)$$

For the outputs, we simply feed forward the network and store the output  $\mathbf{o}_j$  in our buffered lists.

$$\mathbf{o}_j = \varphi(\mathbf{W}\mathbf{x} + \mathbf{b}) \quad (12)$$

As soon as the feed forward is done, we produced an output of the network, which we can denote as  $\mathbf{o}_L$ . Now we need to decide how close our output is to the targetted output  $\mathbf{t}$ . For this we use a cost function in usual cases it is sufficient to use  $MSE$  as such, which is defined as:

$$MSE = \frac{1}{2} (\mathbf{o}_L - \mathbf{t})^2 \quad (13)$$

Here (13) we need to make sure that the last output layer has the same dimensions as the target output.

**Backpropagation.** starts when all layers are trained. The idea behind back propagation is that the hidden layers do not produce any output. So we cannot modify their weights directly, since we don't know how large the error was. With back propagation we calculate the error at the output layer  $L$  and then propagate this error to the hidden layers back. Therefore we update all  $L - 1$  weight matrices and biases.

At first we calculate the differences between the target output and the estimated output.

$$\mathbf{err} = (\mathbf{o}_L - \mathbf{t}) \quad (14)$$

$$\delta_L = \mathbf{err} \otimes \mathbf{D}_L \quad (15)$$

$$\nabla \mathbf{W}_L = \mathbf{o}_L \delta_L^T \quad (16)$$

We store both, the deltas and the nablas to later update the weights (with the  $\nabla$ s) and the biases (using the  $\delta$ ). From

here on we then calculate the other layers backwards, we do:

$$\delta_i = \mathbf{D}_i \otimes (\mathbf{W}_{i+1}^T \delta_{i+1}) \quad (17)$$

$$\nabla \mathbf{W}_i = \mathbf{o}_i \delta_i^T \quad (18)$$

Whereas we again store the deltas and the nablas to later update the weights.

To update the weights, we use the usual gradient descent update rule:

$$\mathbf{W}^* = \mathbf{W} - \alpha \nabla \mathbf{W}_i^T \quad (19)$$

$$(20)$$

To update the biases, we use essentially the same update rule, but only consider the given  $\delta_i$ s.

$$\mathbf{b}^* = \mathbf{b} - \alpha \delta_i^T \quad (21)$$

Finally, if we would like to improve the converging speed we can apply momentum. Momentum adds extra "velocity" towards the gradient curve, by using the last estimated value of the gradient  $\mathbf{W}_{i-1}^*$  ( $i$  denotes the current iteration) and applying on that the momentum ( $\alpha$ ) as:

$$\mathbf{W}_{i+1} = \mathbf{W}_i - \eta \nabla \mathbf{W}_i + \alpha \mathbf{W}_i \quad (22)$$

The momentum is initialized with 0 and takes effect after one iteration of gradient descent.

### 3. IMPLEMENTATION DETAILS

OpenCL offers multiple implementation types. It is natively written in C, but has also a C++ wrapper onboard. We used in our project the C++ wrapper since it is more naturally to use that in a C++ project. First one needs to include the necessary header into any class.

```
1 #include <CL/cl.hpp>
```

Listing 1: OpenCL C++ header

*The interface to OpenCL.* was written in C++11 and makes heavily use of the current variadic args feature. For this small scale task, the interface is definitely too complex, but it can be used for any other OpenCL task.

```
1 __kernel void mult(const int wSrc, ←
   __global const float* A, __global ←
   const float* B, __global float* output←
   )
2 {
3     const int idx = get_global_id(0);
4     const int idy = get_global_id(1);
5
6     output[idy*wSrc+idx] = A[idy*wSrc+idx]←
       * B[idy*wSrc+idx];
7 }
```

Listing 2: Example Kernel function

OpenCL uses externally defined functions ( coined as kernels ) to compile the code and run it, during the runtime. Therefore one needs to write a kernel for its needs. An example kernel can be seen at 3. In our case we defined various kernels, for dot products and other matrix operations.

Since OpenCL is a multi device and platform GPU/CPU interface, one needs to first figure out which kind of platform (e.g. NVIDIA,AMD,Intel) the current machine is using and then decide which accelerator will be used 3.

```

1 void exampleplatform(const char * ←
  programpath){
2  std::vector<cl::Platform> all_platforms;
3  cl::Platform::get(&all_platforms);
4  //Assume having only one platform
5  cl::Platform defaultplatform = ←
    all_platforms[0];
6  std::vector<cl::Device> all_gpu_devices;
7  //CL_DEVICE_TYPE_GPU can also be ←
    CL_DEVICE_TYPE_CPU for CPU
8  defaultplatform.getDevices(←
    CL_DEVICE_TYPE_GPU, &all_gpu_devices)←
    ;
9  //Assume having only one device / take ←
    the first
10 cl::Device device = all_gpu_devices.front←
    ();
11 //Init the current context with the ←
    device
12 cl::Context(device);
13 //We wrote a helper function here to get ←
    the string data out of the kernel ←
    file
14 const char* content = util::file_contents←
    (programpath);
15 }

```

Listing 3: Get Platforms and devices

Moreover since OpenCL is compiled during runtime, one needs to extract the code from the kernel file (e.g. kernel.cl) and give this content to the OpenCL.

As soon as the context is initialized we can run our kernels on the device. The problem here is that the kernel is defined with it's own parameters and types, but we dont know these during the compile time. To have a universal interface we used the variadicargs feature to allow the programmer a very straight forward way to use any kernel function.

```

1 void runKernel(const char *kernelname){
2  cl::Program::Sources sources;
3  //contents is the already read out ←
    content of the kernels file (e.g. ←
    kernels.cl)
4  sources.push_back(std::make_pair(contents←
    ,strlen(contents)+1));
5  //Init the program with the context and ←
    the source
6  cl::Program program(context,sources);
7  //Build the program on the device
8  program.build({device});
9  cl::CommandQueue queue(context,device);
10 //The operator allows us to set args to ←
    the kernel

```

```

11 cl::Kernel kernel_operator(program, ←
    kernelname);
12 //kernel_operator allows us to send ←
    arguments to the kernel by calling
13 //kernel_operator.setArgs(ARGNUMBER, ←
    ARGUMENT);
14
15 //Init space on the device using cl::←
    Buffers
16 //Send Arguments to the device ....
17 // quene.enqueueWriteBuffer() .....
18 //Wait until the arguments did arrive on ←
    the device
19 queue.finish();
20
21 //Execute!
22 cl::Event event;
23 queue.enqueueNDRangeKernel(←
    kernel_operator,cl::NullRange,cl::←
    NDRange(10),cl::NullRange,NULL,&event←
    );
24 //Wait until the execution has finished
25 event.wait();
26 // Read out the results by calling
27 // quene.enqueueReadBuffer()
28 }

```

Listing 4: Kernel usage

The usual Kernel execution can be seen in 3. Even though it is recommended to use OpenCL in this fashion, we cannot cope with different arguments for the kernel. Therefore we would need to init a device and context every time ( or at least as a singleton ) and then rewrite the argument passing for every parameter independently. This would take a lot of time, if the GPU is used extensively.

Our solution to that problem is as follows:

```

1 class OpenCL{
2
3  //Constructor ..... etc
4  // Hook for the iteration
5  template<std::size_t P=0,typename... Tp>
6  typename std::enable_if<P == sizeof←
    ... (Tp), void>::type addkernelargs←
    (std::tuple<Tp...> && t,cl::Kernel←
    &k,cl::CommandQueue &,std::vector←
    <cl::Buffer> &outputbuffers) const←
    {
7      // Do nothing
8  }
9
10 // Start of the iteration
11 template<std::size_t P = 0, typename... ←
    Tp>
12 typename std::enable_if< P < sizeof←
    ... (Tp), void>::type addkernelargs←
    (std::tuple<Tp...> && t,cl::Kernel←
    &kernel,cl::CommandQueue &,std::←
    vector<cl::Buffer> &outputbuffers)←
    const{
13     // Type
14     typedef typename std::←
    tuple_element<P, std::tuple<←
    Tp...>>::type type;
15 }

```

```

16 // Add the value of the current ←
    item from std::get<P> to the ←
    args in kernel
17 // This function decides which ←
    type the kernel arg is
18 addkernelarg(P, std::get<P>(t), ←
    kernel, queue, outputbuffers);
19
20 // Recurse to get the remaining ←
    args
21 addkernelargs<P + 1, Tp...>(std::←
    forward<std::tuple<Tp...>>(t)←
    , kernel, queue, outputbuffers)←
    ;
22 }
23
24 // Adding Std::vector as type to the ←
    kernel args list
25 template<typename T>
26 void addkernelarg(std::size_t i, std::←
    vector<T> const & arg, cl::Kernel ←
    & kernel, cl::CommandQueue &) const←
    {
27     cl::Buffer buffer(this->context, ←
    CL_MEM_READ_WRITE, arg.size()*←
    sizeof(T));
28     queue.enqueueWriteBuffer(buffer, ←
    CL_FALSE, 0, sizeof(T)*arg.size()←
    , &(arg[0]));
29     kernel.setArg(i, buffer);
30 }
31
32
33
34 // Adding any array into the kernel
    args
35 template<typename T, std::size_t N>
36 void addkernelarg(std::size_t i, T ←
    const (& arg)[N], cl::Kernel & ←
    kernel, cl::CommandQueue &) const {
37     cl::Buffer buffer(this->context, ←
    CL_MEM_READ_WRITE, N*sizeof(T));
38     queue.enqueueWriteBuffer(buffer, ←
    CL_FALSE, 0, sizeof(T)*N, &arg);
39     kernel.setArg(i, buffer);
40 }
41
42 // Adding any constant to the kernel
    args
43 template<typename T>
44 void addkernelarg(std::size_t i, T ←
    const & arg, cl::Kernel & kernel, ←
    cl::CommandQueue &) const {
45     cl::Buffer buffer(this->context, ←
    CL_MEM_READ_WRITE, arg.size()*←
    sizeof(T));
46     queue.enqueueWriteBuffer(buffer, ←
    CL_FALSE, 0, sizeof(T)*arg.size()←
    , &(arg[0]));
47     kernel.setArg(i, buffer);
48 }
49
50 }

```

Listing 5: Add arguments to kernel dynamic way

### 3.1 Transparently handling OpenCL within a Matrix class

To be able to use high level C++ code to implement the feedforward and backpropagation algorithm, we introduced a CL\_Matrix class, which provides common matrix operation

like multiplication, addition and so on. Most matrix operations are implemented as OpenCL kernels on the GPU. To avoid unnecessary transfers between the RAM and the GPU memory, the matrix class implements a state handling. Two methods for this are shown below, syncToRam() and moveToRam() work in a similar way.

```

1 template<typename T>
2 inline void CL_Matrix<T>::syncToGpu() ←
    const {
3     if (this->state == OnlyRam) {
4         this->gpu_buf = this->_cl.←
            putDataOnGPU(this->mat);
5         this->state = Synced;
6     }
7 }
8
9 template<typename T>
10 inline void CL_Matrix<T>::moveToGpu() {
11     this->syncToGpu();
12     this->state = OnlyGpu;
13 }
14
15 }

```

Listing 6: State handling to allow the Matrix data to be both used in RAM and in the GPU memory

This way, we can both perform OpenCL operations and access the data from the normal C++ code, for example to get the result. Data transfers are avoided if possible, so for example multiple consecutive OpenCL operations can be performed without transferring data.

This recipe can be used to do the same actions for the reading buffers out after the transfer has finished. Therefore we can create a highly dynamic wrapper class for OpenCL.

### 3.2 Use a C++ Class in Python

To use a C++ class in a Python program, Python's ctypes library can be used to invoke functions on a shared object file. The class will be wrapped into a Python class which uses the ctypes library to invoke functions on the C++ object. As the ctypes library is not able to handle C++ objects directly, a wrapper function must be written in C for every function of the C++ class. These plain C functions can then be invoked by ctypes.

A simple example of such a C++ class is seen in the following figure:

```

1 class WrapperExample {
2     std::string* name;
3
4 public:
5     WrapperExample(std::string name) {
6         this->name = new std::string(name)←
            );
7     }
8     WrapperExample() {
9         delete this->name;
10    }
11    void sayHello() {
12        std::cout << "Hello " << *(this->←
            name) << "!" << std::endl;

```

```

13     }
14 };
15
16 extern "C" {
17     WrapperExample* WrapperExample_new(↵
18         char* name) {
19         return new WrapperExample(name);
20     }
21     void WrapperExample_sayHello(↵
22         WrapperExample* e) {
23         e->sayHello();
24     }
25     void WrapperExample_delete(↵
26         WrapperExample* e) {
27         delete e;
28     }
29 }

```

**Listing 7: Example C++ class for wrapping a C++ object in a Python object (Example.cpp)**

To compile the given C++ code into a shared object file which can be used by Python's ctypes library, following compilation statements can be used:

```

1 g++ -std=c++11 -c -fPIC Example.cpp -o ↵
2   example.o
3 g++ -std=c++11 -shared -Wl,-soname,↵
4   example.so -o example.so example.o

```

**Listing 8: Compilation of the C++ class into a shared object file**

The Python wrapper will call the functions defined in the `extern` section. For every function in the `extern` section, the Python class will have one function to wrap it. The function `WrapperExample_new()` will be called in the constructor of the Python class and `WrapperExample_delete()` in its destructor. All functions besides the `WrapperExample_new()` function receive a pointer to an instance of the C++ class `WrapperExample` on which the function should be invoked. These objects are created in `WrapperExample_new()` and the pointers to them are stored by Python.

The corresponding Python class to wrap the C++ class shown above looks like this:

```

1 from ctypes import cdll
2
3 lib = cdll.LoadLibrary('example.so')
4
5 class ExampleWrapper:
6     def __init__(self, name):
7         self.obj = lib.WrapperExample_new(↵
8             name)
9
10    def sayHello(self):
11        lib.WrapperExample_sayHello(self.↵
12            obj)
13
14    def __del__(self):
15        lib.WrapperExample_delete(self.↵
16            obj)

```

**Listing 9: Python class wrapping the C++ class shown above (Example.py)**

The Python class `ExampleWrapper` can now be used as any other Python class.

### 3.3 Use a C++ Class in Python

To use a C++ class in a Python program, Python's ctypes library can be used to invoke functions on a shared object file. The class will be wrapped into a Python class which uses the ctypes library to invoke functions on the C++ object. As the ctypes library is not able to handle C++ objects directly, a wrapper function must be written in C for every function of the C++ class. These plain C functions can then be invoked by ctypes.

A simple example of such a C++ class is seen in the following figure:

```

1 class WrapperExample {
2     std::string* name;
3
4 public:
5     WrapperExample(std::string name) {
6         this->name = new std::string(name↵
7             );
8     }
9     WrapperExample() {
10        delete this->name;
11    }
12    void sayHello() {
13        std::cout << "Hello " << *(this->↵
14            name) << "!" << std::endl;
15    }
16 };
17
18 extern "C" {
19     WrapperExample* WrapperExample_new(↵
20         char* name) {
21         return new WrapperExample(name);
22     }
23     void WrapperExample_sayHello(↵
24         WrapperExample* e) {
25         e->sayHello();
26     }
27     void WrapperExample_delete(↵
28         WrapperExample* e) {
29         delete e;
30     }
31 }

```

**Listing 10: Example C++ class for wrapping a C++ object in a Python object (Example.cpp)**

To compile the given C++ code into a shared object file which can be used by Python's ctypes library, following compilation statements can be used:

```

1 g++ -std=c++11 -c -fPIC Example.cpp -o ↵
2   example.o
3 g++ -std=c++11 -shared -Wl,-soname,↵
4   example.so -o example.so example.o

```



---

### Listing 11: Compilation of the C++ class into a shared object file

The Python wrapper will call the functions defined in the `extern` section. For every function in the `extern` section, the Python class will have one function to wrap it. The function `WrapperExample_new()` will be called in the constructor of the Python class and `WrapperExample_delete()` in its destructor. All functions besides the `WrapperExample_new()` function receive a pointer to an instance of the C++ class `WrapperExample` on which the function should be invoked. These objects are created in `WrapperExample_new()` and the pointers to them are stored by Python.

The corresponding Python class to wrap the C++ class shown above looks like this:

```
1 from ctypes import cdll
2
3 lib = cdll.LoadLibrary('example.so')
4
5 class ExampleWrapper:
6     def __init__(self, name):
7         self.obj = lib.WrapperExample_new(
8             (name)
9
10        def sayHello(self):
11            lib.WrapperExample_sayHello(self.obj)
12
13        def __del__(self):
14            lib.WrapperExample_delete(self.obj)
```

### Listing 12: Python class wrapping the C++ class shown above (Example.py)

The Python class `ExampleWrapper` can now be used as any other Python class.

## 3.4 Using the Interface

The Python class wrapping our `NeuralNetwork` class can be found in `NeuralNetwork.py`. To create a new `NeuralNetwork` the following constructor can be used:

```
1 nn = NeuralNetwork(layerCount=3,
2                     layerSize=np.array(
3                     ([784, 1000, 10]),
4                     actFunctions=np.array(
5                     ([1, 1]),)
```

### Listing 13: Constructor to create a neural network

In this case a neural network with 3 layers is created. The list `layerSize` contains the size of every layer. The neural network created in this example has an input layer with 784 nodes, one hidden layer with 1000 nodes and a output layer with 10 nodes. The activation functions are defined by the list `actFunctions`. The value 0 will set the activation function `TAN_H` and the value 1 `SIGMOID`. As the input layer

has no activation function, the size of the list `actFunctions` is only 2. The sizes of the input and output layers can later be queried by the functions `getInputSize()` and `getOutputSize()`.

To train the neural network, the functions `trainBatch()` and `trainStochastic()` can be used. Both functions expect two matrices as parameters. The first matrix defines the input values which will be given to the neural network and the second parameter defines the expected output values. One row in one in the matrices is one train case for the neural network. The functions return a matrix with the errors. `trainStochastic()` uses stochastic gradient descent. The learning rate and momentum can be defined by the corresponding parameters `learningRate` and `momentum` and the number of epochs can be defined. An example usage can be seen in the following listing:

```
1 errors = nn.train(trainImages[:, 0:20], ←
2                 trainOutput[:, 0:20], learningRate←
3                 =0.2, momentum=0.1, numEpochs=10000)
4 print errors
5 errors = nn.trainsgd(trainImages[:, ←
6                     0:20], trainOutput[:, 0:20], ←
7                     learningRate=0.2, momentum=0.1, ←
8                     numEpochs=10000)
9 print errors
```

### Listing 14: Usage of `trainsgd()` and `train()`

The function `test()` can be used to test a input value with the neural network. In difference to `train()` only one matrix is expected as parameter. The values from the matrix will be given to the neural network. One column of the matrix is one test case. The function will return a matrix with the value of the output nodes for every test case.

```
1 result = nn.test(testImages[:, 0:20])
2 print result
```

### Listing 15: Usage of `test()`

The current state of the neural network including the weight biases and the configuration can be dumped into a file using the `save()` function. The path to the file must be supplied as a parameter. To restore the neural network later, following constructor can be used:

```
1 nn.save("path/to/savefile.bin")
2
3 nn2 = NeuralNetwork(saveFile="path/to/←
4                     savefile.bin");
```

### Listing 16: Constructor to load a dump file

## 4. EXPERIMENTS

In our experiments we used the MNIST database for digit recognition.

MNIST consists of 60000 data samples of recognized written

digits, stored as greyscale images. Every one of these samples has a size of  $28 \times 28$  pixels. The digits which can be recognized range from zero to nine.

Therefore we have an input layer size of  $28 \times 28 = 784$  and 10 output nodes representing each number respectively.

**Table 1: Network configuration**

configname	layers	neurons/layer	l-rate	momentum
small	1	1024	0.1 to 0.7	0.01
middle	2	1024	0.1 to 0.7	0.01
big	3	1024	0.1 to 0.7	0.01

We used the networks shown in 1, where we tried to use different starting learning rates to achieve better results. All out networks use **10000 epochs** and **stochastic gradient descent** as its learning algorithm.

The results can be seen in the table 2.

**Table 2: Correct results ( in %) depending on the learning rate**

	0.1	0.2	0.3	0.4	0.5	0.6	0.7
small	89	92	<b>96</b>	94	90	89	89
mid	91	85	86	81	86	85	92
big	84	87	77	56	66	11	64

Finally we can see the performance of our neural network. We want to emphasize that we used Intel HD4000 GPU's, which are not very powerful GPU's. Fortunately one could easily run this code with any other GPU, since OpenCL supports every large GPU manufacture product.

**Table 3: Training time ( in sec )**

	time
small	287
mid	530
big	700

## 5. CONCLUSION

As we can see in 2, the results seem to be somewhat inconsistent. This can be explained by knowing that often times a larger neural network abstracts a (maybe simple) correlation between the inputs so much that it decorrelates the input with the output. In our example we can see that digit recognitions work well if only a few layers are used. Moreover, random weights can lead the network to converge to a local minimum which is not necessary the global one.

Many other techniques to increase the performance could be implemented such as Restricted Boltzmann machines (RBMs) to cope with the random initialization of the network.

In our experiments we only used the checker-board algorithm to calculate the dot products, but there are more efficient ways to do so. Moreover loop unrolling is a powerful method to calculate multiple instructions at once, as long as the dataset has a certain size to enable the unroll.

## 6. REFERENCES

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