CS/ECE552 Spring 2019 Project Report

Team 3: Jeremy Intan, Chetankumar Mistry, Nicholas Sielicki $\label{eq:may} \text{May 3, 2019}$

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1 Design Overview

We implemented a modest MIPS-like processor with

- 2 Optimizations and Discussions
- 3 Design Analysis
- 4 Conclusions and Final Thoughts