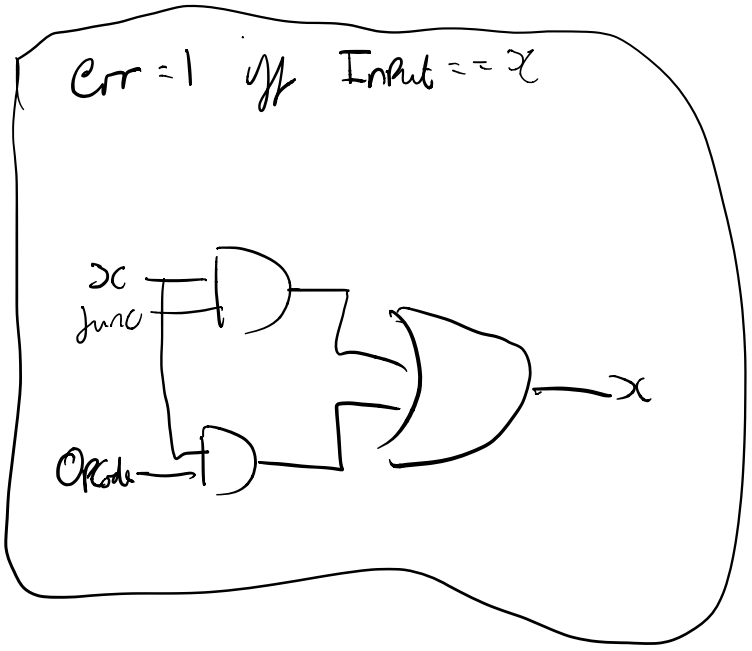
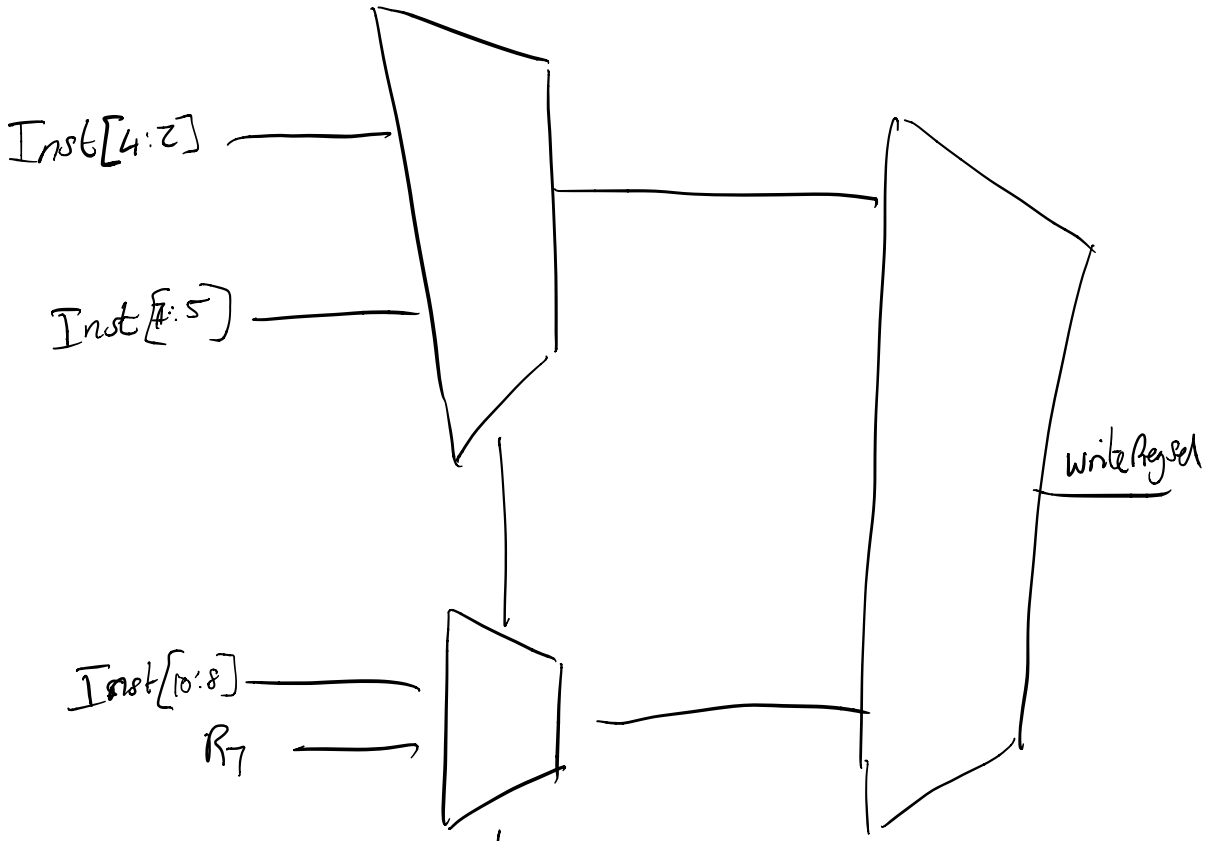


Func [1:0]

OPCode [4:0]



RegDst[0]

RegDst[1]

SESel[2:0]

RegWrite 1 if adds to register 0 otherwise

0: HALT 00000

NOP 00001

BNEZ 01100

BEQZ 01101

BLTZ 01110

BGEZ 01111

J 00100

JR 00101

1: 01000

01001

01010

01011

10100
10101
10110
10111

10001

11001
11011

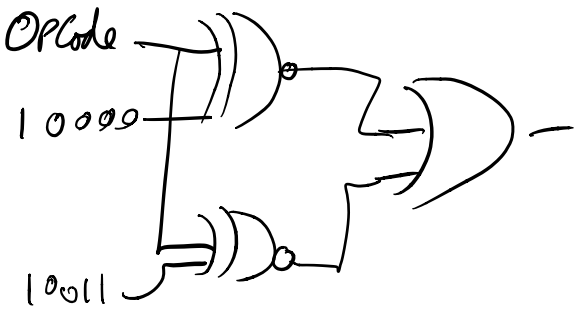
11010
11100
11101
11110

11111

DMemWrite

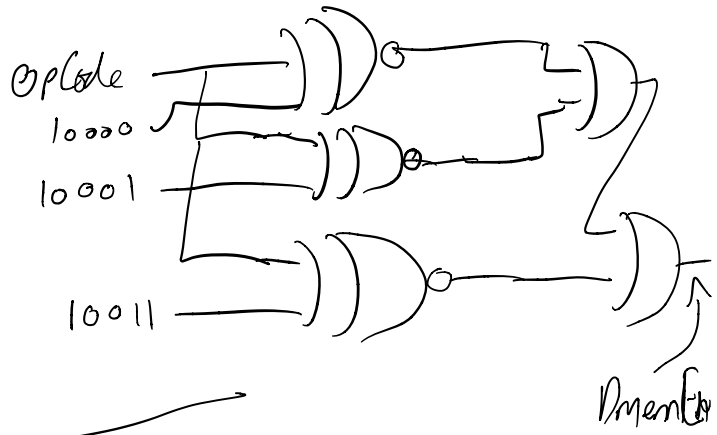
1: 10000
10011

0: everything else



DMemEn

1: 10000
10001
10011



ALUSrc2

1: 11011
11010
11100
11101
11110
11111

PCSrc

1: 01100
01101
01110
01111

MemToReg

1: 10001

DmemDump

1: 00000

PCImm

1: 00100

60101

Jump 1: 00110

60111

RegDst:

01: 01001
01010
01011
10101
10110
10111
10000
10001
10011

00: 11001
11011
11010
11100
11101
11110
11111

C

11 : 00110

00111

10 : 11000

10010

SESel

000 : 01010

01011

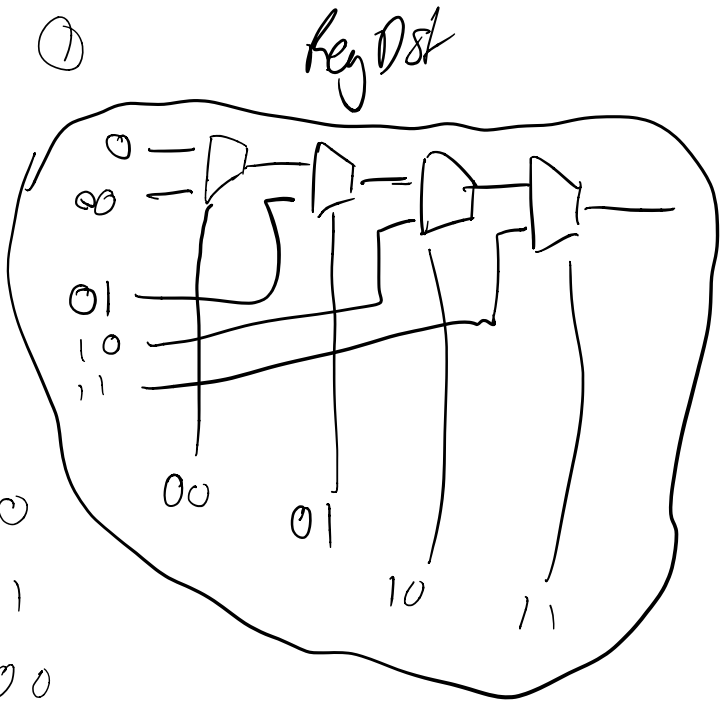
10100

10101

10110

10111

001 : 10010



01X: 01000

01001

10000

10001

10X: 01100

01101

01110

01111

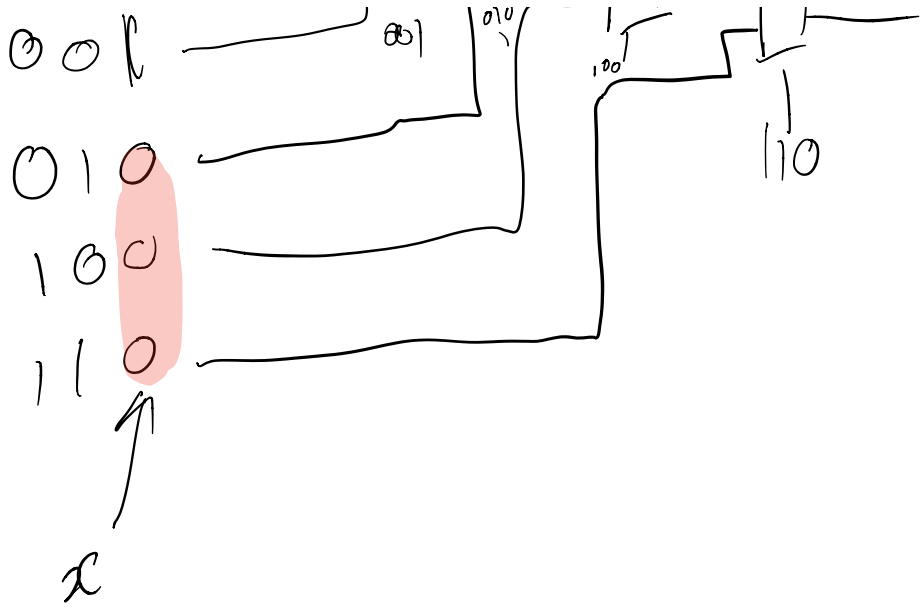
11000

10101

00111

11X: 00100

00110



5_1 xNOR